



Key Parameters

V_{RRM}	= 8500V
$I_{F(AV)}$	= 491A
I_{FSM}	= 8500A
$V_{F(TO)}$	= 0.95V
r_F	= 1.25mΩ

Features

- Full blocking capability over wide temperature range
- Hermetically sealed ceramic package
- High case non-rupture current

Applications

- Traction Rectifiers
- Uncontrolled Rectifiers
- Induction Heating / Melting

Ordering Information

MS PERI	D	491	CZ	XX
Fixed Code	Rectifier Diode	Current code	CZ - Capsule package with Free floating silicon Technology	Voltage Code Code X 100 = V_{RRM}
Order Code MS PERI D491CZ85 : 8500V V_{RRM} , Capsule Diode				

Prepared by : ABA	Date of Publication : 28.08.2018
Approved by :	Revision : 0

Symbol	Characteristic	Conditions	T _j [°C]	Value	Unit
BLOCKING					
V _{RRM}	Repetitive peak reverse voltage		160	8500	V
V _{RSM}	Non-repetitive peak reverse voltage		160	8600	V
I _{RRM}	Repetitive peak reverse current	V = V _{RRM}	160	50	mA
CONDUCTING					
I _{F(AV)}	Mean forward current	180° sin, 50 Hz, T _c =100°C, double side cooled		491	A
I _{FRMS}	RMS current			770	A
I _{FSM1}	Surge forward current	Sine wave, 10 ms Without reverse voltage	160	8500	A
I ² t	I ² t		160	361 x 10 ³	A ² s
V _F	Forward voltage	On-state current = 1200A	160	2.45	V
V _{F(TO)}	Threshold voltage		160	0.95	V
r _F	Forward slope resistance		160	1.25	mΩ
SWITCHING					
Q _{rr}	Reverse recovery charge	I _{FM} =1000A, -di _F /dt = 10A/μs, V _R = 100V, t _p =2000μs	160	6200	μC
THERMAL					
R _{th(j-c)}	Thermal impedance, sin 180°	Junction to case, double side cooled		0.045	°C/W
R _{th(c-h)}	Thermal impedance	Case to heatsink, double side cooled		0.0075	°C/W
T _j	Max. junction temperature			160	°C
T _{stg}	Storage temperature			-40 160	°C
MECHANICAL					
M	Clamping force			8 - 12	KN
W	Weight (Approx.)			350	gm
D _s	Surface creepage distance			38	mm
D _a	Air strike distance			21	mm
			Prepared by : ABA	Date of Publication : 28.08.2018	
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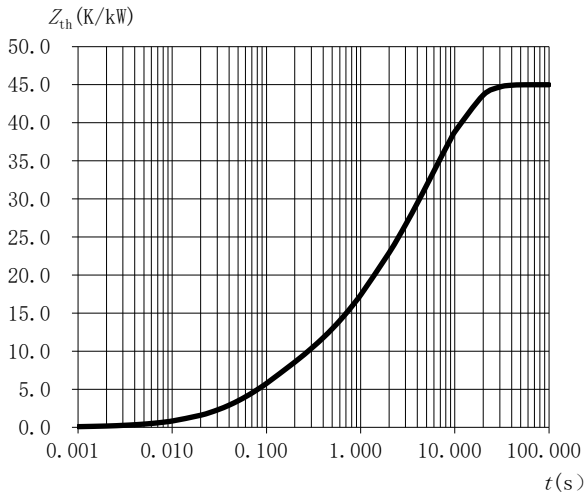


Fig.1 : Transient thermal impedance (junction-to-case) vs. time

$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i(1 - e^{-t/\tau_i})$$

<i>i</i>	1	2	3	4
$R_i(K/kW)$	27.9473	7.26312	5.52637	4.26312
$\tau_i(s)$	6.61575	1.21337	0.08287	0.42312

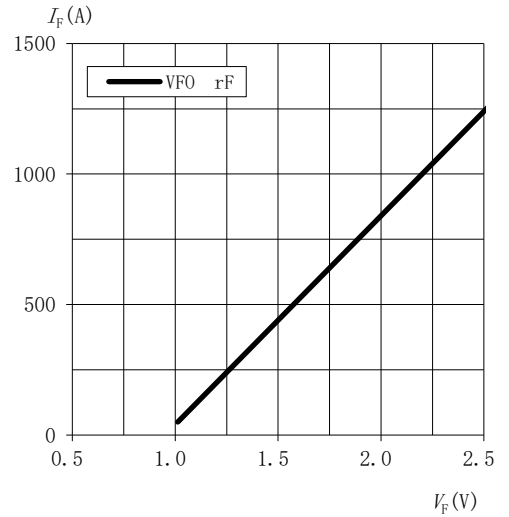


Fig.2 : On-state V-I characteristics

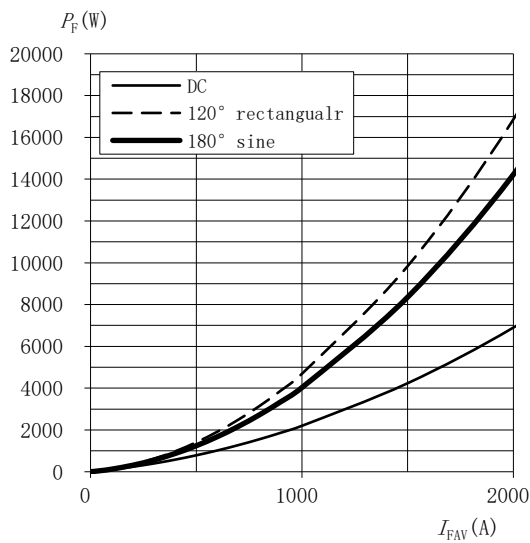


Fig.3 : On-state power loss (P_F) vs. average on-state Current (I_{FAV})

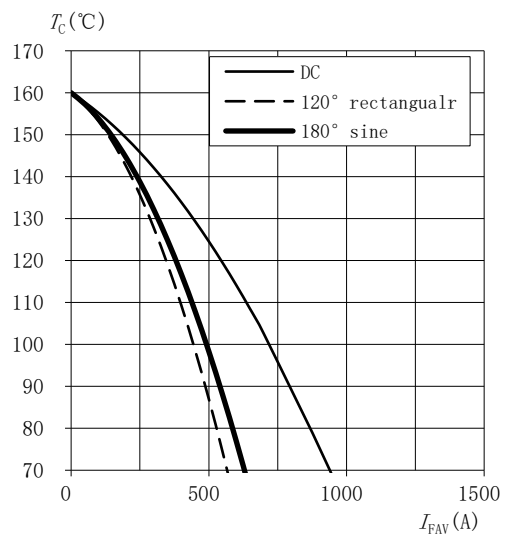


Fig.4 : Max. permissible case temperature(T_c) vs. average on-state current(I_{FAV})

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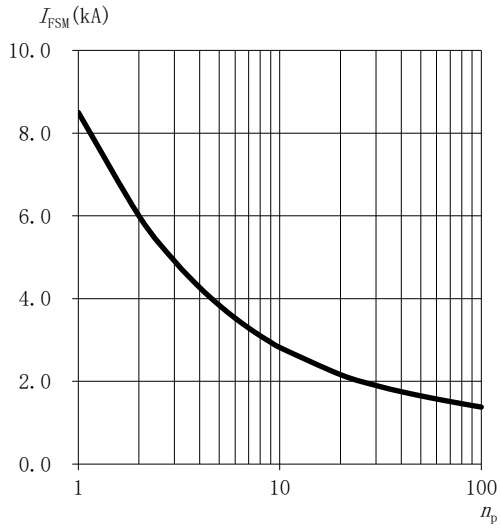


Fig.5: Surge on-state current (I_{FSM}) vs. number of pulses (n_p)

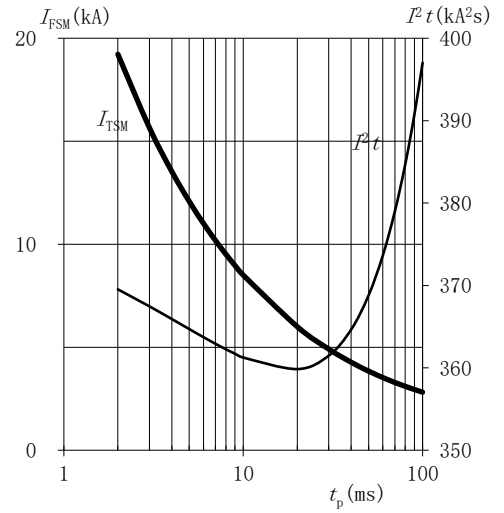
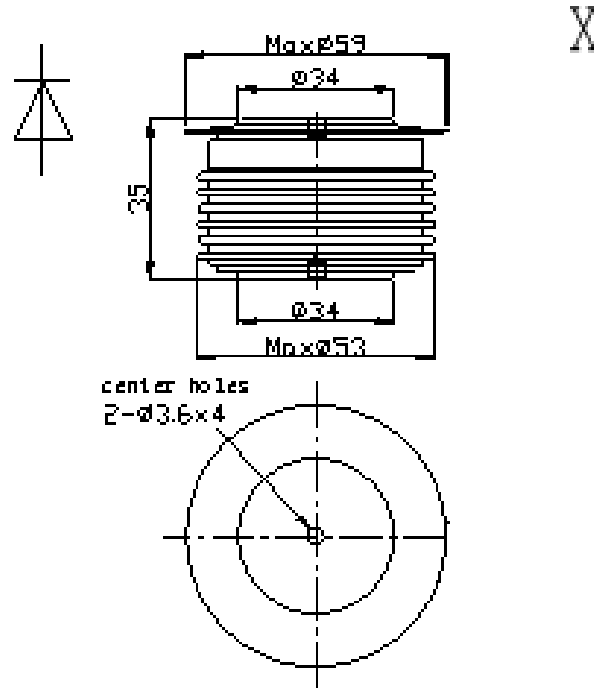


Fig.6: Surge on-state current (I_{FSM}) and surge current Integral (I^2t) vs. pulse length (t_p)

Prepared by : ABA	Date of Publication : 28.08.2018
Approved by :	Revision : 0

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Prepared by : ABA	Date of Publication : 28.08.2018
Approved by :	Revision : 0