



Key Parameters

V_{DRM} / V_{RRM}	= 1800V
$I_{T(AV)}$	= 1194A
I_{TSM}	= 18.5kA
$V_{T(TO)}$	= 0.85V
r_T	= 0.32mΩ

Features

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator

Applications

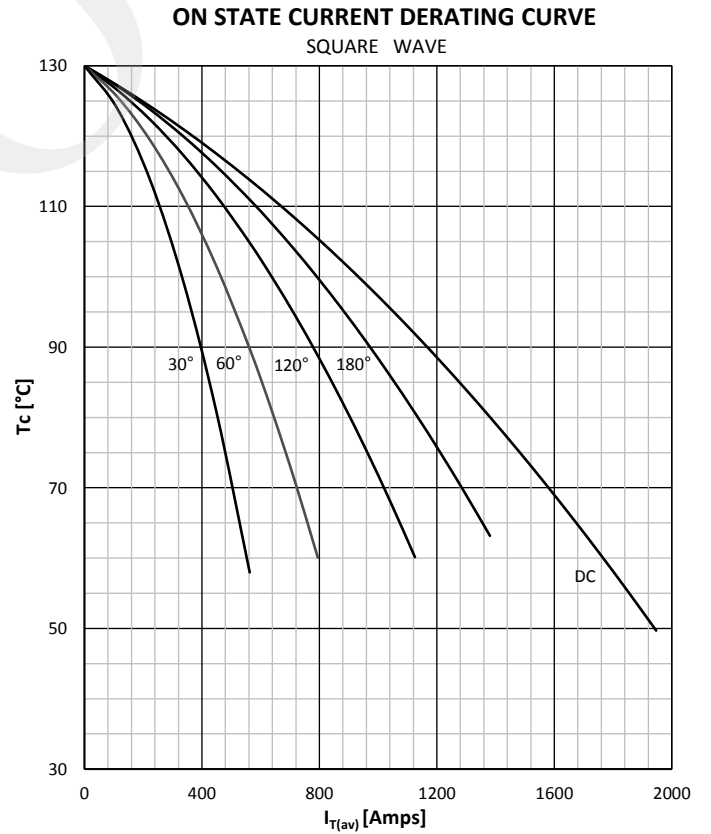
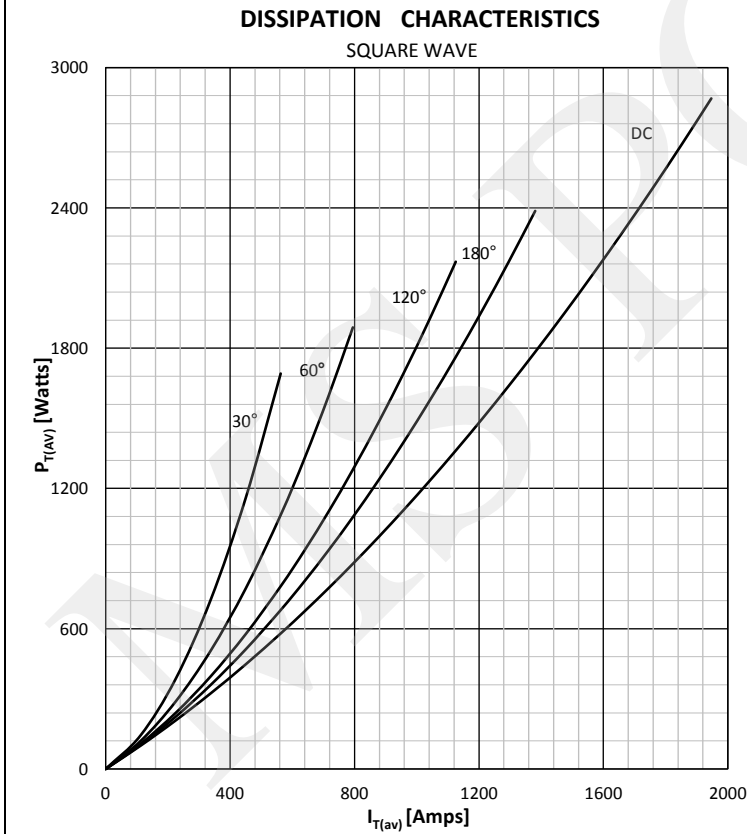
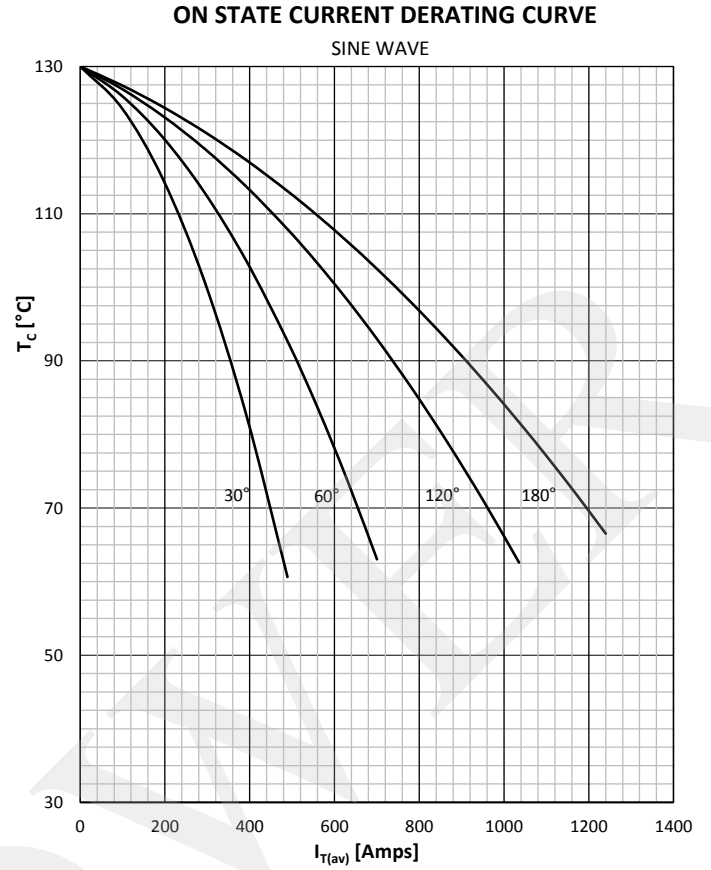
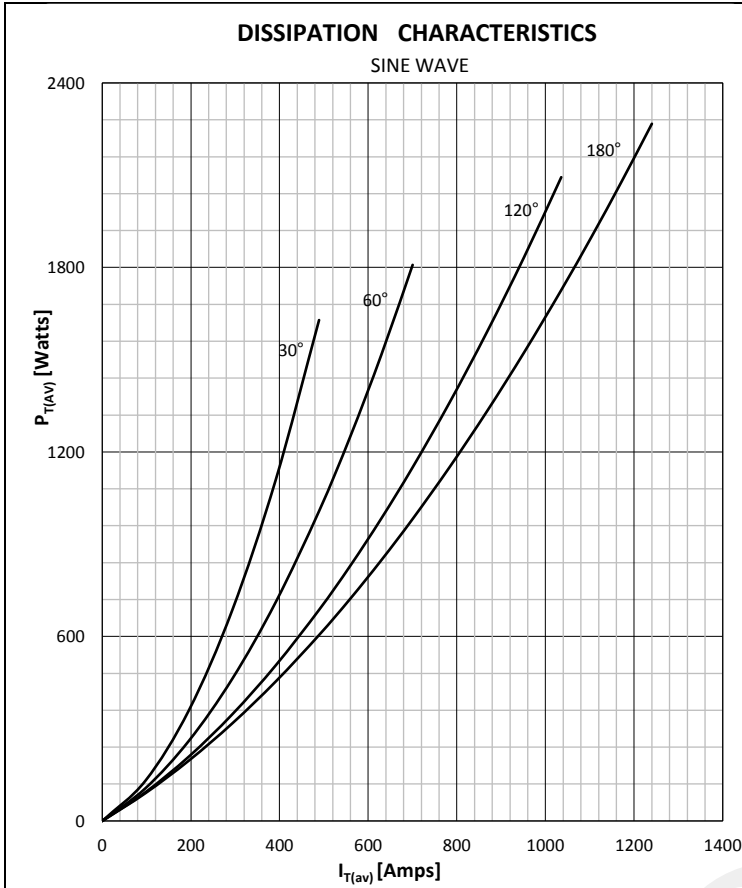
- Battery Chargers
- Medical Equipment
- UPS
- Power Supplies
- Motor control
- Controlled Rectifiers
- Transportation
- Induction Heating
- Welding

Ordering Information

MS T	1194	C	XX
Phase Control Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V_{DRM}/V_{RRM}
Order Code MS T1194C18 : 1800V V_{DRM}, V_{RRM} , 26mm clamp height capsule thyristor			

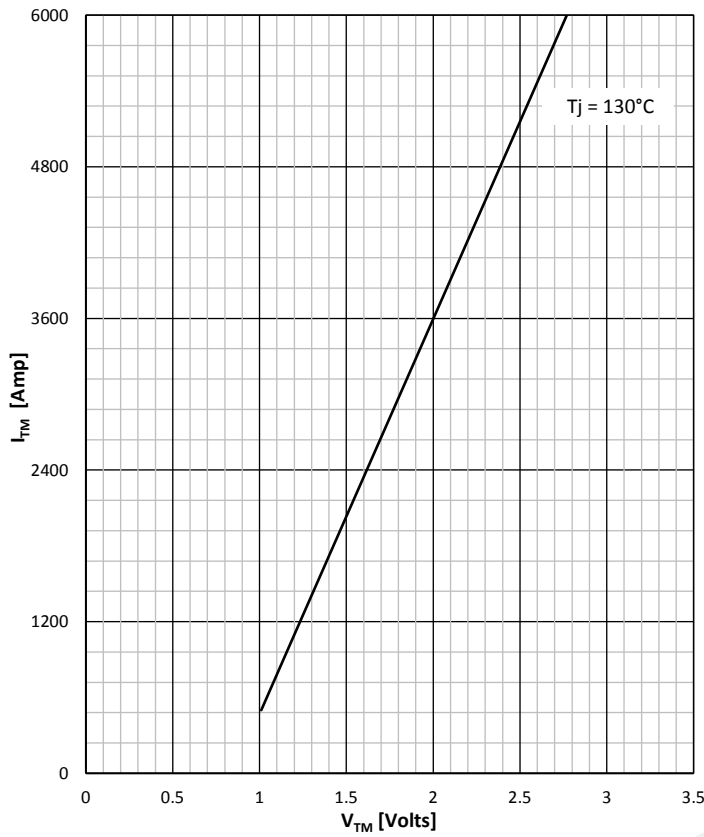
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Symbol	Characteristic	Conditions	T _j [°C]	Value	Unit
BLOCKING					
V _{RRM}	Repetitive peak reverse voltage		130	200 - 1800	V
V _{RSM}	Non-repetitive peak reverse voltage		130	300 - 1900	V
V _{DRM}	Repetitive peak off-state voltage		130	200 - 1800	V
I _{RRM}	Repetitive peak reverse current	V = V _{RRM}	130	100	mA
I _{DRM}	Repetitive peak off-state current	V = V _{DRM}	130	100	mA
CONDUCTING					
I _{T(AV)}	Mean on state current	180° sin ,50 Hz, T _c =70°C, Double side cooled 180° sin ,50 Hz, T _c =66°C, Double side cooled		1194 1240	A
I _{RMS}	RMS on-state current	T _c =66°C, Double side cooled		1947	A
I _{TSM}	Surge on-state current	Sine wave, 10 ms Without reverse voltage	25	18500	A
			130	17500	A
I ² t	I ² t	Sine wave, 10 ms Without reverse voltage	25	1711 x 10 ³	A ² s
			130	1531 x 10 ³	A ² s
V _T	On-state voltage	On-state current = 3700A	130	2.02	V
V _{T(TO)}	Threshold voltage		130	0.85	V
r _T	On-state slope resistance		130	0.32	mΩ
SWITCHING					
di/dt	Critical rate of rise of on-state current	V _D = 67%V _{DRM} , I _{TM} =1600A, I _{FG} =2A, tr≤0.5μs	130	200	A/μs
dv/dt	Critical rate of rise of off-state voltage	V _{DR} = 67%V _{DRM}	130	1000	V/μs
GATE					
I _{gt}	Gate trigger current	V _D =6V	25	250	mA
V _{gt}	Gate trigger voltage	V _D =6V	25	3.0	V
I _H	Holding current	V _D =6V, gate open circuit	25	300	mA
I _L	Latching current	V _D =6V	25	1000	mA
MOUNTING					
R _{th(j-c)}	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.028	°C/W
R _{th(j-c)}	Thermal impedance, rec120°	Junction to case, Double side cooled		0.032	°C/W
R _{th(c-h)}	Thermal impedance	Case to heatsink, Double side cooled		0.005	°C/W
T _j	Max. junction temperature			130	°C
T _{stg}	Storage temperature			-40 ... 130	°C
M	Clamping Force			12 - 15	kN
W	Weight (Approx.)			255	gm
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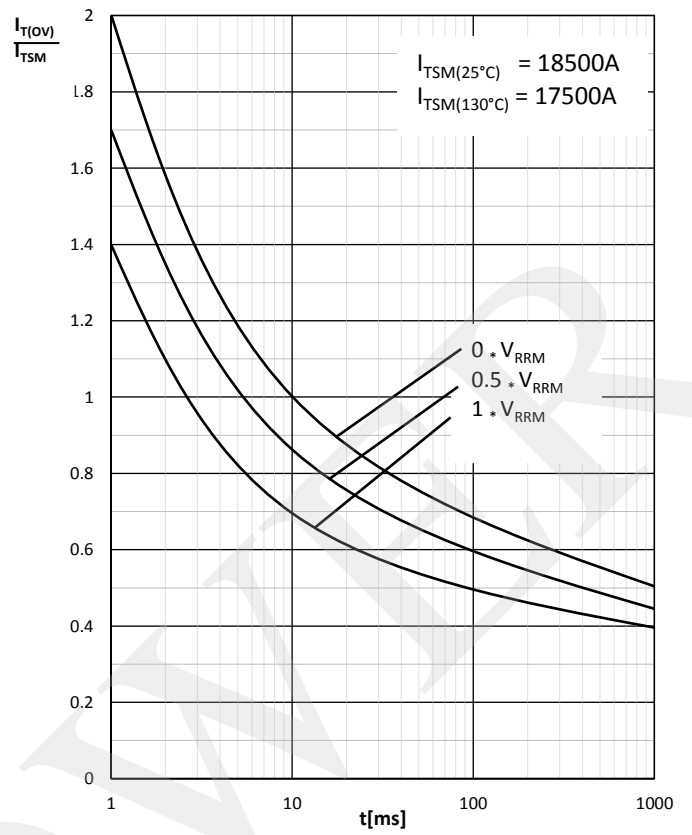


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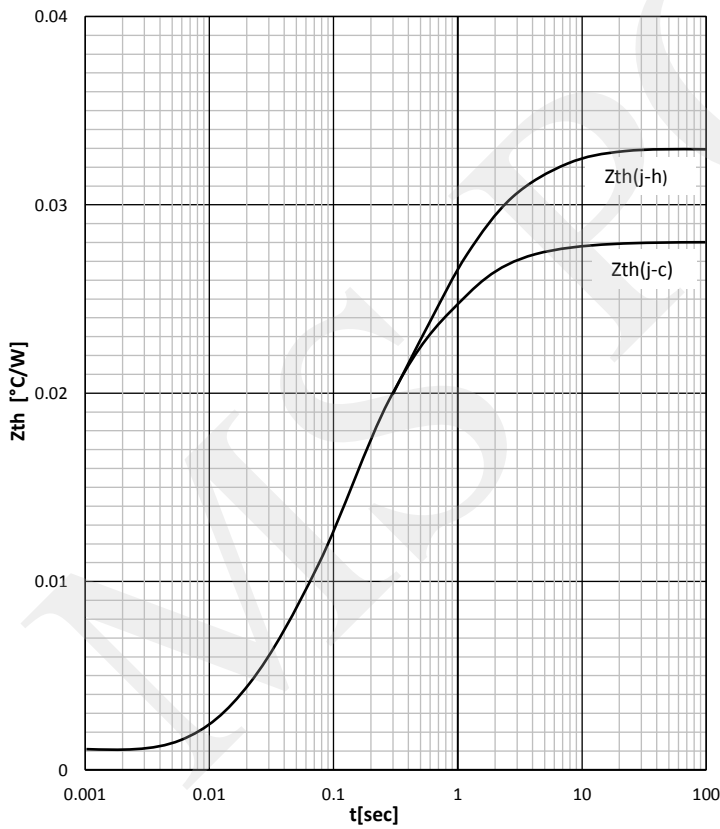
ON STATE CHARACTERISTIC



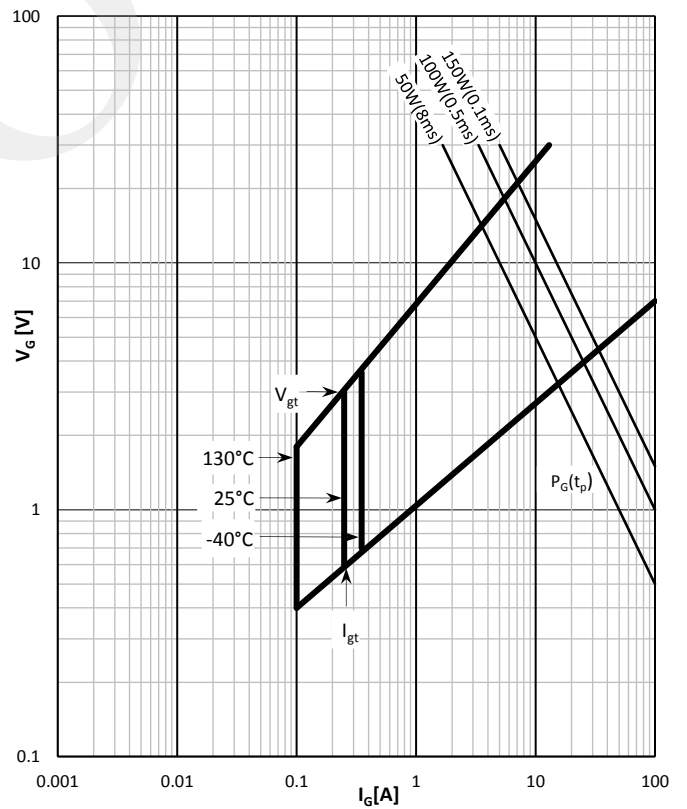
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE



GATE TRIGGER CHARACTERISTICS



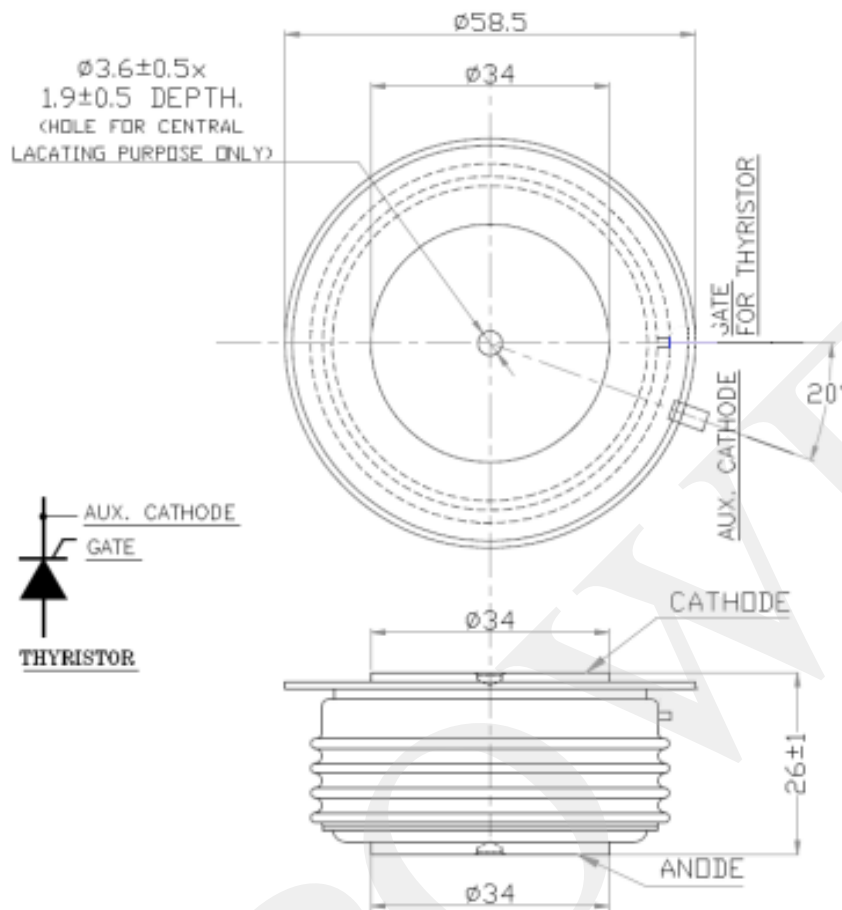
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