### **MS T1436**





### **Key Parameters**

 $\begin{array}{lll} V_{DRM} \, / \, V_{RRM} & = 3000 V \\ I_{T(AV)} & = 1436 A \\ I_{TSM} & = 25 k A \\ V_{T(TO)} & = 1.0 V \\ r_{T} & = 0.342 m \Omega \end{array}$ 

#### **Features**

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator

### **Applications**

- Battery Chargers
- Medical Equipment
- UPS
- Power Supplies
- Motor control
- Controlled Rectifiers
- Transportation
- Induction Heating
- Welding

### **Ordering Information**

MST	1436	С	ХX
Phase Control Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = $V_{DRM}/V_{RRM}$
Order Code MS T1436C30: 3000V VDRM, VRRM, 26mm clamp height capsule thyristor			

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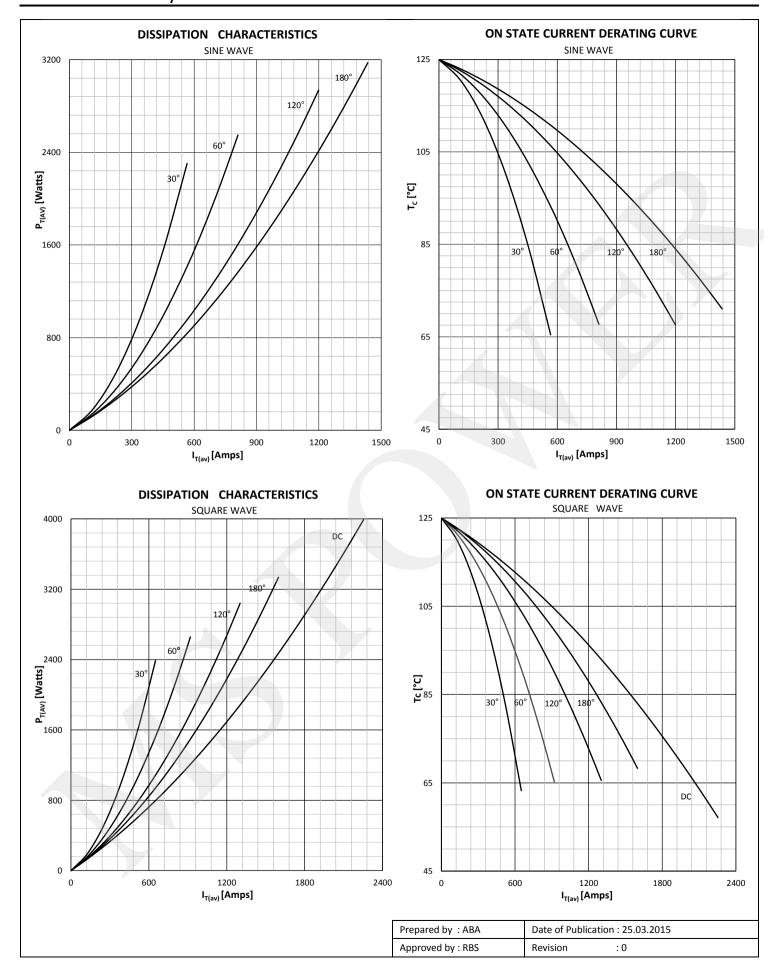


Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	NG				
V RRM	Repetitive peak reverse voltage		125	2400 - 3000	V
V RSM	Non-repetitive peak reverse voltage		125	2500 - 3100	V
V DRM	Repetitive peak off-state voltage		125	2400 - 3000	V
I RRM	Repetitive peak reverse current	V= V RRM	125	100	mA
I DRM	Repetitive peak off-state current	V= V DRM	125	100	mA
CONDU	CTING				
I T (AV)	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =70°C, Double side cooled		1436	А
I RMS	RMS on-state current	T <sub>c</sub> =70°C, Double side cooled		2254	Α
		Sine wave, 10 ms	25	25000	А
I тѕм	Surge on-state current	Without reverse voltage	125	23000	А
		Sine wave, 10 ms	25	3125 x 10 <sup>3</sup>	A <sup>2</sup> s
l² t	l² t	Without reverse voltage	125	2645 x 10 <sup>3</sup>	A <sup>2</sup> s
Vт	On-state voltage	On-state current = 2550A	125	1.9	V
V T(TO)	Threshold voltage		125	1.0	V
rт	On-state slope resistance		125	0.342	mΩ
SWITCH	ING				
di/dt	Critical rate of rise of on-state current	V <sub>D</sub> = 67%V <sub>DRM</sub> , I <sub>TM</sub> =3000A, I <sub>FG</sub> =2A, t <sub>r</sub> ≤0.5µs	125	200	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 80\%V_{DRM}$	125	1000	V/µs
GATE	1				
I gt	Gate trigger current	V <sub>D</sub> =6V	25	250	mA
V gt	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	600	mA
ΙL	Latching current	V <sub>D</sub> =6V	25	1000	mA
MOUNTI	NG				
R th(j-c)	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.017	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, Double side cooled		0.019	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, Double side cooled		0.005	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 125	°C
М	Clamping Force			19 - 26	kN
W	Weight (Approx.)			500	gm

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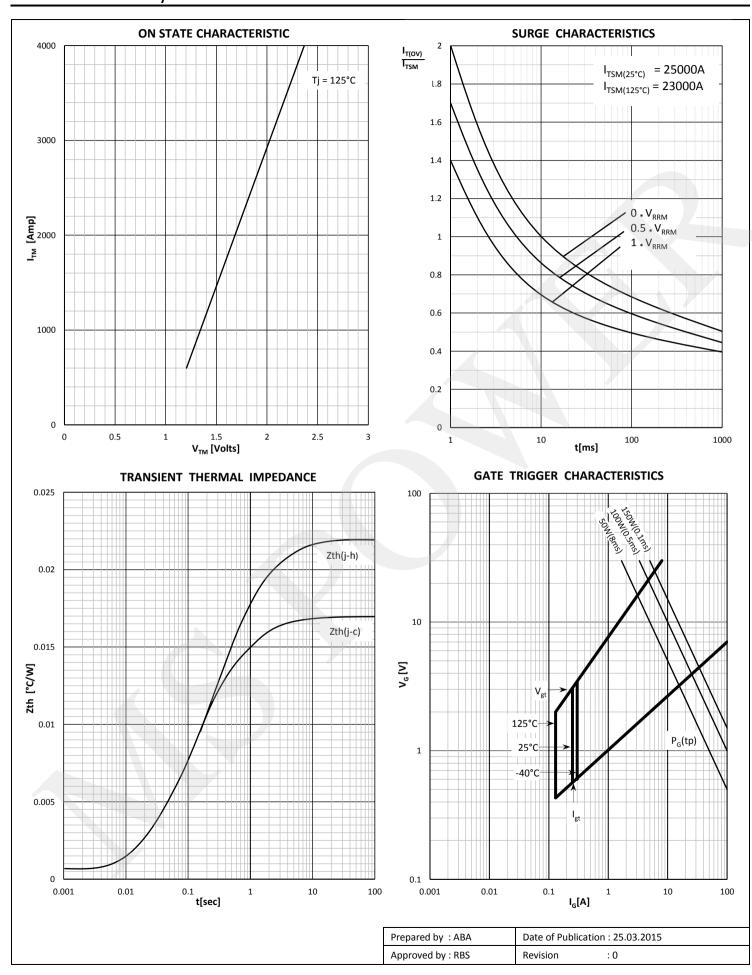
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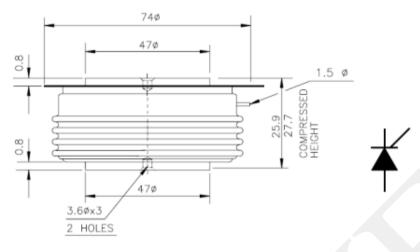


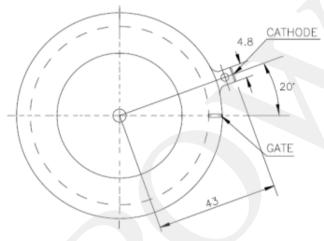


### **MS T1436**



### **Outline**





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