MS T1804





Key Parameters

VDRM / VRRM = 3700 VI_{T(AV)} = 1800A=28kA**I**TSM $V_{T(TO)}$ = 1.113V $= 0.278 m\Omega$ rт

Features

- Full blocking capability over wide temperature
- High Surge current capability
- Hermetic metal case with ceramic insulator

- **Applications** Battery Chargers
- Medical Equipment
- **UPS**
- Power Supplies
- Motor control
- Controlled Rectifiers
- Transportation
- Induction Heating
- Welding

Ordering Information

MS T	1804	С	ХX
Phase Control Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V _{DRM} /V _{RRM}
Order Code MS T1804C37: 3700V VDRM, VRRM, 36mm clamp height capsule thyristor			

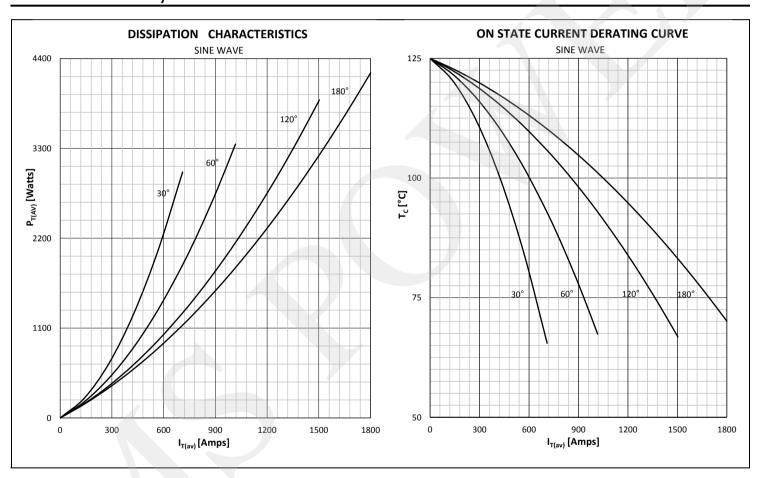
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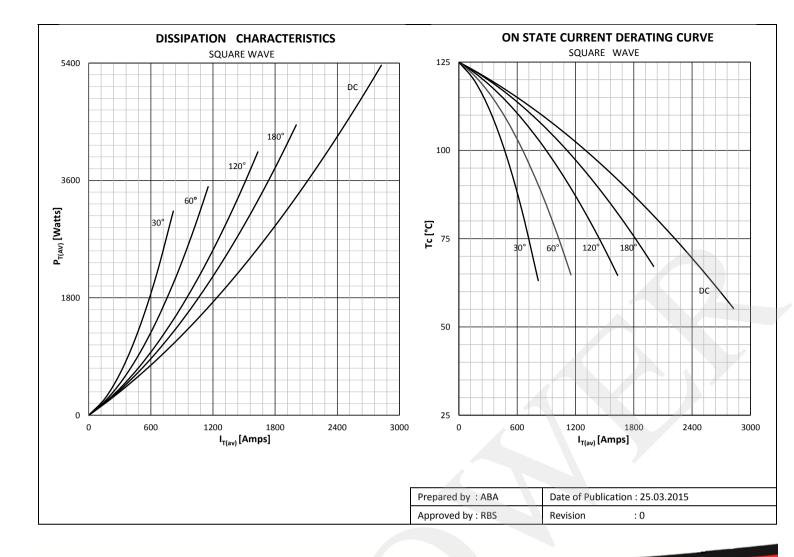


Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	ING				
V RRM	Repetitive peak reverse voltage		125	3000 - 3700	V
V RSM	Non-repetitive peak reverse voltage		125	3100 - 3800	V
V DRM	Repetitive peak off-state voltage		125	3000 - 3700	V
I RRM	Repetitive peak reverse current	V= V RRM	125	150	mA
I DRM	Repetitive peak off-state current	V= V DRM	125	150	mA
CONDU	CTING				
I T (AV)	Mean on state current	180° sin ,50 Hz, T _c =70°C, Double side cooled		1800	А
I RMS	RMS on-state current			2826	Α
		Sine wave, 10 ms	25	28000	Α
I TSM	Surge on-state current	Without reverse voltage	125	27000	А
	l² t	Sine wave, 10 ms	25	3920 x 10 ³	A²s
l² t		Without reverse voltage	125	3645 x 10 ³	A²s
Vт	On-state voltage	On-state current = 2000A	125	1.71	V
V T(TO)	Threshold voltage		125	1.113	V
rт	On-state slope resistance		125	0.278	mΩ
SWITCH	IING				
di/dt	Critical rate of rise of on-state current		125	100	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 67\%V_{DRM}$	125	500	V/µs
GATE					
I gt	Gate trigger current	V _D =6V	25	200	mA
V gt	Gate trigger voltage	V _D =6V	25	3.0	V
1 _H	Holding current	V _D =6V, gate open circuit	25	800	mA
I _L	Latching current	V _D =6V	25	1000	mA
MOUNT	ING				
R th(j-c)	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.012	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, Double side cooled		0.014	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, Double side cooled		0.002	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 125	°C
М	Clamping Force			40 - 44	kN
W	Weight (Approx.)			1600	gm

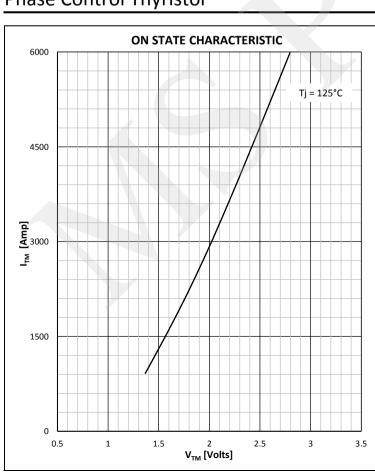
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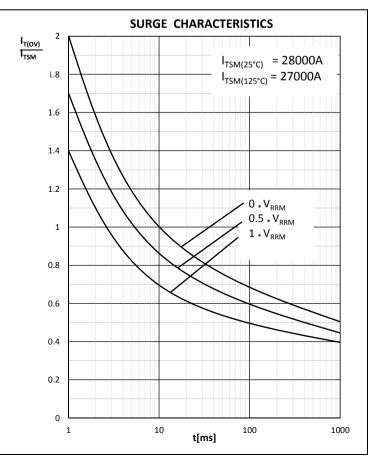


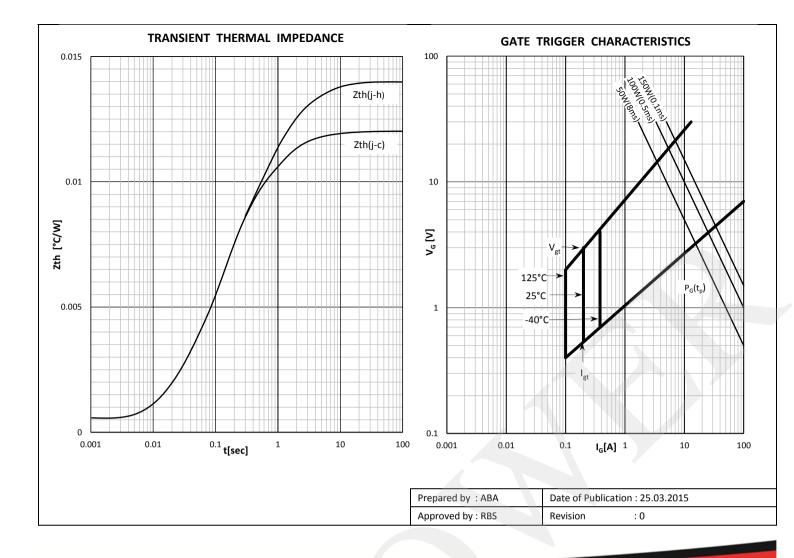










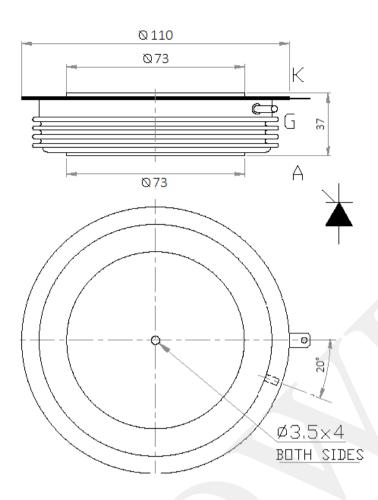


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Technical Information
Phase Control Thyristor



Outline



MS Power GmbH

Mergenthalerallee 79-81 65760 Eschborn, Germany Web: www.mspowergroup.com Mail: info@mspowergroup.de

Sales & Enquiry:

sales@mspowergroup.de

Technical Support:

solution@mspowergroup.de

After sales Service:

service@mspowergroup.de

Phone: +49 (0) 6196/7768 666 Fax: +49 (0) 6196/7757 888



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