# **MS T2485**





## **Key Parameters**

 $\begin{array}{lll} V_{DRM} \, / \, V_{RRM} & = 3000 V \\ I_{T(AV)} & = 2485 A \\ I_{TSM} & = 41 k A \\ V_{T(TO)} & = 0.78 V \\ r_{T} & = 0.274 m \Omega \end{array}$ 

### **Features**

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator

### **Applications**

- Battery Chargers
- Medical Equipment
- UPS
- Power Supplies
- Motor control
- Controlled Rectifiers
- Transportation
- Induction Heating
- Welding

## **Ordering Information**

MS T	2485	С	ХX
Phase Control Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub>
Order Code, MS T2485C30 : 3000V Vooy Vooy 37mm clamp height capsule thyristor			

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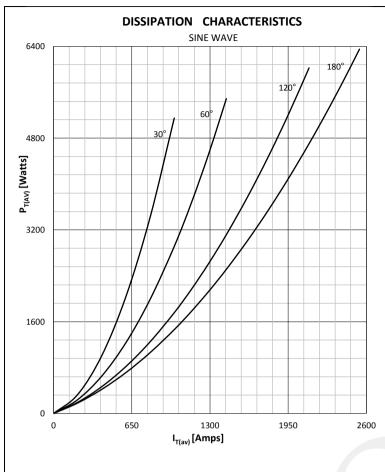


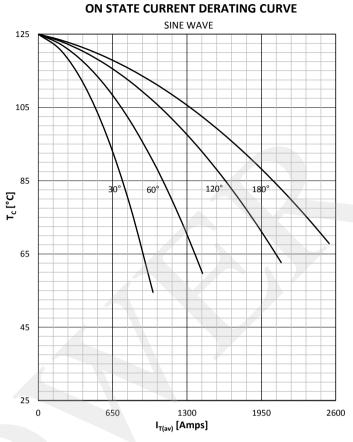
Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	NG				
V RRM	Repetitive peak reverse voltage		125	2400 - 3000	V
V RSM	Non-repetitive peak reverse voltage		125	2500 - 3100	V
V DRM	Repetitive peak off-state voltage		125	2400 - 3000	V
I RRM	Repetitive peak reverse current	V= V RRM	125	200	mA
I DRM	Repetitive peak off-state current	V= V DRM	125	200	mA
CONDU	CTING		II.		
I T (AV)	Mean on state current	180° sin ,50 Hz, $T_c$ =70°C, Double side cooled 180° sin ,50 Hz, $T_c$ =67°C, Double side cooled		2485 2543	A
I RMS	RMS on-state current	T <sub>c</sub> =67°C, Double side cooled		3992	Α
		Sine wave, 10 ms	25	41000	A
I TSM	Surge on-state current	Without reverse voltage	125	38000	Α
		Sine wave, 10 ms	25	8405 x 10 <sup>3</sup>	A²s
l² t	l <sup>2</sup> t	Without reverse voltage	125	7220 x 10 <sup>3</sup>	A <sup>2</sup> s
V т	On-state voltage	On-state current = 3000A	125	1.60	V
V T(TO)	Threshold voltage		125	0.78	V
rт	On-state slope resistance		125	0.274	mΩ
SWITCH	ING				
di/dt	Critical rate of rise of on-state current	V <sub>D</sub> = 67%V <sub>DRM</sub> ,I <sub>TM</sub> =1000A,I <sub>FG</sub> =2A,tr≤0.5μs	125	150	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 80\% V_{DRM}$	125	1000	V/µs
GATE					
I gt	Gate trigger current	V <sub>D</sub> =6V	25	250	mA
V <sub>gt</sub>	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	800	mA
I L	Latching current	V <sub>D</sub> =6V	25	1000	mA
MOUNTI	NG				
R th(j-c)	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.009	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, Double side cooled		0.010	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, Double side cooled		0.002	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 125	°C
М	Clamping Force			27 - 47	kN
W	Weight (Approx.)			1700	gm

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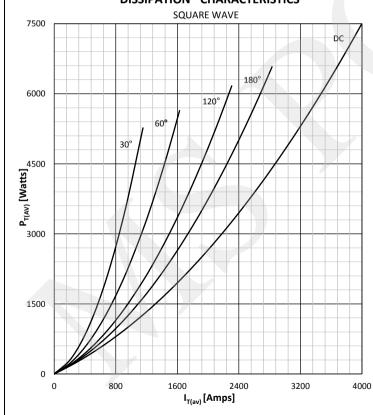
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## DISSIPATION CHARACTERISTICS



# 105 85 30° 60° 120° 180° 65 45

ON STATE CURRENT DERATING CURVE

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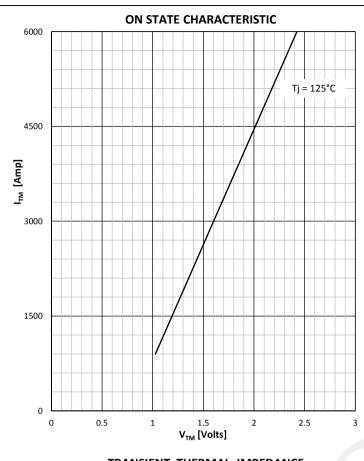
 $I_{T(av)}[Amps]$ 

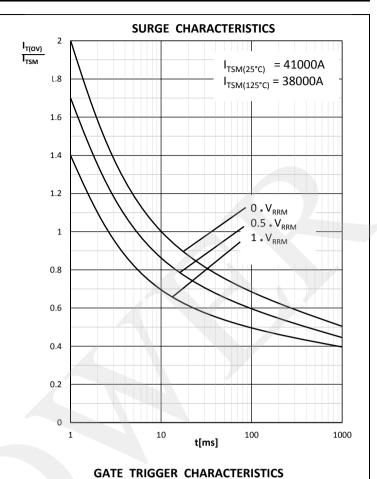
4000

3200

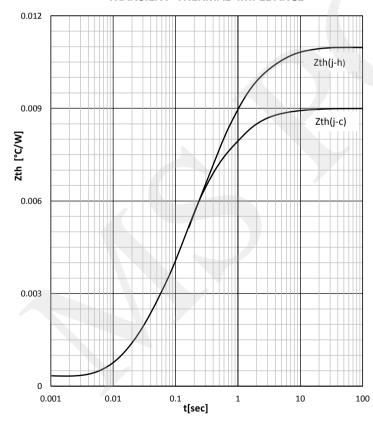
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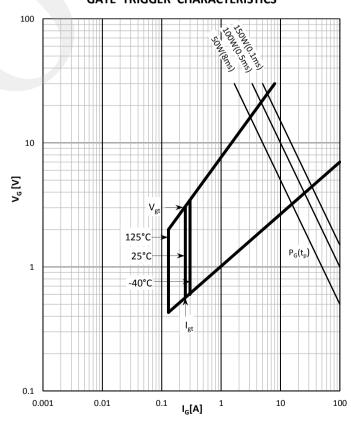










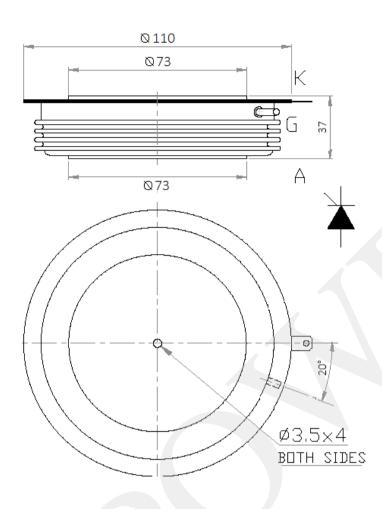


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### **Outline**



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