



**Key Parameters**

|                     |           |
|---------------------|-----------|
| $V_{DRM} / V_{RRM}$ | = 2200V   |
| $I_{T(AV)}$         | = 3472A   |
| $I_{TSM}$           | = 58kA    |
| $V_{T(TO)}$         | = 0.90V   |
| $r_T$               | = 0.112mΩ |

**Features**

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator

**Applications**

- Battery Chargers
- Medical Equipment
- UPS
- Power Supplies
- Motor control
- Controlled Rectifiers
- Transportation
- Induction Heating
- Welding

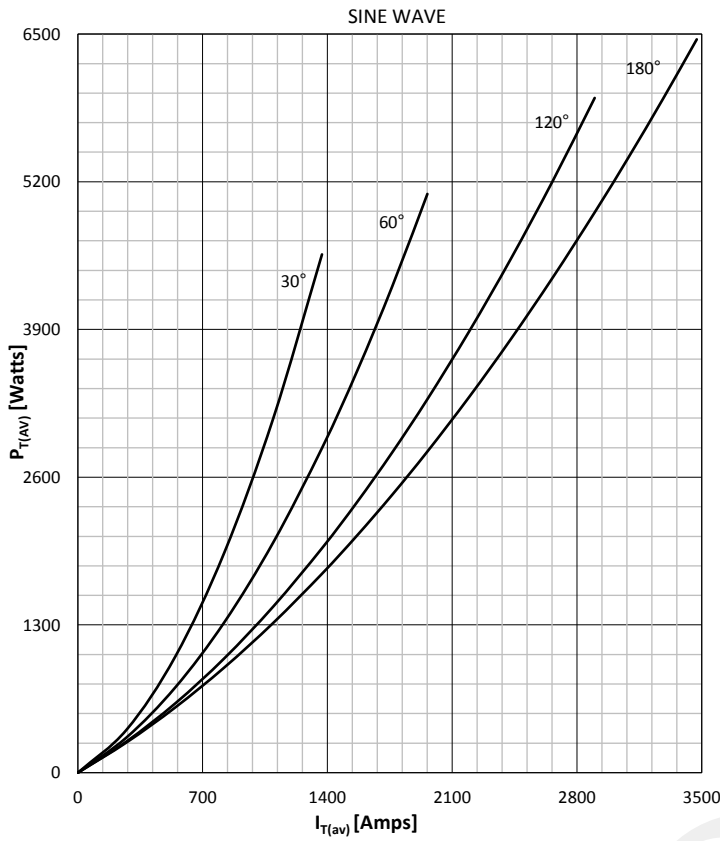
**Ordering Information**

|                                                                                         |              |                                                     |                                                |
|-----------------------------------------------------------------------------------------|--------------|-----------------------------------------------------|------------------------------------------------|
| <b>MS T</b>                                                                             | <b>3472</b>  | <b>C</b>                                            | <b>XX</b>                                      |
| Phase Control Thyristor                                                                 | Current Code | C - Capsule package with Alloyed silicon technology | Voltage Code<br>Code X 100 = $V_{DRM}/V_{RRM}$ |
| Order Code MS T3472C22 : 2200V $V_{DRM}, V_{RRM}$ , 26mm clamp height capsule thyristor |              |                                                     |                                                |

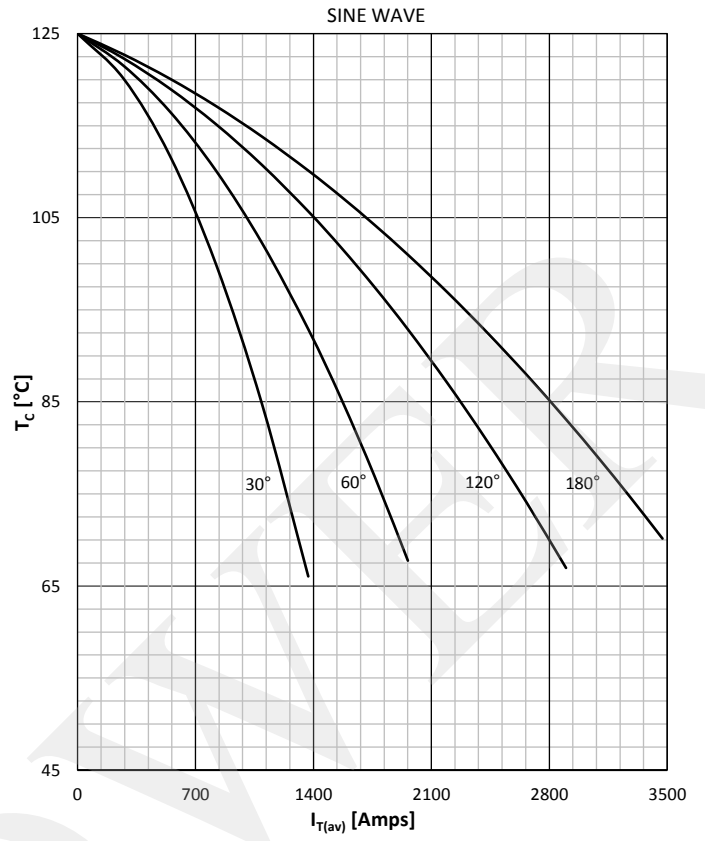
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| Symbol               | Characteristic                             | Conditions                                                                                                             | T <sub>j</sub><br>[°C] | Value                            | Unit             |
|----------------------|--------------------------------------------|------------------------------------------------------------------------------------------------------------------------|------------------------|----------------------------------|------------------|
| <b>BLOCKING</b>      |                                            |                                                                                                                        |                        |                                  |                  |
| V <sub>RRM</sub>     | Repetitive peak reverse voltage            |                                                                                                                        | 125                    | 2000 - 2200                      | V                |
| V <sub>RSM</sub>     | Non-repetitive peak reverse voltage        |                                                                                                                        | 125                    | 2100 - 2300                      | V                |
| V <sub>DRM</sub>     | Repetitive peak off-state voltage          |                                                                                                                        | 125                    | 2000 - 2200                      | V                |
| I <sub>RRM</sub>     | Repetitive peak reverse current            | V = V <sub>RRM</sub>                                                                                                   | 125                    | 250                              | mA               |
| I <sub>DRM</sub>     | Repetitive peak off-state current          | V = V <sub>DRM</sub>                                                                                                   | 125                    | 250                              | mA               |
| <b>CONDUCTING</b>    |                                            |                                                                                                                        |                        |                                  |                  |
| I <sub>T(AV)</sub>   | Mean on state current                      | 180° sin ,50 Hz, T <sub>c</sub> =70°C, Double side cooled<br>180° sin ,50 Hz, T <sub>c</sub> =85°C, Double side cooled |                        | 3472<br>2810                     | A                |
| I <sub>RMS</sub>     | RMS on-state current                       | T <sub>c</sub> =70°C, Double side cooled                                                                               |                        | 5451                             | A                |
| I <sub>TSM</sub>     | Surge on-state current                     | Sine wave, 10 ms<br>Without reverse voltage                                                                            | 25                     | 58000                            | A                |
|                      |                                            |                                                                                                                        | 125                    | 50000                            | A                |
| I <sup>2</sup> t     | I <sup>2</sup> t                           | Sine wave, 10 ms<br>Without reverse voltage                                                                            | 25                     | 16820 x 10 <sup>3</sup>          | A <sup>2</sup> s |
|                      |                                            |                                                                                                                        | 125                    | 12500 x 10 <sup>3</sup>          | A <sup>2</sup> s |
| V <sub>T</sub>       | On-state voltage                           | On-state current = 3000A                                                                                               | 125                    | 1.25                             | V                |
| V <sub>T(TO)</sub>   | Threshold voltage                          |                                                                                                                        | 125                    | 0.90                             | V                |
| r <sub>T</sub>       | On-state slope resistance                  |                                                                                                                        | 125                    | 0.112                            | mΩ               |
| <b>SWITCHING</b>     |                                            |                                                                                                                        |                        |                                  |                  |
| di/dt                | Critical rate of rise of on-state current  | V <sub>D</sub> = 67%V <sub>DRM</sub> , I <sub>GM</sub> =1A, di <sub>G</sub> /dt = 1A/μs, f=50Hz                        | 125                    | 200                              | A/μs             |
| dv/dt                | Critical rate of rise of off-state voltage | V <sub>DR</sub> = 67%V <sub>DRM</sub>                                                                                  | 125                    | 1000                             | V/μs             |
| <b>GATE</b>          |                                            |                                                                                                                        |                        |                                  |                  |
| I <sub>gt</sub>      | Gate trigger current                       | V <sub>D</sub> =6V                                                                                                     | 25                     | 250                              | mA               |
| V <sub>gt</sub>      | Gate trigger voltage                       | V <sub>D</sub> =6V                                                                                                     | 25                     | 3.0                              | V                |
| I <sub>H</sub>       | Holding current                            | V <sub>D</sub> =6V, gate open circuit                                                                                  | 25                     | 300                              | mA               |
| I <sub>L</sub>       | Latching current                           | V <sub>D</sub> =6V                                                                                                     | 25                     | 1500                             | mA               |
| <b>MOUNTING</b>      |                                            |                                                                                                                        |                        |                                  |                  |
| R <sub>th(j-c)</sub> | Thermal impedance, sin 180°                | Junction to case, Double side cooled                                                                                   |                        | 0.0085                           | °C/W             |
| R <sub>th(j-c)</sub> | Thermal impedance, rec120°                 | Junction to case, Double side cooled                                                                                   |                        | 0.0098                           | °C/W             |
| R <sub>th(c-h)</sub> | Thermal impedance                          | Case to heatsink, Double side cooled                                                                                   |                        | 0.0025                           | °C/W             |
| T <sub>j</sub>       | Max. junction temperature                  |                                                                                                                        |                        | 125                              | °C               |
| T <sub>stg</sub>     | Storage temperature                        |                                                                                                                        |                        | -40 .... 125                     | °C               |
| M                    | Clamping Force                             |                                                                                                                        |                        | 60 - 95                          | kN               |
| W                    | Weight (Approx.)                           |                                                                                                                        |                        | 1200                             | gm               |
|                      |                                            |                                                                                                                        | Prepared by : ABA      | Date of Publication : 25.03.2015 |                  |
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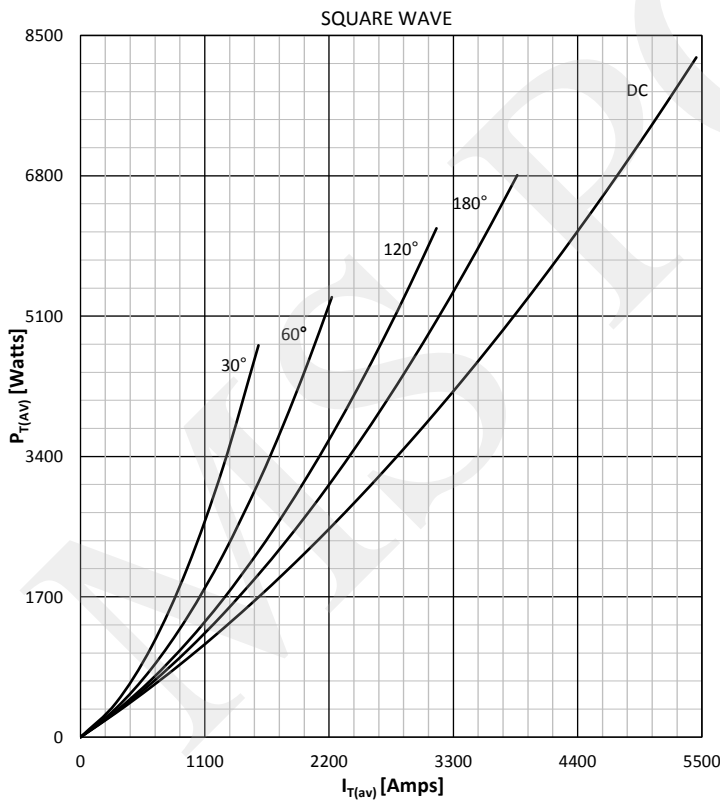
DISSIPATION CHARACTERISTICS



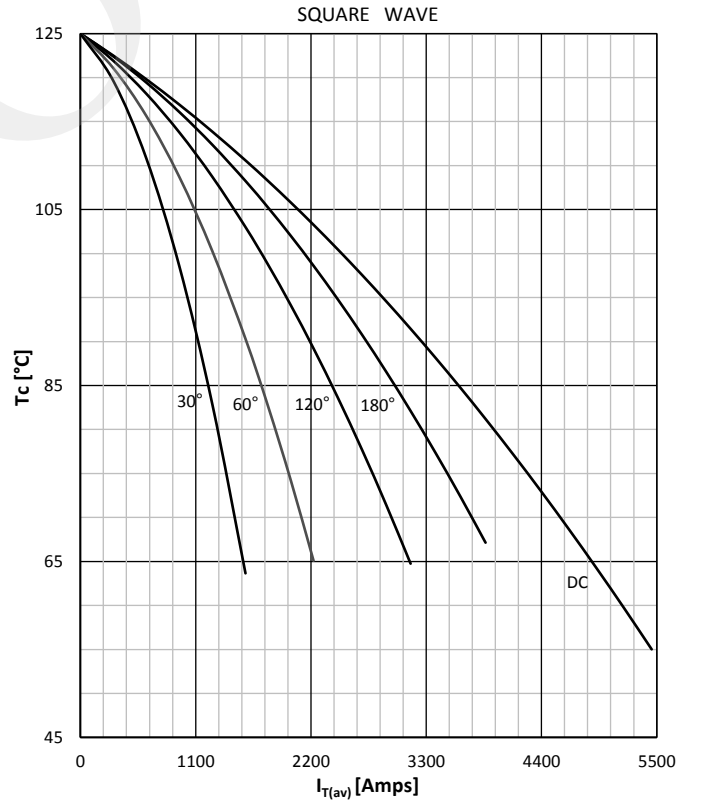
ON STATE CURRENT DERATING CURVE



DISSIPATION CHARACTERISTICS

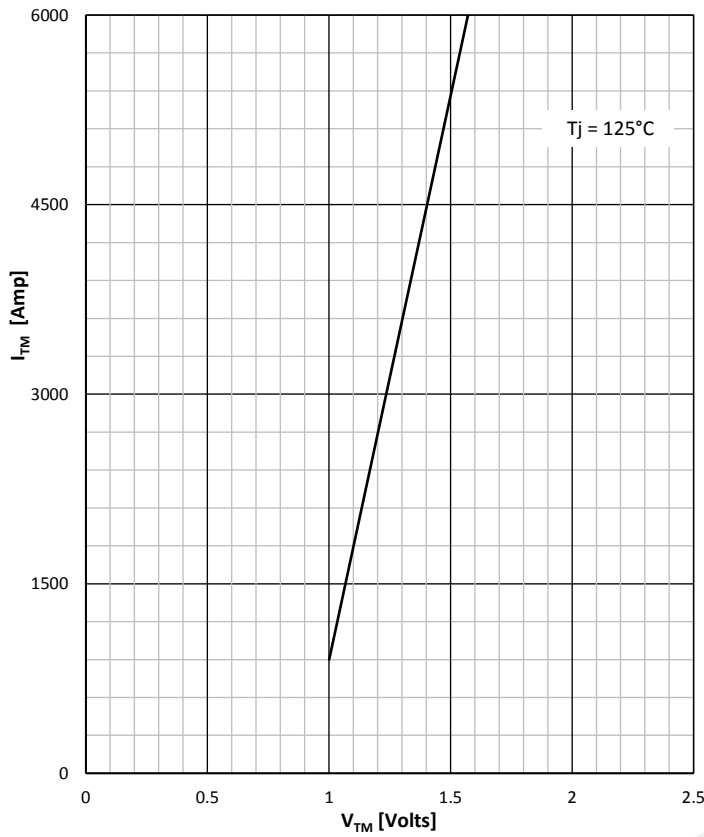


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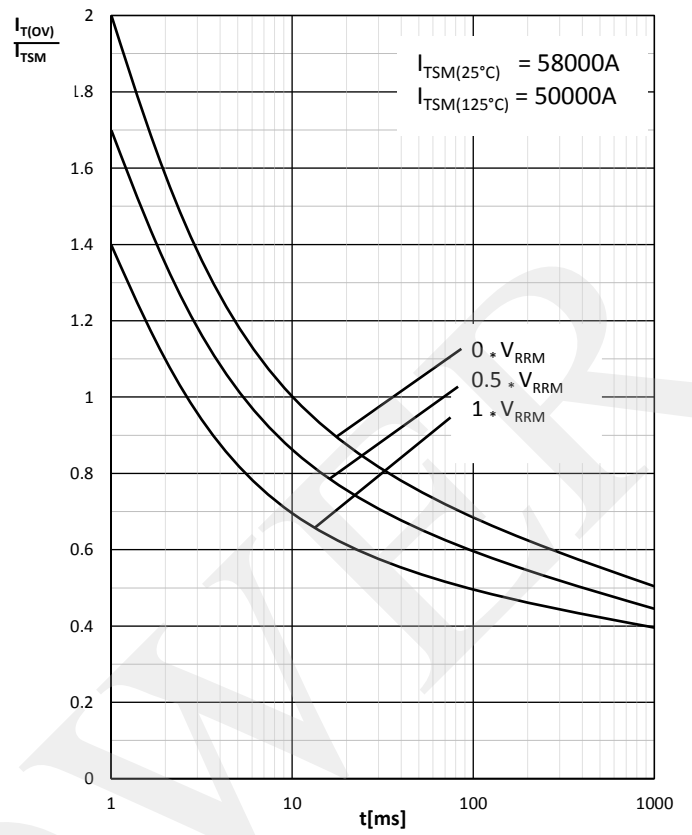


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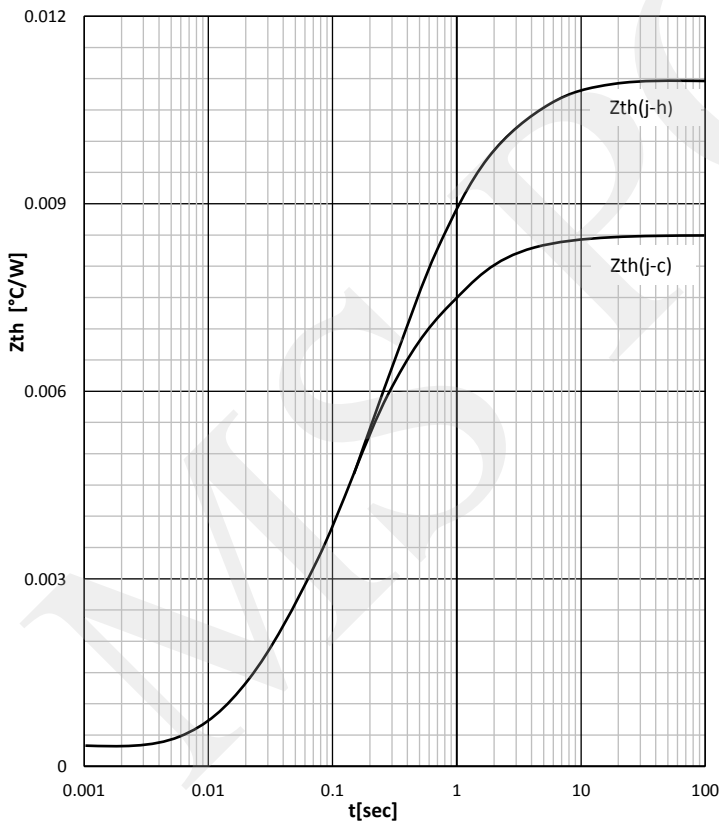
ON STATE CHARACTERISTIC



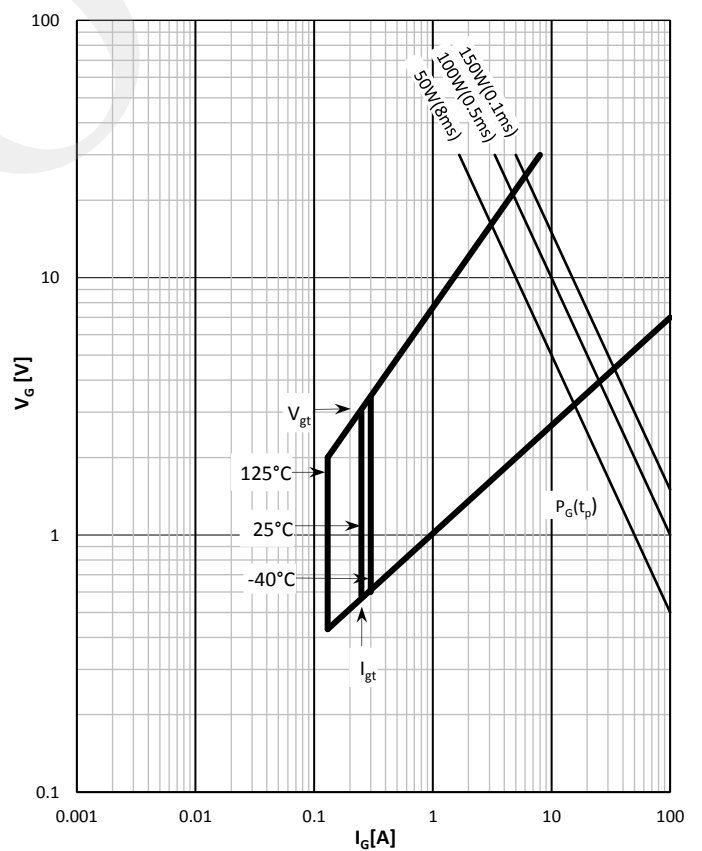
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE



GATE TRIGGER CHARACTERISTICS



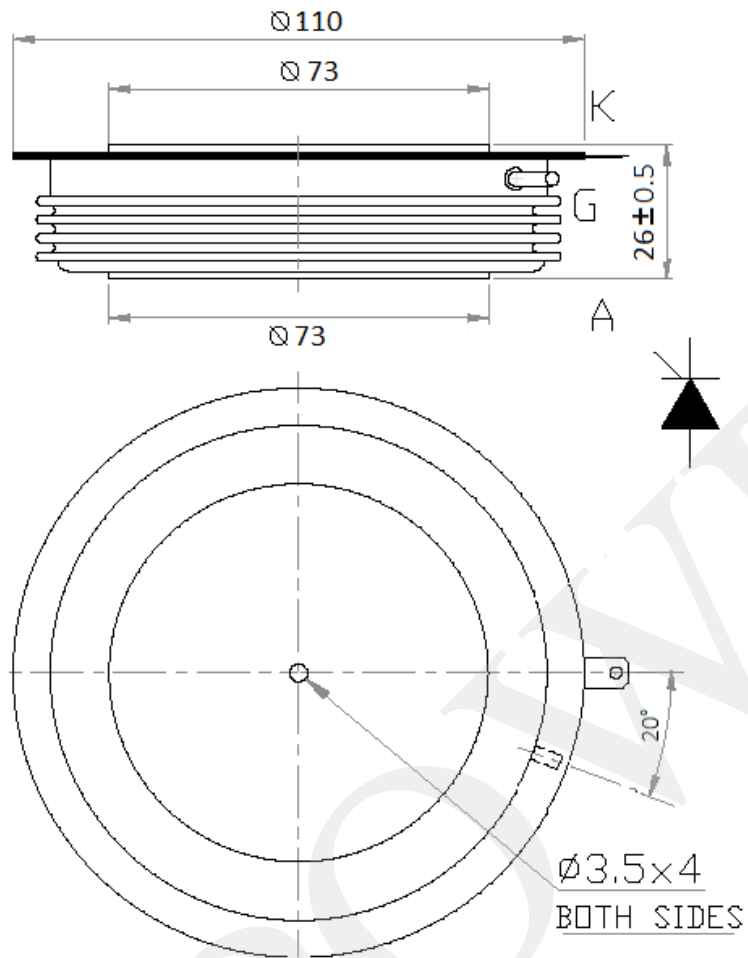
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