### **MS T3502**





### **Key Parameters**

VDRM / VRRM = 1600VI<sub>T(AV)</sub> = 3502A=42kA**I**TSM  $V_{T(TO)}$ = 0.776 V $= 0.0889 m\Omega$ rт

#### **Features**

- Full blocking capability over wide temperature
- High Surge current capability
  Hermetic metal case with ceramic insulator

- ApplicationsBattery ChargersMedical Equipment
- UPS
- Power Supplies
- Motor control
- Controlled Rectifiers
- Transportation
- Induction Heating
- Welding

### **Ordering Information**

MST	3502	С	ХX
Phase Control Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub>
Order Code MS T3502C16: 1600V V <sub>DRM</sub> , V <sub>RRM</sub> , 26mm clamp height capsule thyristor			

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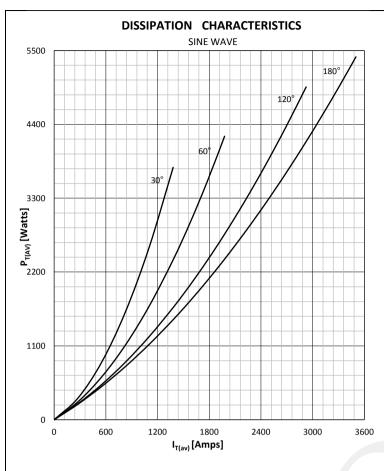


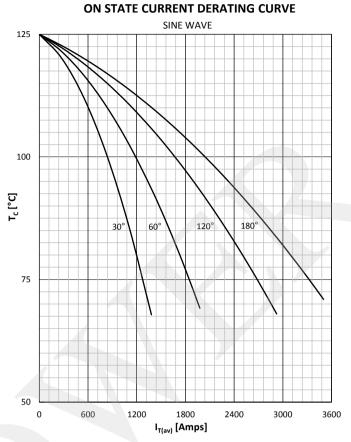
Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	NG				
V RRM	Repetitive peak reverse voltage		125	400 - 1600	V
V RSM	Non-repetitive peak reverse voltage		125	500 - 1700	V
V DRM	Repetitive peak off-state voltage		125	400 - 1600	V
I RRM	Repetitive peak reverse current	V= V RRM	125	150	mA
I DRM	Repetitive peak off-state current	V= V DRM	125	150	mA
CONDU	CTING		<u> </u>		
I T (AV)	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =70°C, Double side cooled 180° sin ,50 Hz, T <sub>c</sub> =76°C, Double side cooled		3502 3200	А
I RMS	RMS on-state current	T <sub>c</sub> =70°C, Double side cooled		5498	Α
	_	Sine wave, 10 ms	25	42000	A
I TSM	Surge on-state current	Without reverse voltage	125	40500	Α
		Sine wave, 10 ms	25	8820 x 10 <sup>3</sup>	A <sup>2</sup> s
l² t	l² t	Without reverse voltage	125	8201 x 10 <sup>3</sup>	A²s
V т	On-state voltage	On-state current = 3000A	25	1.25	V
V т(то)	Threshold voltage		125	0.776	V
rт	On-state slope resistance		125	0.0889	mΩ
SWITCH	ING			L	
di/dt	Critical rate of rise of on-state current	For 67%V <sub>DRM</sub> to 3000A, Gate 30V/10 $\Omega$ , t <sub>r</sub> <0.5 $\mu$ s	125	100	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 67\%V_{DRM}$	125	1000	V/µs
GATE					
l gt	Gate trigger current	V <sub>D</sub> =6V	25	250	mA
V <sub>gt</sub>	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	200	mA
I <sub>L</sub>	Latching current	V <sub>D</sub> =6V	25	1000	mA
MOUNTI	NG				
R th(j-c)	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.010	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, Double side cooled		0.0115	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, Double side cooled		0.002	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 125	°C
М	Clamping Force			27 - 44	kN
W	Weight (Approx.)			1450	gm

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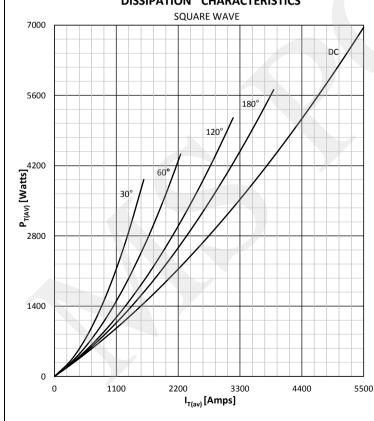
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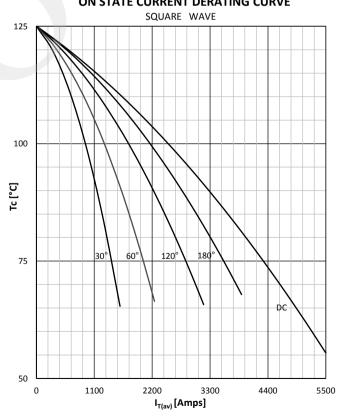




#### **DISSIPATION CHARACTERISTICS**



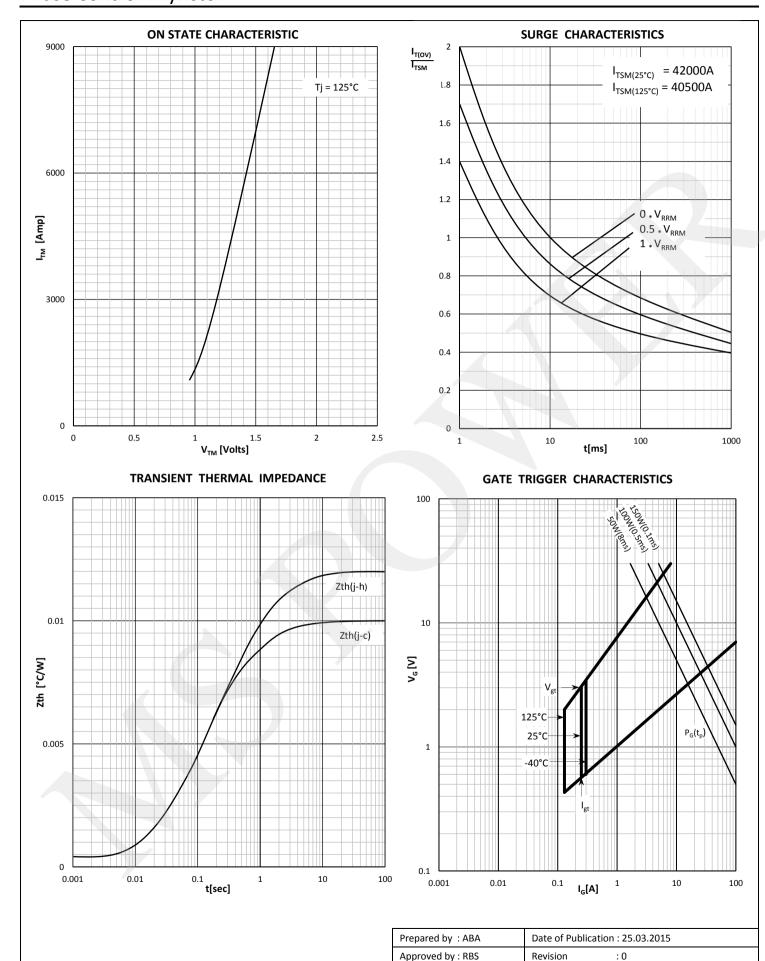
#### ON STATE CURRENT DERATING CURVE



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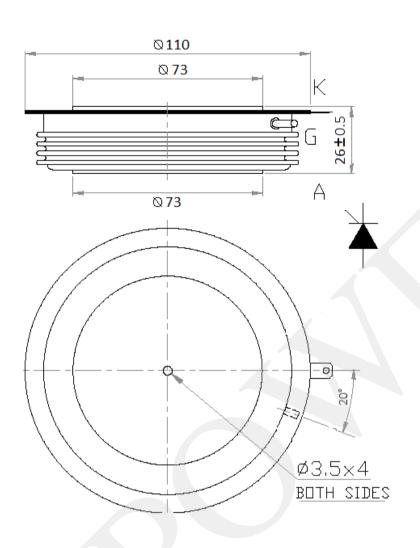




## **MS T3502**



#### **Outline**



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### **MS T3502**



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