MS T356





Key Parameters

Vdrm / Vrrm	= 1800V
IT(AV)	= 350A
ITSM	= 9000A
V _{T(TO)}	= 0.85V
rΤ	= 0.50mΩ

Features

- Full blocking capability over wide temperature range
- Pressure contacts technology for high reliability'
- Highest robustness

- ApplicationsPower SuppliesDC motor control
- **Controlled Rectifiers**
- AC switch

Ordering Information

MS T	356	S	X	Х		М	К
Phase Control Thyristor	Current Code	Stud / Flat Base Version		e Code = V _{DRM} /V _{RRM}	M =	Stud Threads = M24x1.5 = 3/4" 16UNF-2A	Technology K = Pressure Contact Technology
Order Code M	Order Code MS T356S16MK : 1600V VDRM, VRRM, Stud Thyristor with metric threads						
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				Prepar Approv			te of Publication : 25.03.2015 vision : 1

Technical Information Phase Control Thyristor

MS T356

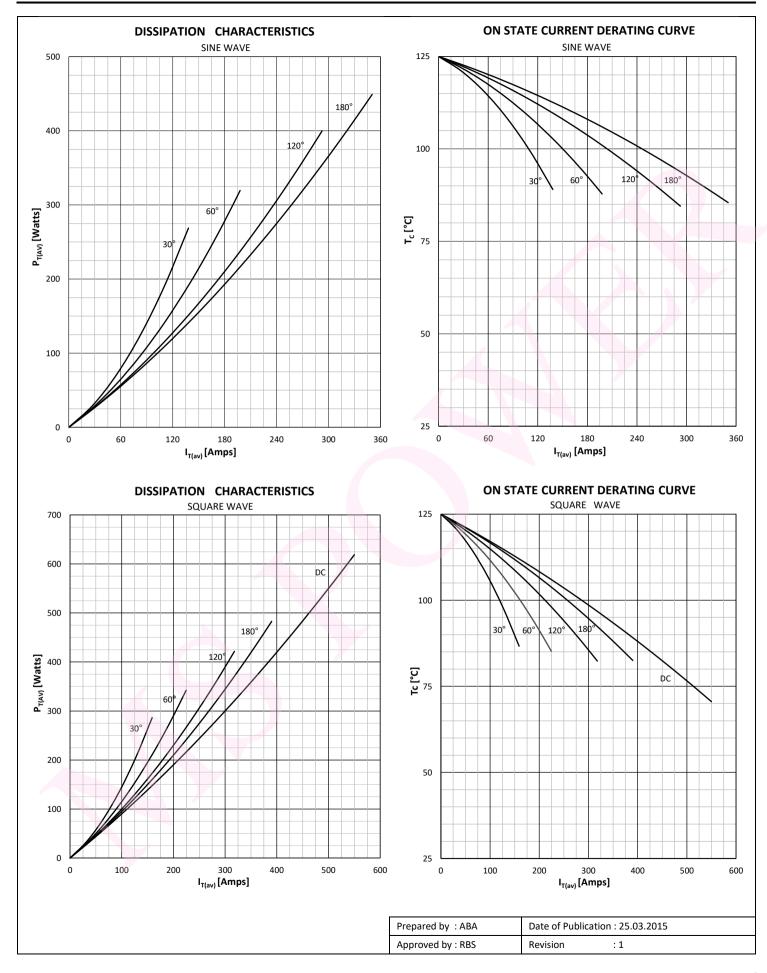


Symbol	Characteristic	Conditions	Тј [°С]	Value	Unit
BLOCK	NG				
V RRM	Repetitive peak reverse voltage		125	200 - 1800	V
V RSM	Non-repetitive peak reverse voltage		125	300 - 1900	V
V drm	Repetitive peak off-state voltage		125	200 - 1800	V
I RRM	Repetitive peak reverse current	V= V RRM	125	80	mA
I DRM	Repetitive peak off-state current	V= V drm	125	80	mA
CONDU	CTING				
I T (AV)	Mean on state current	180° sin ,50 Hz, T _c =85°C		350	А
I RMS	RMS on-state current	T _c =85°C		550	А
	_	V= V RRM V= V DRM 180° sin ,50 Hz, T _c =85°C	25	9000	А
I TSM	Surge on-state current		125	8000	А
		Sino wayo 10 mg	I I <thi< th=""> <thi< th=""> <thi< th=""> <thi< th=""></thi<></thi<></thi<></thi<>	A ² s	
l² t	l ² t	V= V DRM 180° sin ,50 Hz, T _c =85°C T _c =85°C Sine wave, 10 ms Without reverse voltage Sine wave, 10 ms Without reverse voltage On-state current = 1100A $V_{DR} = 67\%V_{DRM}$ $V_{D}=6V$ $V_{D}=6V$ $V_{D}=6V$ $V_{D}=6V$ $V_{D}=6V$ U_{D}=6V U_{D}=6V U_{D}=6V	125	320000	A²s
Vт	On-state voltage	On-state current = 1100A	125	1.65	V
V τ(το)	Threshold voltage		125	0.85	V
<u>г</u> т	On-state slope resistance		125	0.50	mΩ
SWITCH					
di/dt	Critical rate of rise of on-state current		125	200	A/µs
dv/dt	Critical rate of rise of off-state voltage	Vpp = 67%Vppm			V/µs
GATE					.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
l _{gt}	Gate trigger current	Vp=6V	25	200	mA
V gt	Gate trigger voltage				V
I _H	Holding current				mA
	Latching current		25	1000	mA
MOUNT					
R th(j-c)	Thermal impedance, sin 180°	Junction to case		0.088	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case		0.10	°C/W
R th(c-h)	Thermal impedance	Case to heatsink		0.02	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 125	°C
М	Mounting torque			2.5 - 2.7	kgM
	Weight (Approx.)			520	gm

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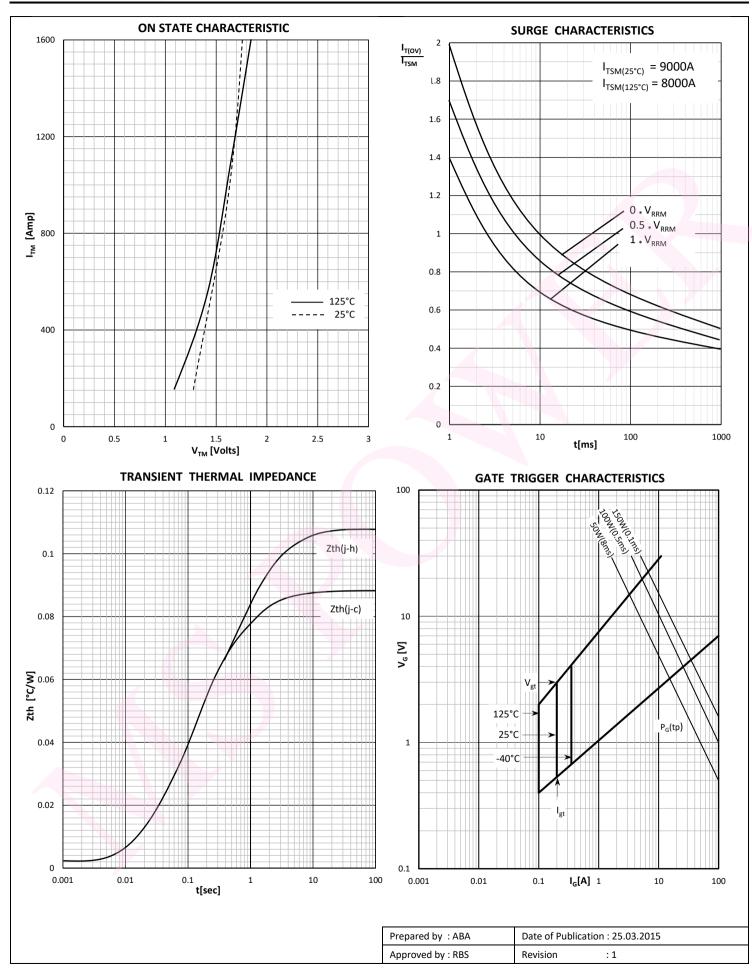




Technical Information Phase Control Thyristor

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Outline

ø39

M24×1.5PITCH (3/4" 16UNF 2A) 20

41 A/F

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NSpower

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