MS T710





Key Parameters

| Vdrm / Vrrm | = 2400V |
|--------------------|----------|
| T(AV) | = 710A |
| ТЅМ | = 9.0kA |
| V _{T(TO)} | = 0.95V |
| rΤ | = 0.72mΩ |
| | |

Features

- Full blocking capability over wide temperature range
- High Surge current capability .
- Hermetic metal case with ceramic insulator

- ApplicationsBattery ChargersMedical Equipment
- UPS
- Power Supplies
- Motor control
- **Controlled Rectifiers**
- Transportation
- Induction Heating
- Welding •

Ordering Information

| MS T | 710 | С | XX |
|----------------------------|--------------------------------|--------------------------------------------------------|-----------------------------------------------------------------|
| Phase Control Thyristor | Current Code | C - Capsule package with Alloyed silicon technology | Voltage Code Code X 100 = V _{DRM} /V _{RRM} |
| der Code MS T710C | 24 : 2400V V _{DRM} ,\ | / _{RRM} , 14mm clamp height capsule th | yristor |
| | | | |
| | | | |
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Technical Information Phase Control Thyristor

MS T710

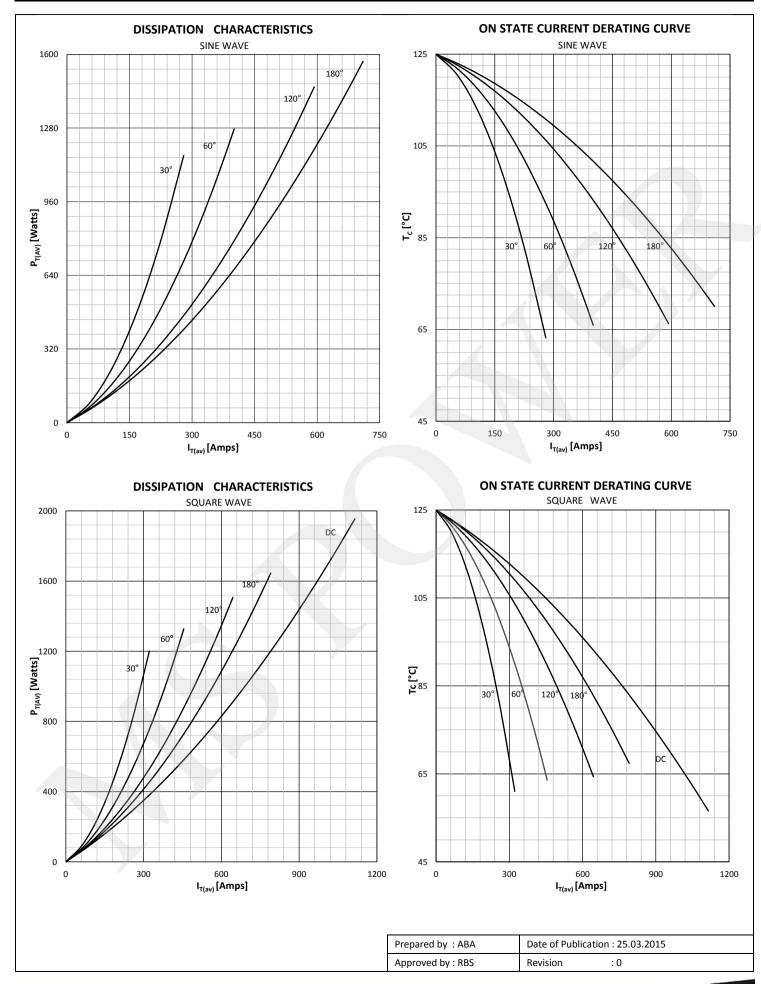


| Symbol | Characteristic | Conditions | Тј [°С] | Value | Unit |
|-----------------|--------------------------------------------|---------------------------------------------------------------------------------------------------|------------|-----------------------|------|
| BLOCKI | NG | | | | |
| V RRM | Repetitive peak reverse voltage | | 125 | 2000 - 2400 | V |
| V RSM | Non-repetitive peak reverse voltage | | 125 | 2100 - 2500 | V |
| V drm | Repetitive peak off-state voltage | | 125 | 2000 - 2400 | V |
| I RRM | Repetitive peak reverse current | V= V rrm | 125 | 50 | mA |
| I DRM | Repetitive peak off-state current | V= V drm | 125 | 50 | mA |
| CONDU | CTING | | | | |
| I T (AV) | Mean on state current | 180° sin ,50 Hz, $T_c=70$ °C, Double side cooled 180° sin ,50 Hz, $T_c=76$ °C, Double side cooled | | 710 660 | A |
| I RMS | RMS on-state current | T _c =70°C, Double side cooled | | 1115 | А |
| 1 | | Sine wave, 10 ms | 25 | 9000 | А |
| I TSM | Surge on-state current | Without reverse voltage | 125 | 7500 | Α |
| ² t ² t | n . | Sine wave, 10 ms | 25 | 405 x 10 ³ | A²s |
| | ² t | Without reverse voltage | 125 | 281 x 10 ³ | A²s |
| νт | On-state voltage | On-state current = 1650A | 125 | 2.16 | V |
| V T(TO) | Threshold voltage | | 125 | 0.95 | V |
| rт | On-state slope resistance | | 125 | 0.72 | mΩ |
| SWITCH | ING | | | | |
| di/dt | Critical rate of rise of on-state current | $V_D = 75\% V_{DRM}$ up to 860A, Gate 10V, 5 Ω | 125 | 200 | A/µs |
| dv/dt | Critical rate of rise of off-state voltage | $V_{DR} = 70\% V_{DRM}$ | 125 | 500 | V/µs |
| GATE | L | | | 1 | |
| l _{gt} | Gate trigger current | V _D =6V | 25 | 150 | mA |
| V _{gt} | Gate trigger voltage | V _D =6V | 25 | 3.0 | V |
| Г _н | Holding current | $V_{D}=6V$, gate open circuit | 25 | 300 | mA |
| I L | Latching current | V _D =6V | 25 | 600 | mA |
| MOUNTI | NG | | | | |
| R th(j-c) | Thermal impedance, sin 180° | Junction to case, Double side cooled | | 0.035 | °C/W |
| R th(j-c) | Thermal impedance, rec120° | Junction to case, Double side cooled | | 0.040 | °C/W |
| R th(c-h) | Thermal impedance | Case to heatsink, Double side cooled | | 0.015 | °C/W |
| Тj | Max. junction temperature | | | 125 | °C |
| T stg | Storage temperature | | | -40 125 | °C |
| М | Clamping Force | | | 8 - 9 | kN |
| W | Weight (Approx.) | | | 85 | gm |

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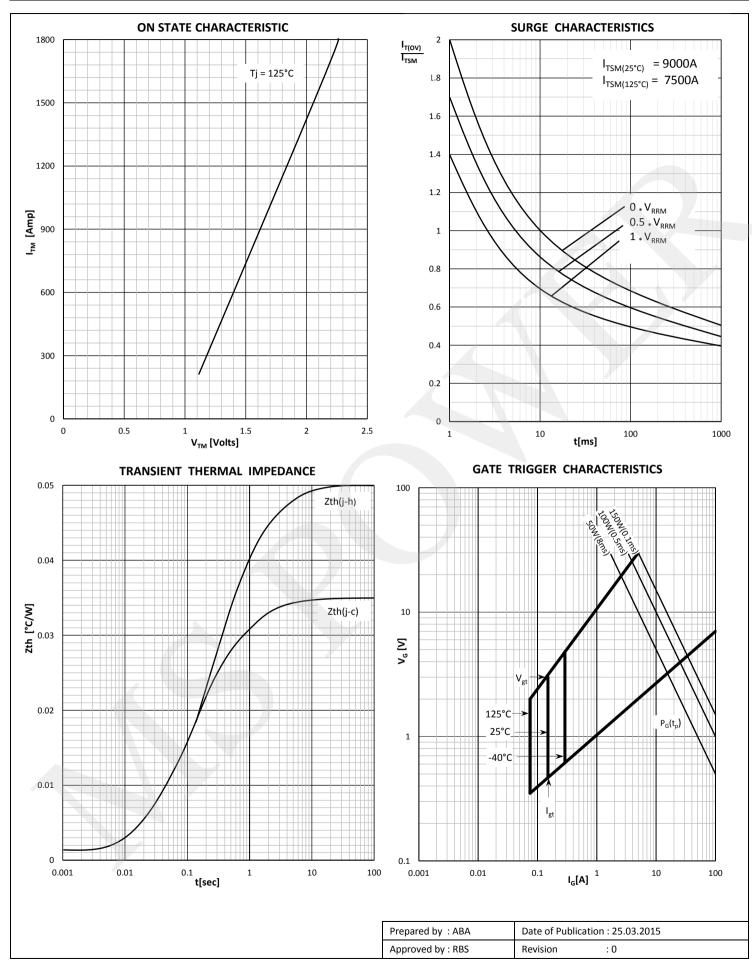




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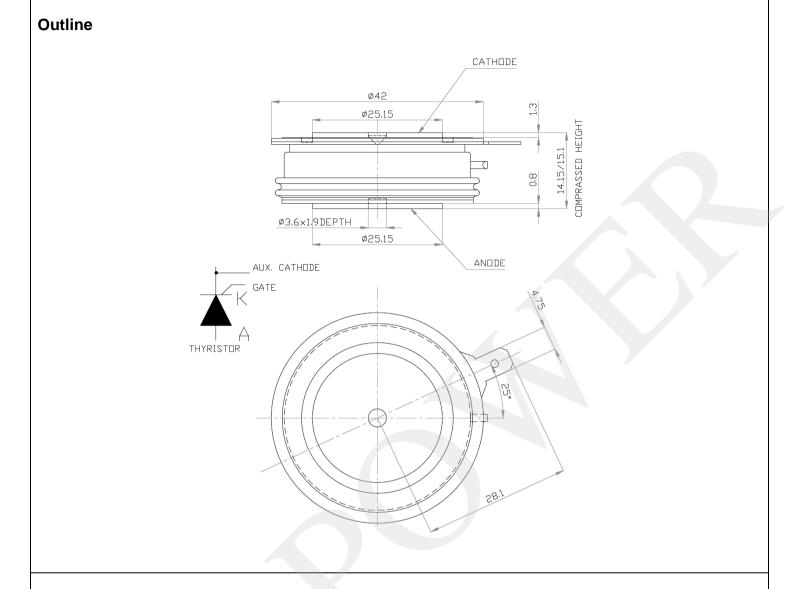




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