# **MS T775**





# **Key Parameters**

Vdrm / Vrrm	= 2400V
IT(AV)	= 775A
ТЅМ	= 11.0kA
V <sub>T(TO)</sub>	= 0.97V
ГТ	= 0.48mΩ

### **Features**

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator

- ApplicationsBattery ChargersMedical Equipment
- . UPS
- **Power Supplies** •
- Motor control
- **Controlled Rectifiers** •
- Transportation
- Induction Heating
- Welding

# **Ordering Information**

MS T	775	С	ХХ
Phase Control Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub>
Code MS T775	5C24 : 2400V Vdrm,Vrr	M, 14mm clamp height capsule thy	ristor
		Prepared by : ABA	Date of Publication : 25.03.2

# Technical Information Phase Control Thyristor

**MS T775** 

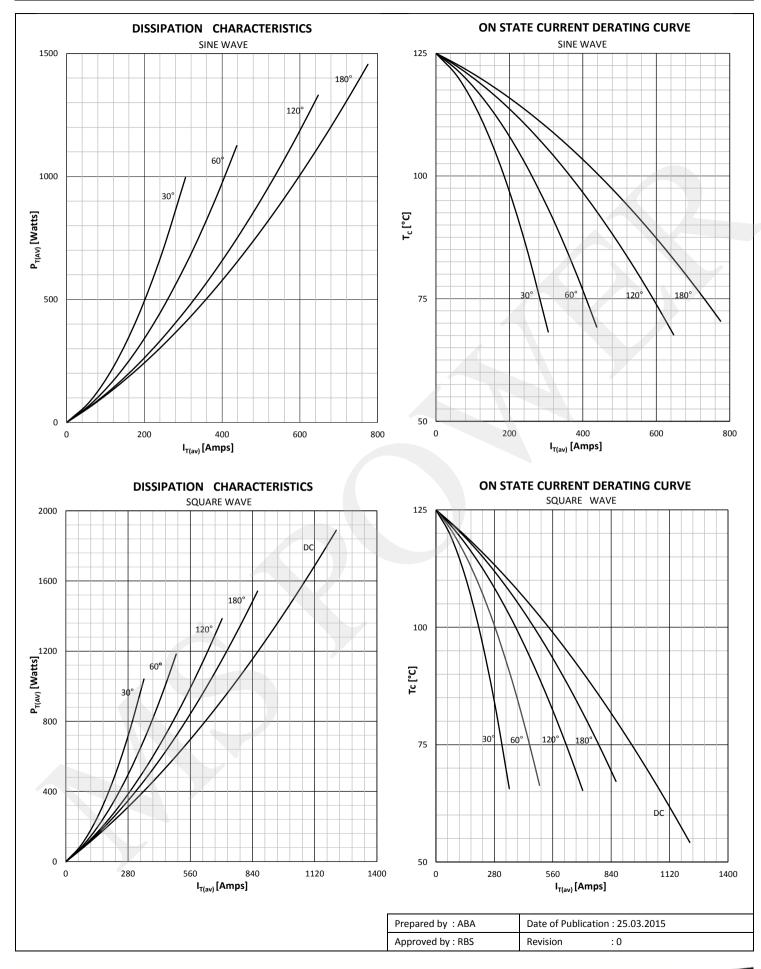


Symbol	Characteristic	Conditions	Тј [°С]	Value	Unit
BLOCKI	NG				
V RRM	Repetitive peak reverse voltage		125	1800 - 2400	V
V RSM	Non-repetitive peak reverse voltage		125	1900 - 2500	V
V drm	Repetitive peak off-state voltage		125	1800 - 2400	V
I RRM	Repetitive peak reverse current	V= V RRM	125	30	mA
I DRM	Repetitive peak off-state current	V= V drm	125	30	mA
CONDU	CTING				
I T (AV)	Mean on state current	180° sin ,50 Hz, $T_c=70$ °C, Double side cooled 180° sin ,50 Hz, $T_c=73$ °C, Double side cooled		775 750	Α
I RMS	RMS on-state current	T <sub>c</sub> =70°C, Double side cooled		1217	А
1		Sine wave, 10 ms Without reverse voltage	25	11000	А
I TSM	Surge on-state current		125	9600	Α
	Sine wave 10 ms	Sine wave, 10 ms	25	605 x 10 <sup>3</sup>	A²s
l² t	l <sup>2</sup> t	Without reverse voltage	125	461 x 10 <sup>3</sup>	A²s
νт	On-state voltage	On-state current = 625A	125	1.35	V
V t(to)	Threshold voltage		125	0.97	V
rт	On-state slope resistance		125	0.48	mΩ
SWITCH					
di/dt	Critical rate of rise of on-state current		125	150	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 67\% V_{DRM}$	125	500	V/µs
GATE	1				
l <sub>gt</sub>	Gate trigger current	V <sub>D</sub> =6V	25	150	mA
V <sub>gt</sub>	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
Iн	Holding current	$V_D=6V$ , gate open circuit	25	600	mA
I L	Latching current	V <sub>D</sub> =6V	25	1000	mA
MOUNT	NG		1		
R th(j-c)	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.035	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, Double side cooled		0.040	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, Double side cooled		0.02	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 125	°C
М	Clamping Force			8 - 12	kN
W	Weight (Approx.)			150	gm

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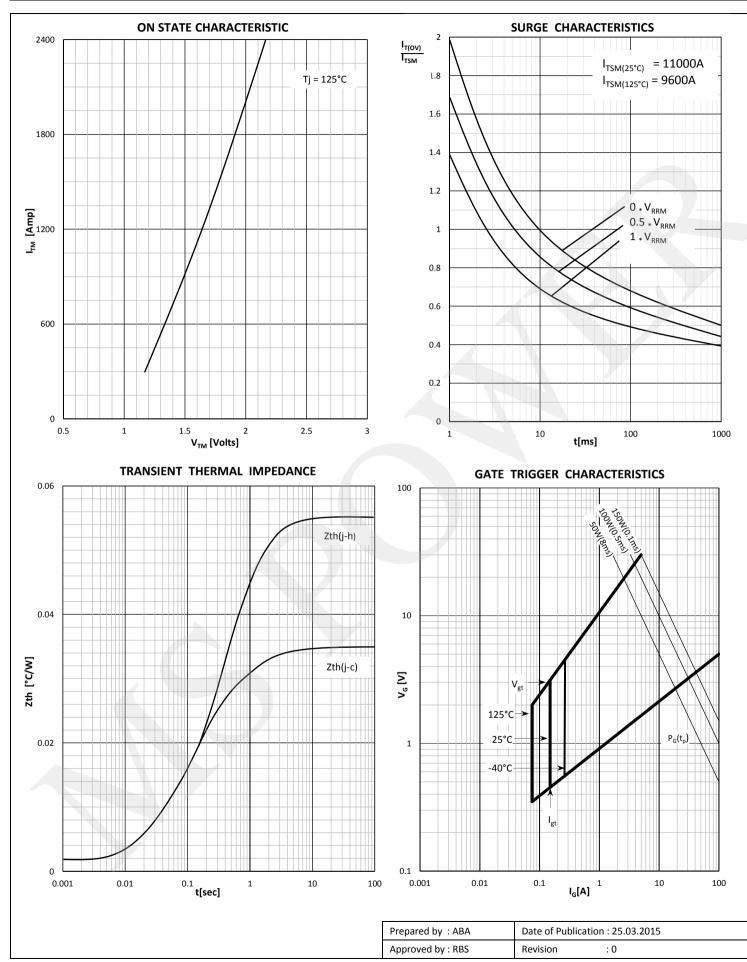




# Technical Information Phase Control Thyristor

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# Outline

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