



Key Parameters

V_{DRM} / V_{RRM}	= 2400V
$I_{T(AV)}$	= 261A
I_{TSM}	= 9500A
$V_{T(TO)}$	= 0.85V
r_T	= 0.64mΩ

Features

- Full blocking capability over wide temperature range
- Heat transfer through aluminium oxide ceramic isolated metal baseplate
- Pressure contacts technology for high reliability
- UL Recognized, file no. E505556


Applications

- Power Supplies
- DC motor control
- Controlled Rectifiers
- AC switch

Ordering Information

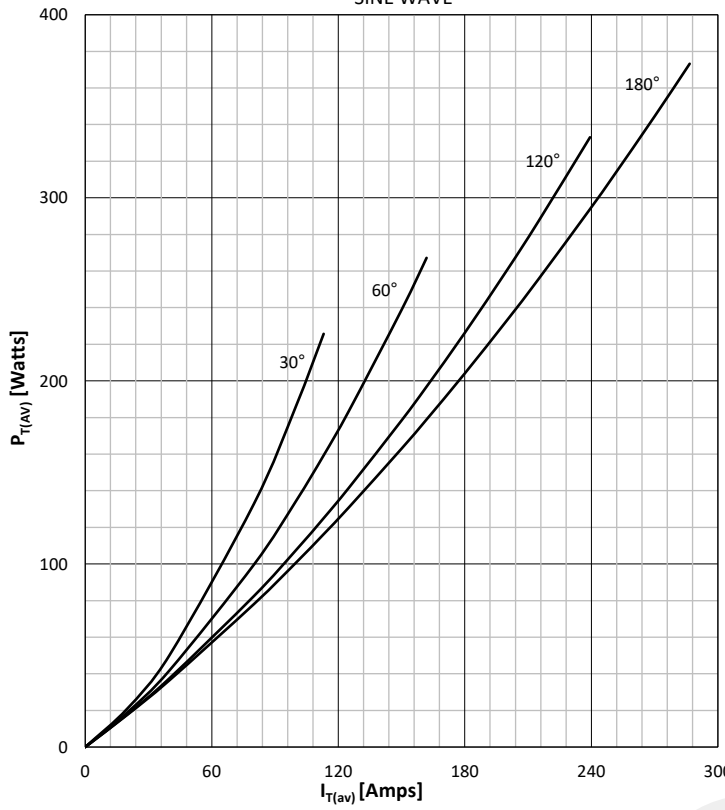
MS	TD	261	K	24
Fixed code	TD- Thyristor- Diode Module	Current Code	Technology K = Pressure Contact Technology	Voltage Code Code X 100 = V_{DRM}/V_{RRM}
Order Code MS TD261K24 : 2400V V_{DRM}, V_{RRM} , Thyristor-Diode Module				

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Symbol	Characteristic	Conditions	T _j [°C]	Value	Unit
BLOCKING					
V _{RRM}	Repetitive peak reverse voltage		125	2000 - 2400	V
V _{RSM}	Non-repetitive peak reverse voltage		125	2100 - 2500	V
V _{DRM}	Repetitive peak off-state voltage		125	2000 - 2400	V
I _{RRM}	Repetitive peak reverse current	V = V _{RRM}	125	70	mA
I _{DRM}	Repetitive peak off-state current	V = V _{DRM}	125	70	mA
CONDUCTING					
I _{T(AV)}	Mean on state current	180° sin ,50 Hz, T _c =85°C		261	A
I _{RMS}	RMS on-state current			450	A
I _{TSM}	Surge on-state current	Sine wave, 10 ms Without reverse voltage	25	9500	A
			125	8000	A
I ² t	I ² t	Sine wave, 10 ms Without reverse voltage	25	451000	A ² s
			125	320000	A ² s
V _T	On-state voltage	On-state current = 800A	125	1.45	V
V _{T(TO)}	Threshold voltage		125	0.85	V
r _T	On-state slope resistance		125	0.64	mΩ
SWITCHING					
di/dt	Critical rate of rise of on-state current	f=50Hz, I _{GM} =1A, di _G /dt=1A/μs	125	250	A/μs
dv/dt	Critical rate of rise of off-state voltage	V _{DR} = 67%V _{DRM}	125	1000	V/μs
GATE					
I _{gt}	Gate trigger current	V _D =6V	25	200	mA
V _{gt}	Gate trigger voltage	V _D =6V	25	3.0	V
I _H	Holding current	V _D =6V, gate open circuit	25	600	mA
I _L	Latching current	V _D =6V	25	1000	mA
MOUNTING					
R _{th(j-c)}	Thermal impedance, sin 180°	Junction to case, per arm per module		0.12 0.06	°C/W
R _{th(j-c)}	Thermal impedance, rec120°	Junction to case, per arm per module		0.14 0.07	°C/W
R _{th(c-h)}	Thermal impedance	Case to heatsink, per arm per module		0.04 0.02	°C/W
T _j	Max. junction temperature			125	°C
T _{stg}	Storage temperature			-40 150	°C
V _{ISOL}	Insulation test voltage,RMS	F=50Hz, 1min		3.0	KV
M1	Mounting torque			5 ± 15%	Nm
M2	Terminal connection torque			12 ± 15%	Nm
W	Weight (Approx.)			650	gm
	File No.			E505556	
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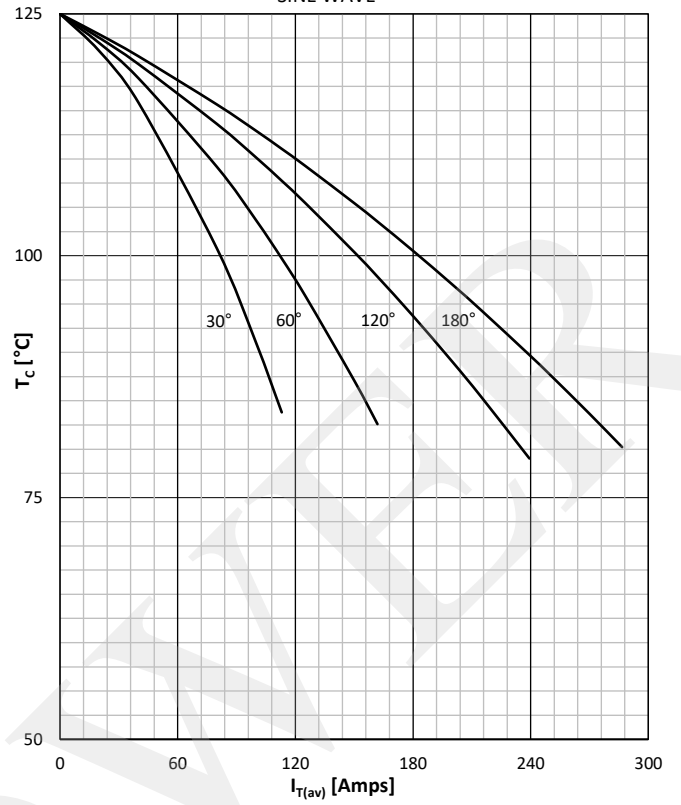
DISSIPATION CHARACTERISTICS

SINE WAVE



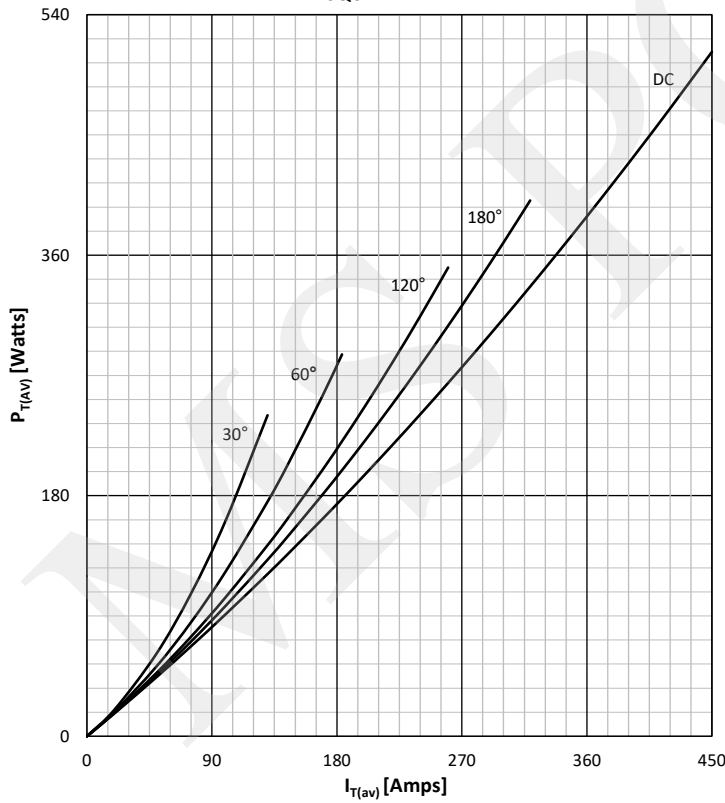
ON STATE CURRENT DERATING CURVE

SINE WAVE



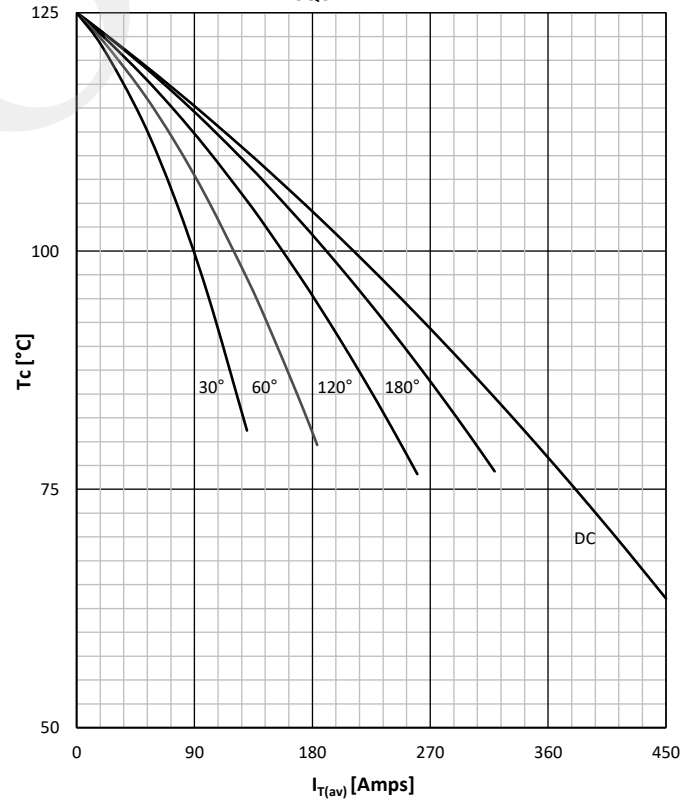
DISSIPATION CHARACTERISTICS

SQUARE WAVE



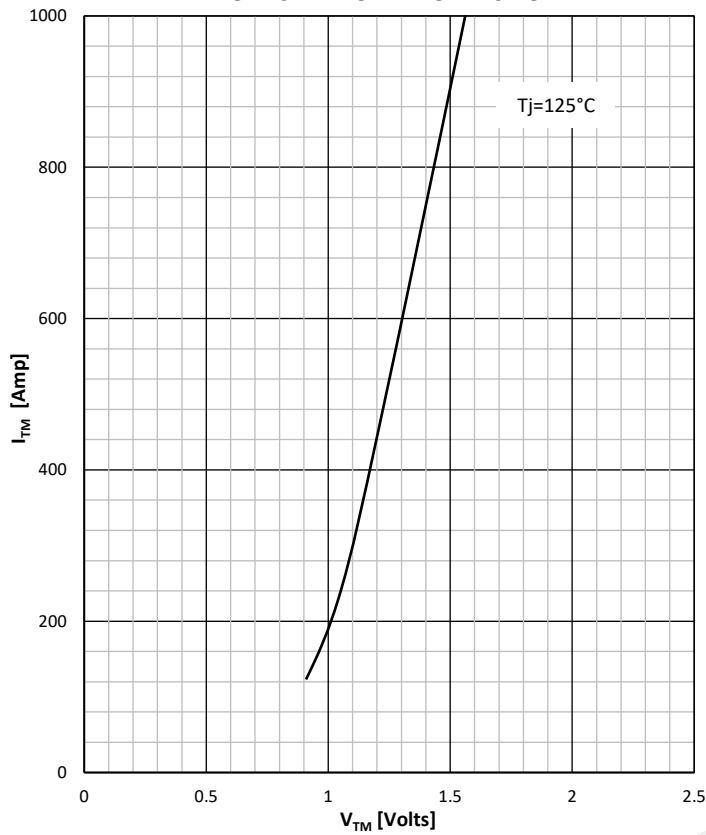
ON STATE CURRENT DERATING CURVE

SQUARE WAVE

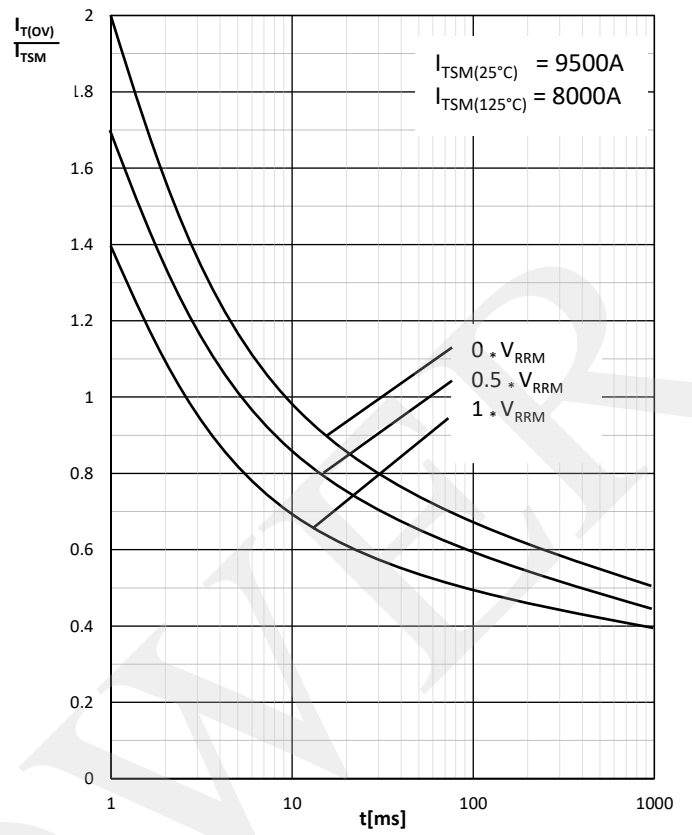


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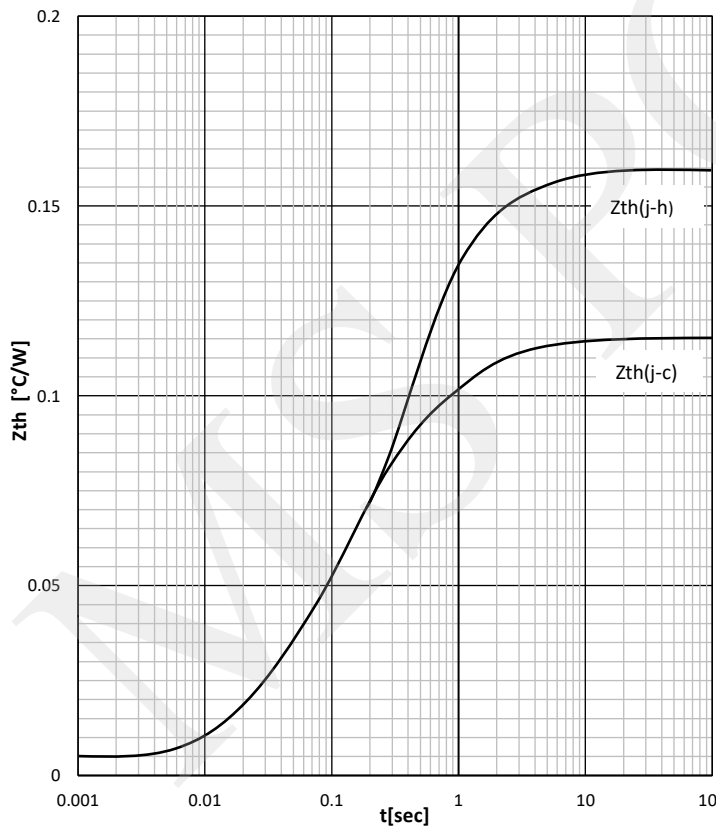
ON -STATE CHARACTERISTIC



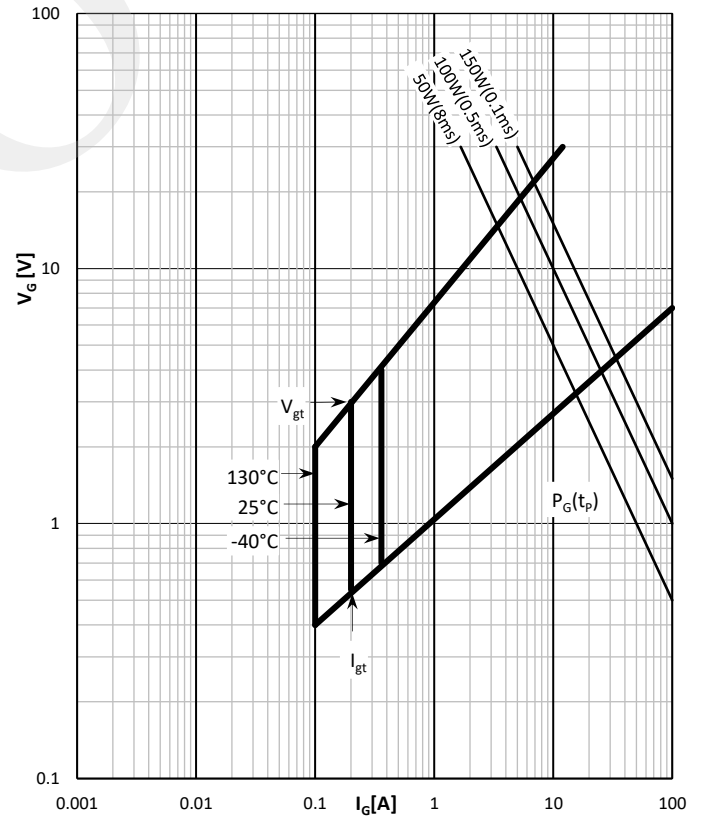
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE, PER ARM



GATE TRIGGER CHARACTERISTICS



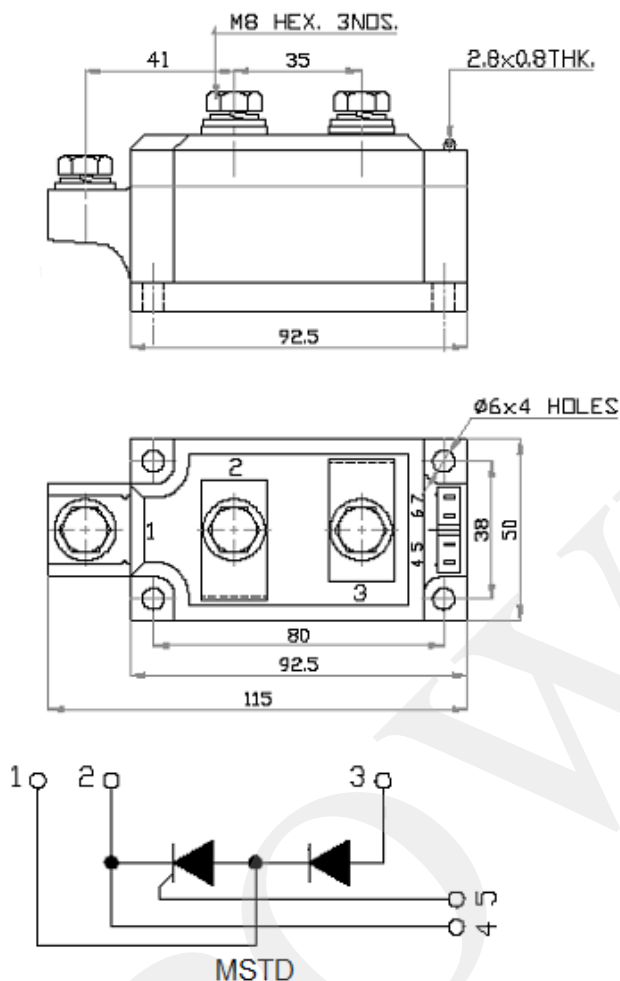
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Outline



MS Power GmbH

Mergenthalerallee 79-81
65760 Eschborn, Germany
Web: www.mspowergroup.com
Mail: info@mspowergroup.de

Sales & Enquiry:

sales@mspowergroup.de

Technical Support:

solution@mspowergroup.de

After sales Service:

service@mspowergroup.de

Phone: +49 (0) 6196/7768 666

Fax: +49 (0) 6196/7757 888



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