



**Key Parameters**

$V_{DRM} / V_{RRM}$	= 3600V
$I_{T(AV)}$	= 321A
$I_{TSM}$	= 6000A
$V_{T(TO)}$	= 1.15V
$r_T$	= 0.8mΩ

**Features**

- Full blocking capability over wide temperature range
- Heat transfer through aluminium oxide ceramic isolated metal baseplate
- Pressure contacts technology for high reliability
- UL Recognized, file no. E505556


**Applications**

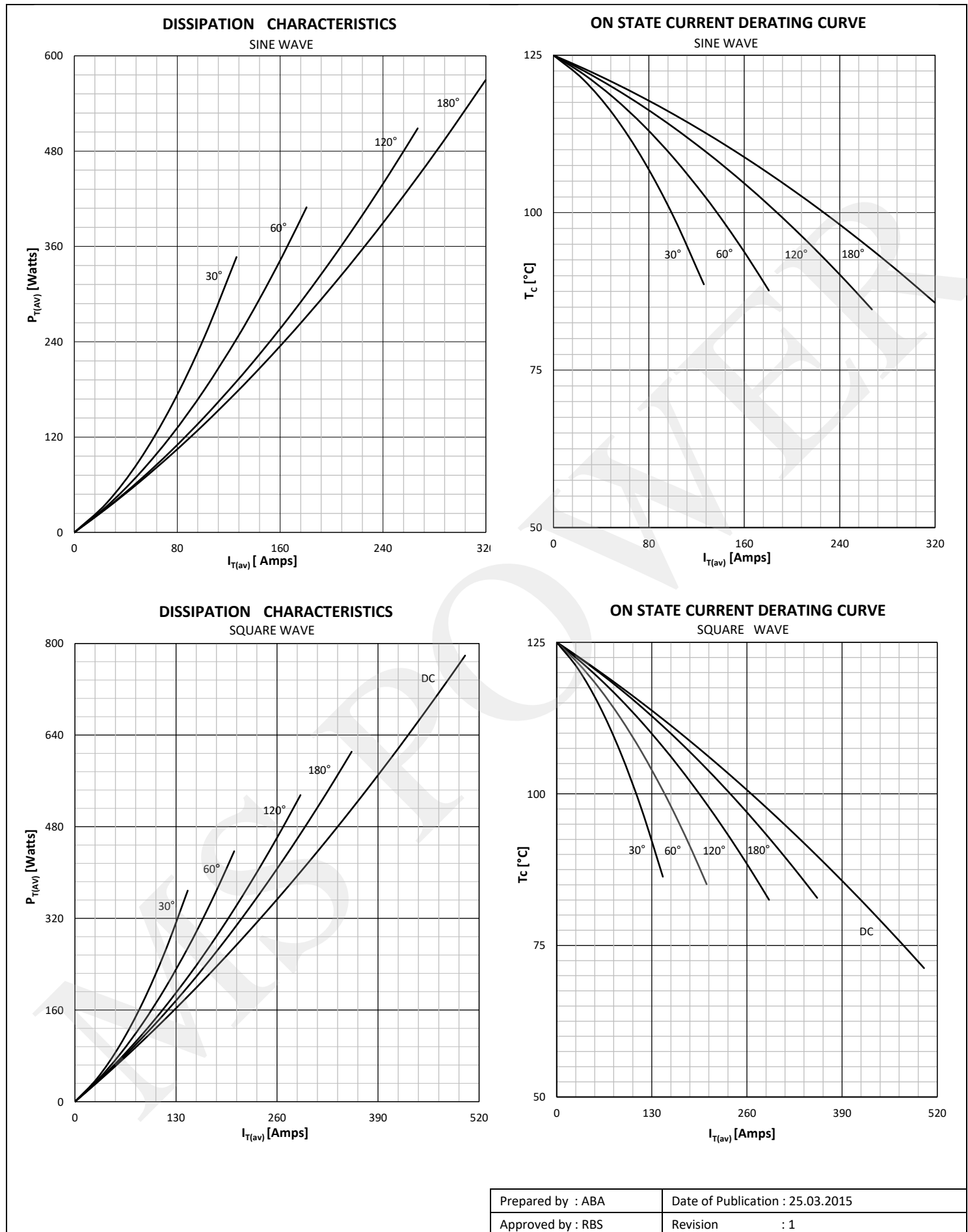
- Power Supplies
- DC motor control
- Controlled Rectifiers
- AC switch

**Ordering Information**

MS	TD	321	K	36
Fixed code	TD- Thyristor- Diode Module	Current Code	Technology K = Pressure Contact Technology	Voltage Code Code X 100 = $V_{DRM}/V_{RRM}$
Order Code MS TD321K36 : 3600V $V_{DRM}, V_{RRM}$ , Thyristor-Diode Module				

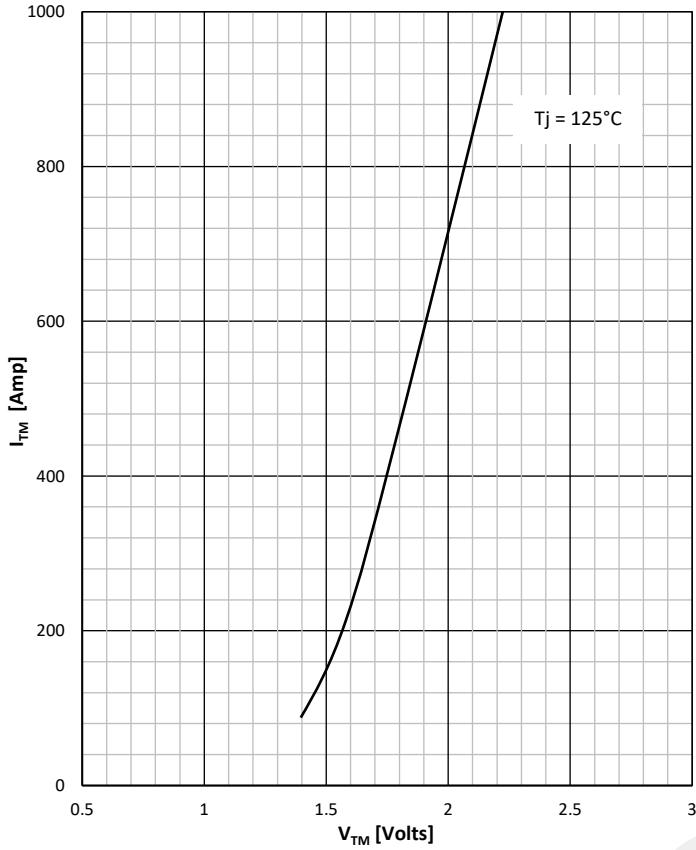
Prepared by : ABA	Date of Publication : 25.03.2015
Approved by : RBS	Revision : 1

Symbol	Characteristic	Conditions	T <sub>j</sub> [°C]	Value	Unit
<b>BLOCKING</b>					
V <sub>RRM</sub>	Repetitive peak reverse voltage		125	3000 - 3600	V
V <sub>RSM</sub>	Non-repetitive peak reverse voltage		125	3100 - 3700	V
V <sub>DRM</sub>	Repetitive peak off-state voltage		125	3000 - 3600	V
I <sub>RRM</sub>	Repetitive peak reverse current	V = V <sub>RRM</sub>	125	200	mA
I <sub>DRM</sub>	Repetitive peak off-state current	V = V <sub>DRM</sub>	125	200	mA
<b>CONDUCTING</b>					
I <sub>T(AV)</sub>	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =85°C		321	A
I <sub>RMS</sub>	RMS on-state current			502	A
I <sub>TSM</sub>	Surge on-state current	Sine wave, 10 ms Without reverse voltage	25	6000	A
			125	5500	A
I <sup>2</sup> t	I <sup>2</sup> t	Sine wave, 10 ms Without reverse voltage	25	180 x 10 <sup>3</sup>	A <sup>2</sup> s
			125	151 x 10 <sup>3</sup>	A <sup>2</sup> s
V <sub>T</sub>	On-state voltage	On-state current = 785A	25	2.20	V
V <sub>T(TO)</sub>	Threshold voltage		125	1.15	V
r <sub>T</sub>	On-state slope resistance		125	0.8	mΩ
<b>SWITCHING</b>					
di/dt	Critical rate of rise of on-state current	Non-repetitive f=1Hz, I <sub>GM</sub> =2.0A, di <sub>G</sub> /dt>1.0A/μs, I <sub>TM</sub> =2I <sub>TAV</sub> , V <sub>D</sub> =67%V <sub>DRM</sub>	125	400	A/μs
dv/dt	Critical rate of rise of off-state voltage	V <sub>DR</sub> = 67%V <sub>DRM</sub>	125	1000	V/μs
<b>GATE</b>					
I <sub>gt</sub>	Gate trigger current	V <sub>D</sub> =6V	25	250	mA
V <sub>gt</sub>	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	300	mA
I <sub>L</sub>	Latching current	V <sub>D</sub> =6V	25	1500	mA
<b>MOUNTING</b>					
R <sub>th(j-c)</sub>	Thermal impedance, sin 180°	Junction to case, per arm per module		0.068 0.034	°C/W
R <sub>th(j-c)</sub>	Thermal impedance, rec120°	Junction to case, per arm per module		0.078 0.039	°C/W
R <sub>th(c-h)</sub>	Thermal impedance	Case to heatsink, per arm per module		0.02 0.01	°C/W
T <sub>j</sub>	Max. junction temperature			125	°C
T <sub>stg</sub>	Storage temperature			-40 .... 150	°C
V <sub>ISOL</sub>	Insulation test voltage,RMS	F=50Hz, 1min		3.0	KV
M1	Mounting torque			6 ± 15%	Nm
M2	Terminal connection torque			12 ± 15%	Nm
W	Weight (Approx.)			1450	gm
	File No.			E505556	
			Prepared by : ABA	Date of Publication : 25.03.2015	
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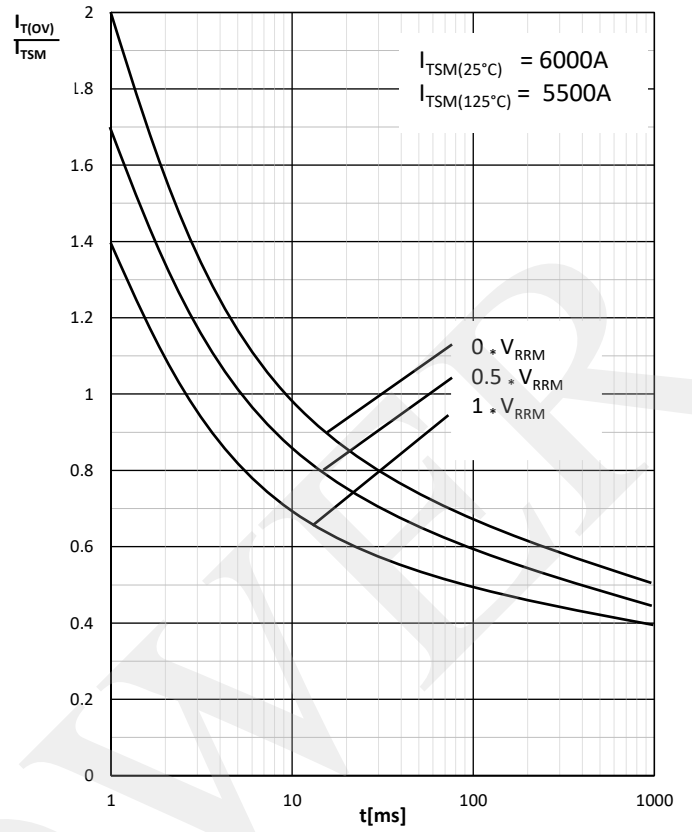


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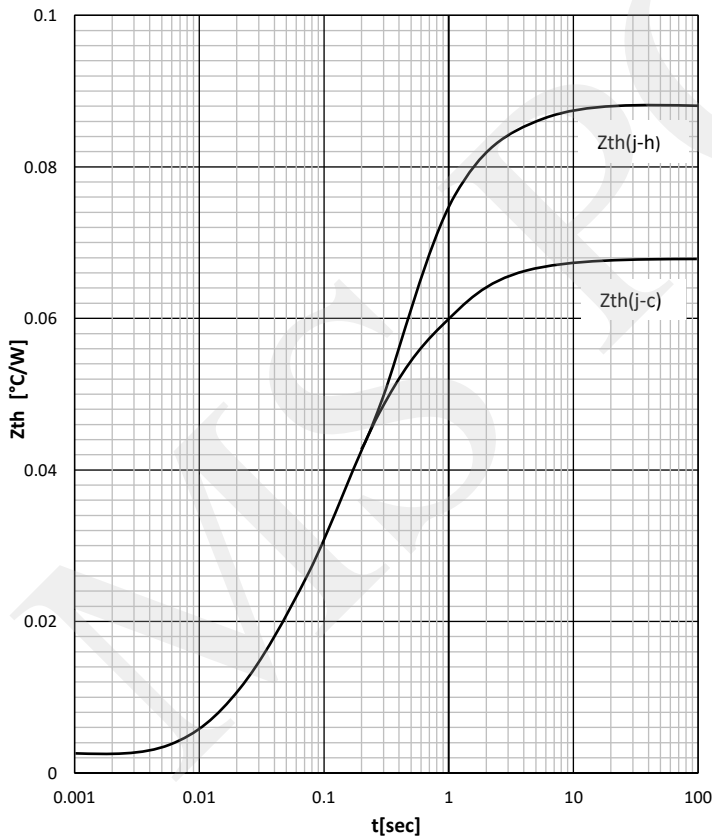
ON -STATE CHARACTERISTIC



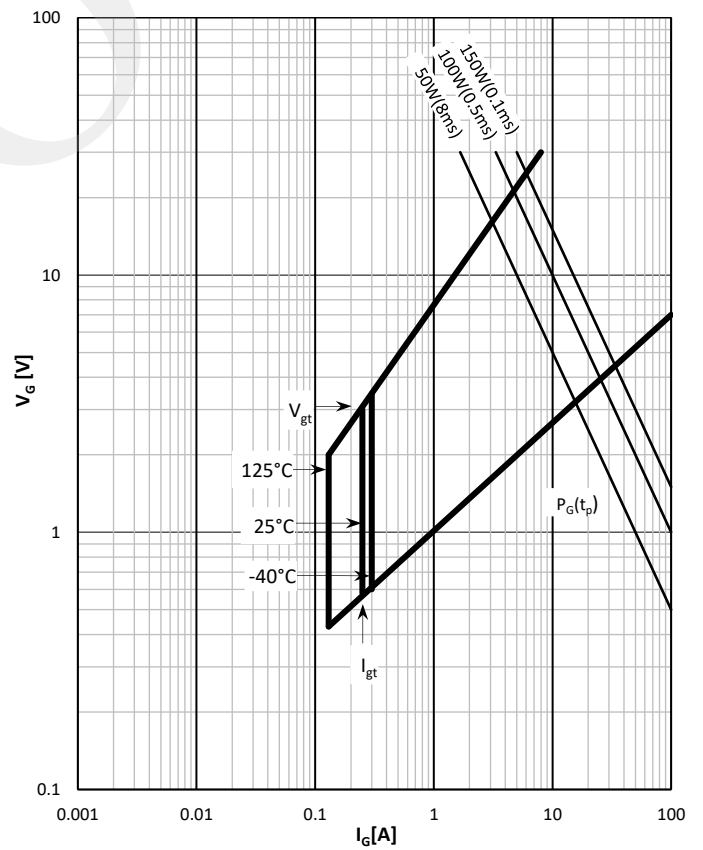
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE, PER ARM



GATE TRIGGER CHARACTERISTICS



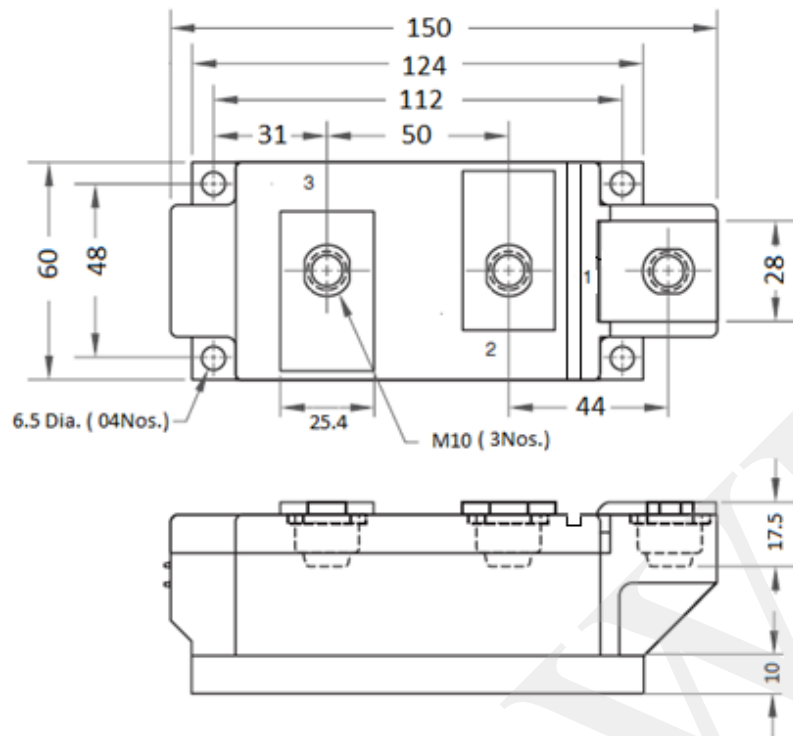
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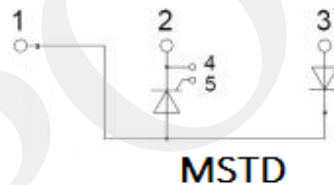
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## Outline



Note : All dimensions are in mm.  
Tolerance :  $\pm 0.5\text{mm}$



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