



**Key Parameters**

$V_{DRM} / V_{RRM}$	= 2000V
$I_{T(AV)}$	= 1110A
$I_{TSM}$	= 19.2kA
$V_{T(TO)}$	= 1.207V
$r_T$	= 0.342mΩ

**Features**

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator
- Distributed gate

**Applications**

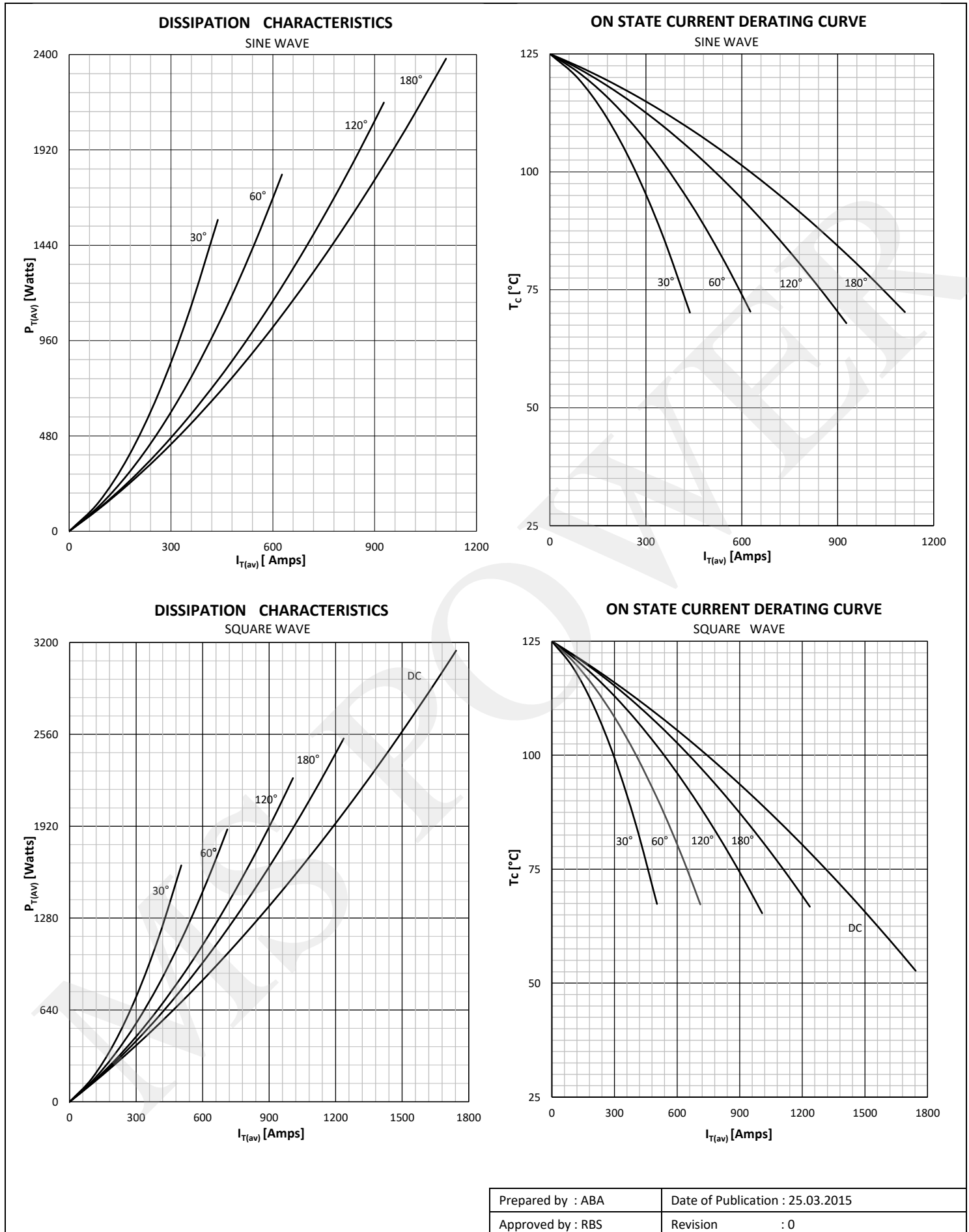
- Battery Chargers
- Medical Equipment
- UPS
- Power Supplies
- Motor control
- Transportation
- Induction Heating
- Welding

**Ordering Information**

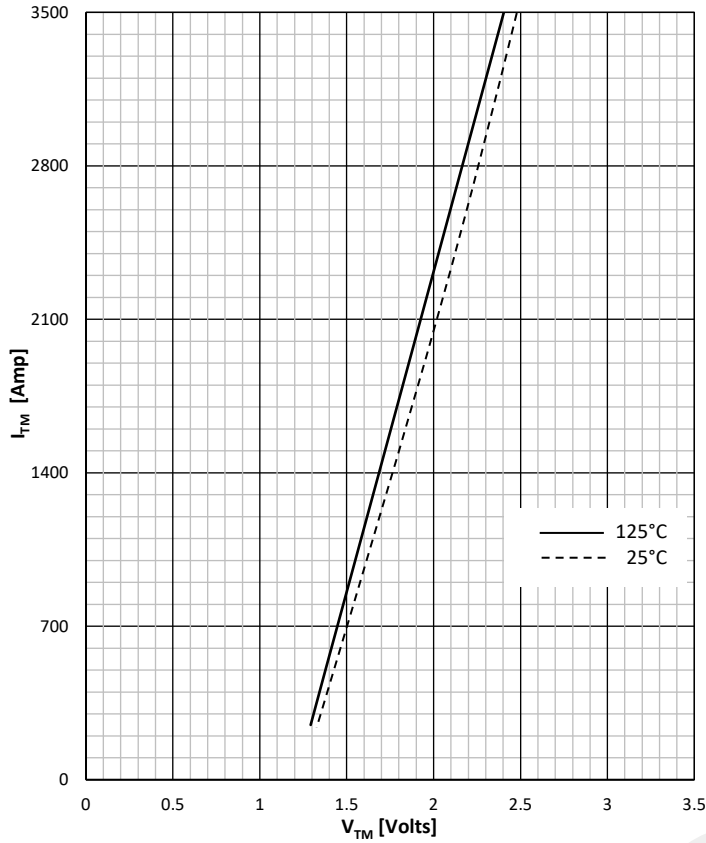
MS TF	1110	C	XX	H	2J
Fast Switching Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = $V_{DRM}/V_{RRM}$	Reapplied dv/dt H = 400V/μsec	Turn Off time code 2J = 50μsec 4J = 100μsec
Order Code MS TF1110C20H2J – 2000V $V_{DRM}, V_{RRM}, T_q=50\mu\text{sec}$ , 26mm clamp height capsule					

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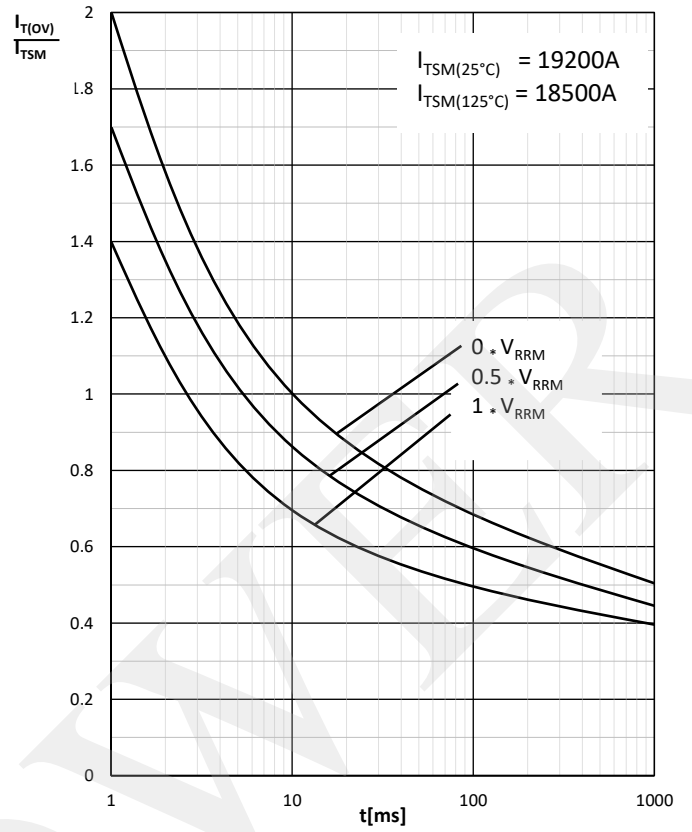
Symbol	Characteristic	Conditions	T <sub>j</sub> [°C]	Value	Unit
<b>BLOCKING</b>					
V <sub>RRM</sub>	Repetitive peak reverse voltage		125	1400 - 2000	V
V <sub>RSM</sub>	Non-repetitive peak reverse voltage		125	1500 - 2100	V
V <sub>DRM</sub>	Repetitive peak off-state voltage		125	1400 - 2000	V
I <sub>RRM</sub>	Repetitive peak reverse current	V = V <sub>RRM</sub>	125	90	mA
I <sub>DRM</sub>	Repetitive peak off-state current	V = V <sub>DRM</sub>	125	90	mA
<b>CONDUCTING</b>					
I <sub>T(AV)</sub>	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =70°C, Double side cooled 180° sin ,50 Hz, T <sub>c</sub> =77°C, Double side cooled		1110 1000	A
I <sub>RMS</sub>	RMS on-state current	T <sub>c</sub> =70°C, Double side cooled		1743	A
I <sub>TSM</sub>	Surge on-state current	Sine wave, 10 ms Without reverse voltage	25	19200	A
			125	18500	A
I <sup>2</sup> t	I <sup>2</sup> t	Sine wave, 10 ms Without reverse voltage	25	1843 x 10 <sup>3</sup>	A <sup>2</sup> s
			125	1711 x 10 <sup>3</sup>	A <sup>2</sup> s
V <sub>T</sub>	On-state voltage	On-state current = 2000A	125	1.90	V
V <sub>T(TO)</sub>	Threshold voltage		125	1.207	V
r <sub>T</sub>	On-state slope resistance		125	0.342	mΩ
<b>SWITCHING</b>					
di/dt	Critical rate of rise of on-state current	Repetitive	125	250	A/μs
dv/dt	Critical rate of rise of off-state voltage	V <sub>DR</sub> = 67%V <sub>DRM</sub>	125	400	V/μs
T <sub>q</sub>	Circuit commutated turn off time	I <sub>TM</sub> =1000A, -di <sub>F</sub> /dt = 60A/μs, V <sub>R</sub> = 100V, t <sub>p</sub> =1000μs Reapplied dv/dt = 400V/μs, V <sub>DR</sub> = 80%V <sub>DRM</sub>	125	50 - 100	μs
<b>GATE</b>					
I <sub>gt</sub>	Gate trigger current	V <sub>D</sub> =6V	25	200	mA
V <sub>gt</sub>	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	600	mA
I <sub>L</sub>	Latching current	V <sub>D</sub> =6V	25	1000	mA
<b>MOUNTING</b>					
R <sub>th(j-c)</sub>	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.023	°C/W
R <sub>th(j-c)</sub>	Thermal impedance, rec120°	Junction to case, Double side cooled		0.026	°C/W
R <sub>th(c-h)</sub>	Thermal impedance	Case to heatsink, Double side cooled		0.0075	°C/W
T <sub>j</sub>	Max. junction temperature			125	°C
T <sub>stg</sub>	Storage temperature			-40 ... 125	°C
M	Clamping Force			22 - 24	kN
W	Weight (Approx.)			500	gm
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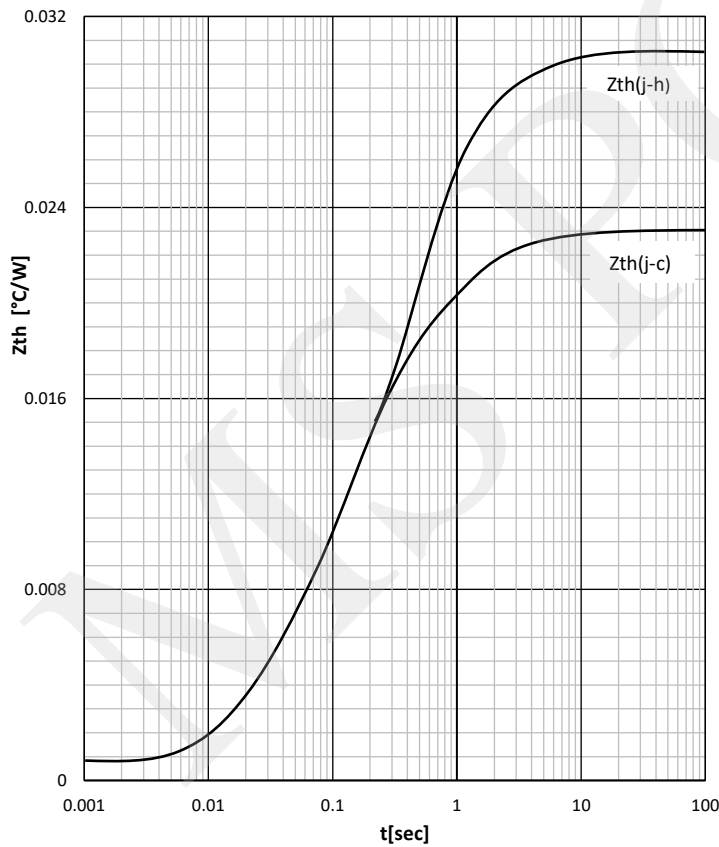
ON-STATE CHARACTERISTIC



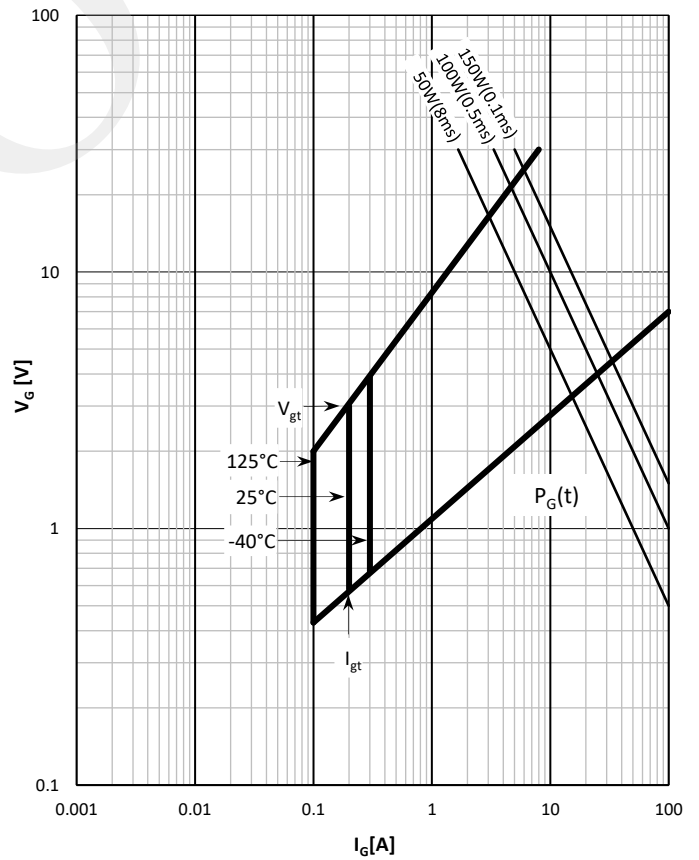
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE, PER ARM



GATE TRIGGER CHARACTERISTICS



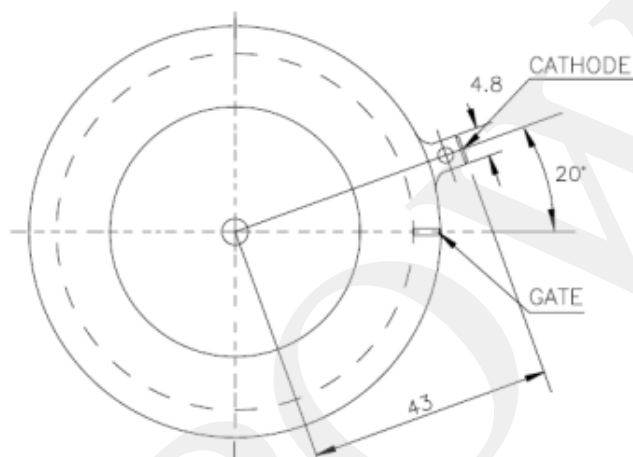
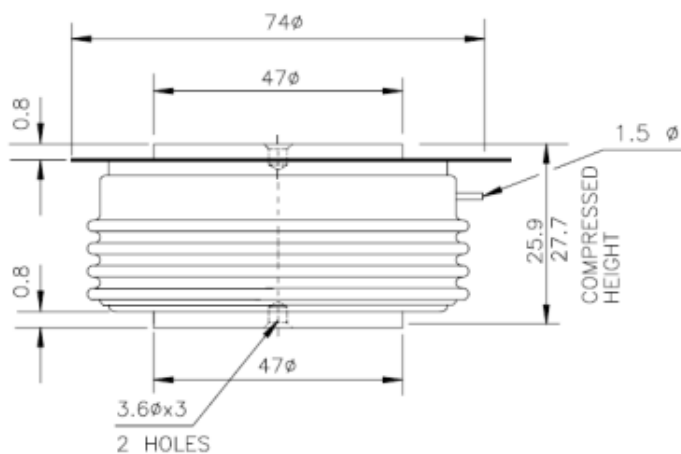
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