



Key Parameters

V_{DRM} / V_{RRM}	= 1200V
$I_{T(AV)}$	= 1290A
I_{TSM}	= 22kA
$V_{T(TO)}$	= 1.45V
r_T	= 0.285mΩ

Features

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator
- Distributed gate

Applications

- Battery Chargers
- Medical Equipment
- UPS
- Power Supplies
- Motor control
- Transportation
- Induction Heating
- Welding

Ordering Information

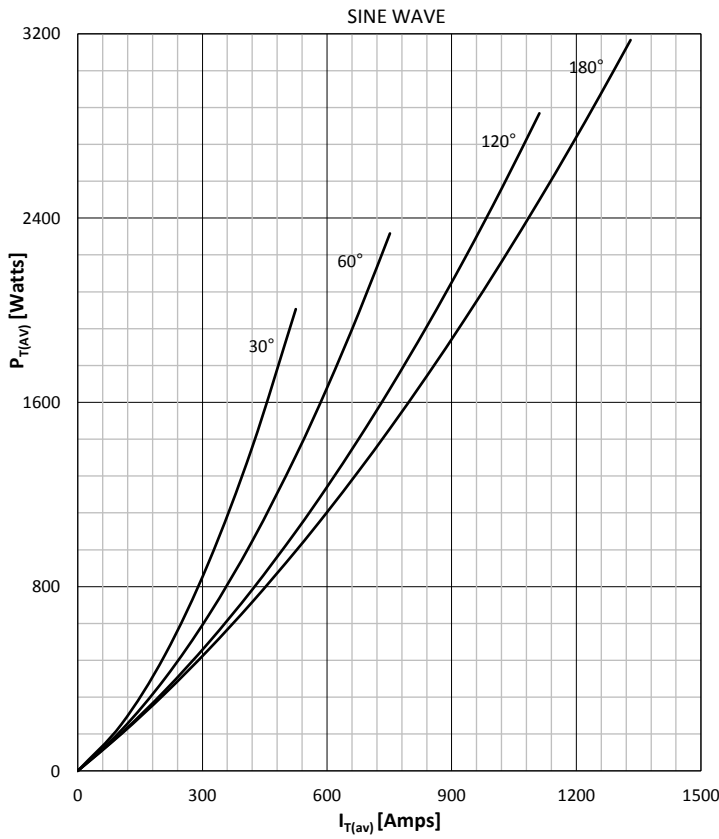
MS TF	1290	C	XX	L	A
Fast Switching Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V_{DRM}/V_{RRM}	Reapplied dv/dt L = 20V/μsec	Turn Off time code A = 10μsec B = 12μsec E = 15μsec

Order Code MS TF1290C12LA – 1200V V_{DRM}, V_{RRM} , $T_q=10\mu\text{sec}$, 26mm clamp height capsule

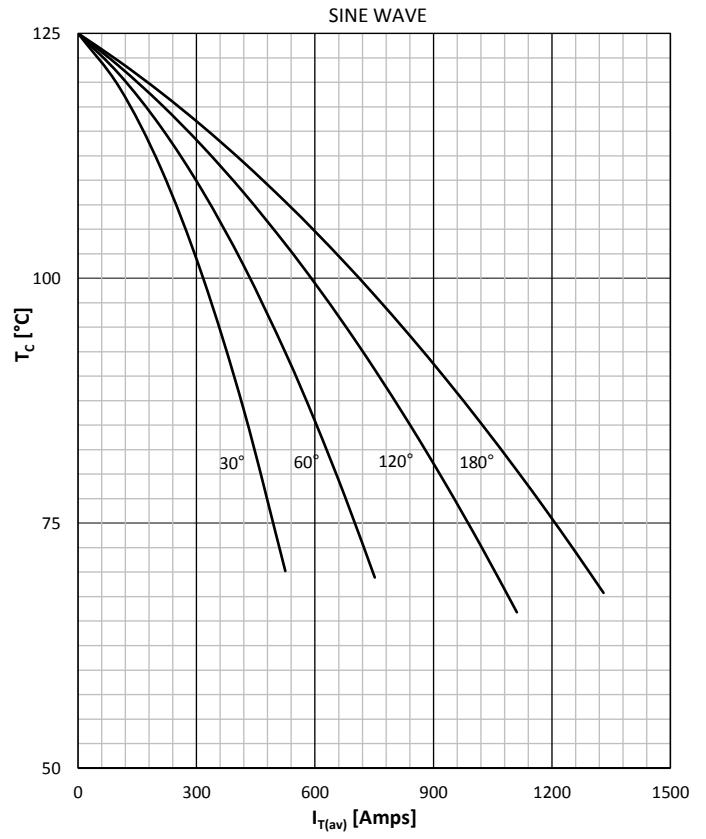
Prepared by : ABA	Date of Publication : 25.03.2015
Approved by : RBS	Revision : 0

Symbol	Characteristic	Conditions	T _j [°C]	Value	Unit
BLOCKING					
V _{RRM}	Repetitive peak reverse voltage		125	800 - 1200	V
V _{RSM}	Non-repetitive peak reverse voltage		125	900 - 1300	V
V _{DRM}	Repetitive peak off-state voltage		125	800 - 1200	V
I _{RRM}	Repetitive peak reverse current	V = V _{RRM}	125	150	mA
I _{DRM}	Repetitive peak off-state current	V = V _{DRM}	125	150	mA
CONDUCTING					
I _{T(AV)}	Mean on state current	180° sin ,50 Hz, T _c =70°C, Double side cooled 180° sin ,50 Hz, T _c =68°C, Double side cooled		1290 1331	A
I _{RMS}	RMS on-state current	T _c =68°C, Double side cooled		2089	A
I _{TSM}	Surge on-state current	Sine wave, 10 ms Without reverse voltage	25	22.0	kA
			125	20.0	kA
I ² t	I ² t	Sine wave, 10 ms Without reverse voltage	25	2420 x 10 ³	A ² s
			125	2000 x 10 ³	A ² s
V _T	On-state voltage	On-state current = 2000A	125	2.02	V
V _{T(TO)}	Threshold voltage		125	1.45	V
r _T	On-state slope resistance		125	0.285	mΩ
SWITCHING					
di/dt	Critical rate of rise of on-state current	Repetitive, V _D = 67%V _{DRM} , I _{FG} =2A, tr≤0.5μs	125	1000	A/μs
dv/dt	Critical rate of rise of off-state voltage	V _{DR} = 80%V _{DRM}	125	200	V/μs
T _q	Circuit commutated turn off time	I _{TM} =1000A, -di _F /dt = 60A/μs, V _R = 50V, t _p =1000μs Reapplied dv/dt = 20V/μs, V _{DR} = 33%V _{DRM}	125	10 - 15	μs
GATE					
I _{gt}	Gate trigger current	V _D =6V	25	300	mA
V _{gt}	Gate trigger voltage	V _D =6V	25	3.0	V
I _H	Holding current	V _D =6V, gate open circuit	25	1000	mA
I _L	Latching current	V _D =6V	25	1200	mA
MOUNTING					
R _{th(j-c)}	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.018	°C/W
R _{th(j-c)}	Thermal impedance, rec120°	Junction to case, Double side cooled		0.021	°C/W
R _{th(c-h)}	Thermal impedance	Case to heatsink, Double side cooled		0.004	°C/W
T _j	Max. junction temperature			125	°C
T _{stg}	Storage temperature			-40 ... 125	°C
M	Clamping Force			20 - 24	kN
W	Weight (Approx.)			500	gm
			Prepared by : ABA	Date of Publication : 25.03.2015	
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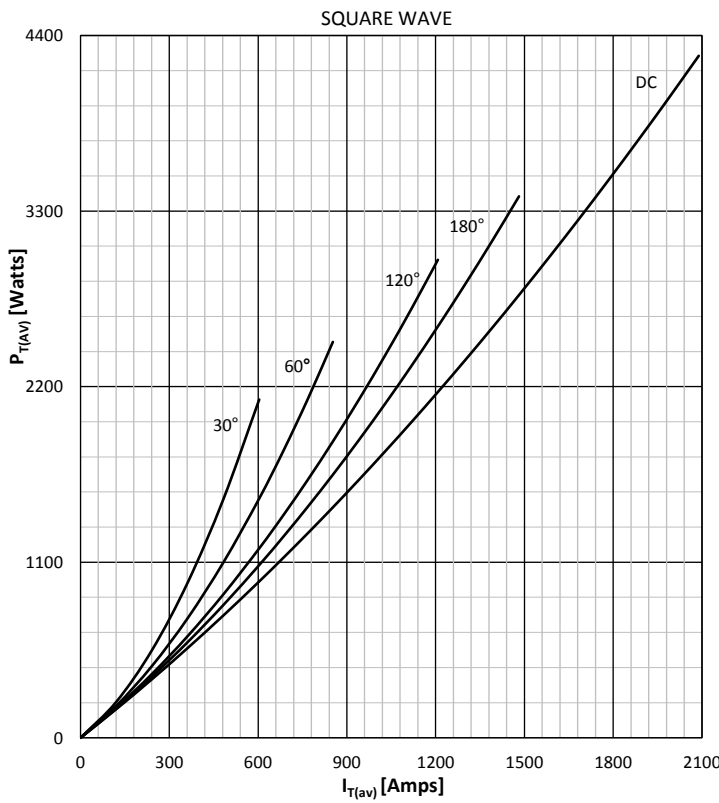
DISSIPATION CHARACTERISTICS



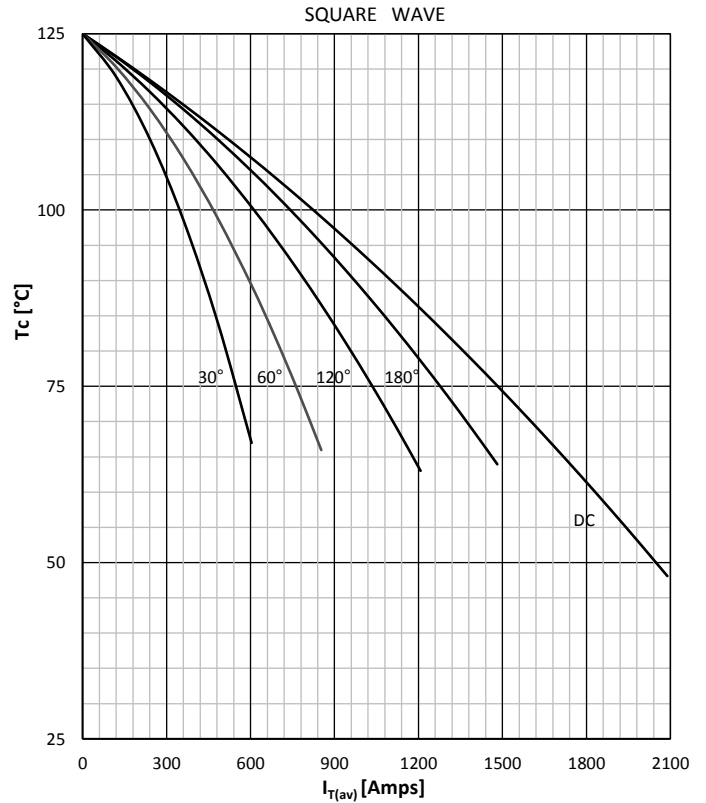
ON STATE CURRENT DERATING CURVE



DISSIPATION CHARACTERISTICS

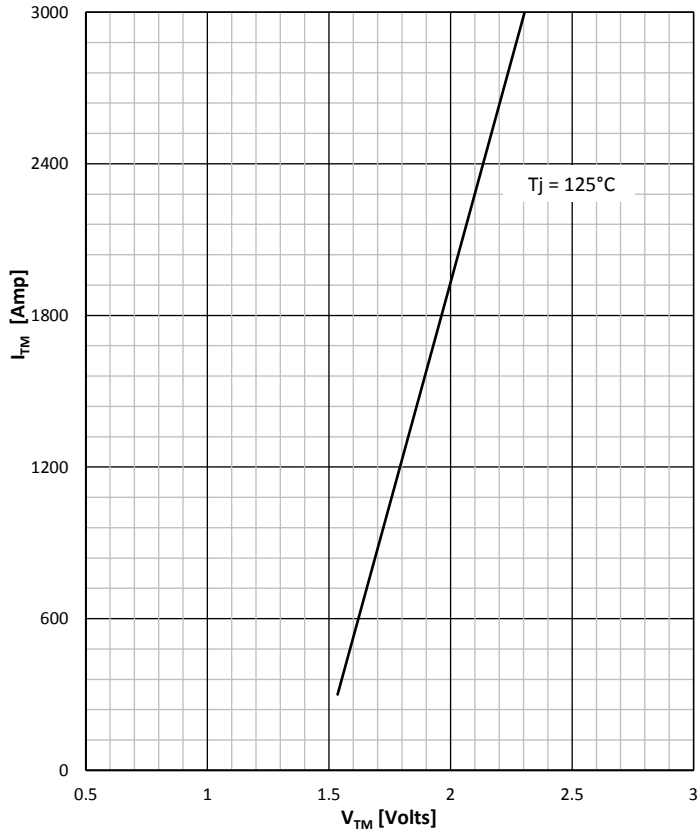


ON STATE CURRENT DERATING CURVE

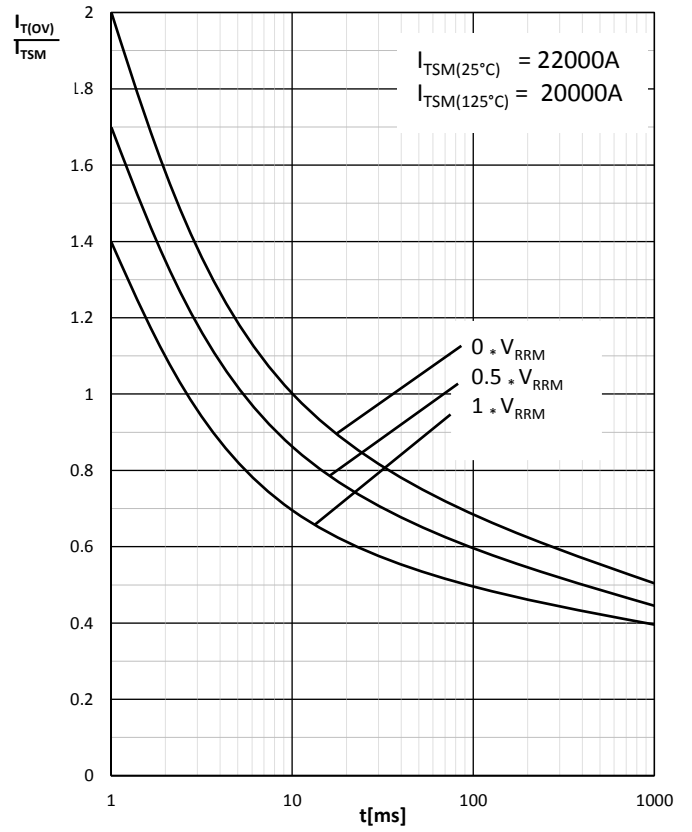


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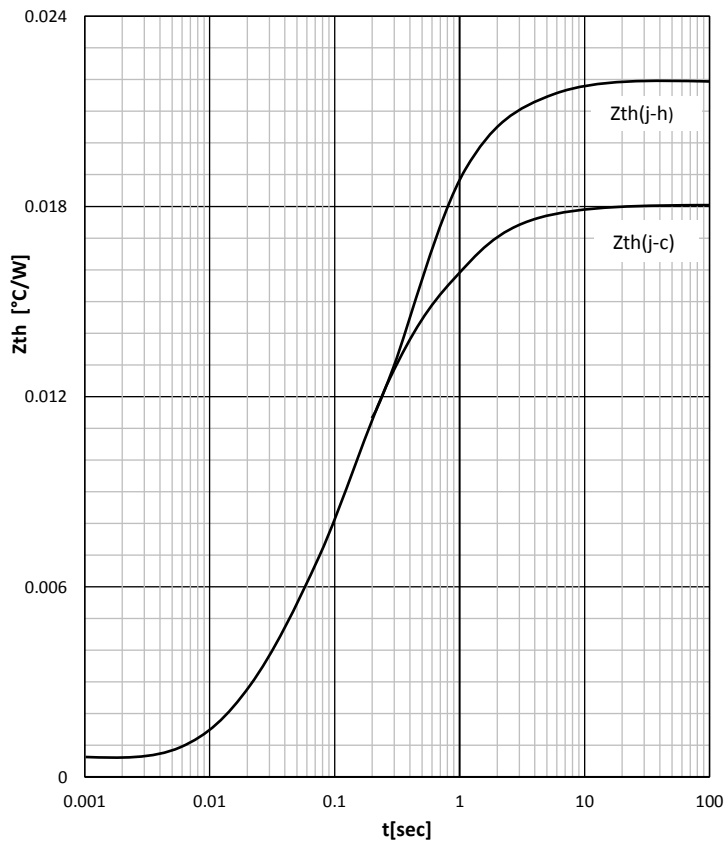
ON STATE CHARACTERISTIC



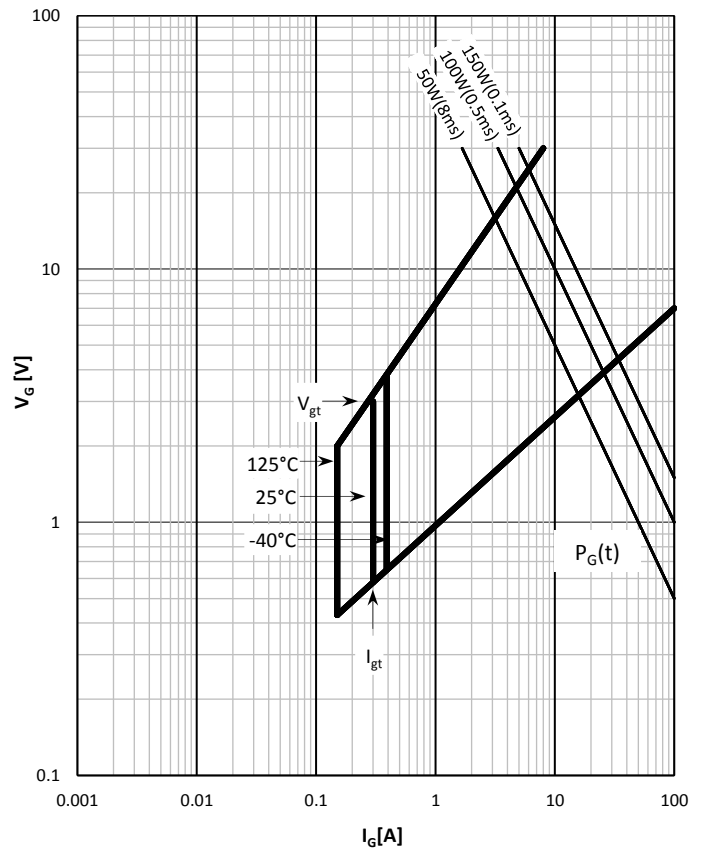
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE, PER ARM



GATE TRIGGER CHARACTERISTICS



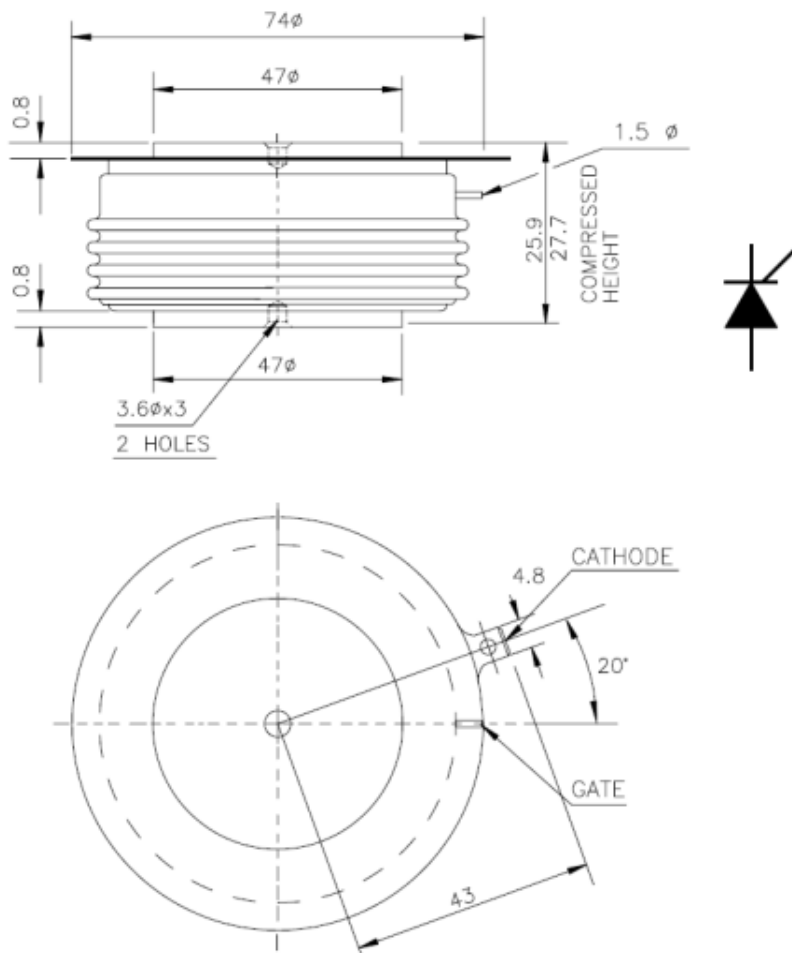
Prepared by : ABA

Date of Publication : 25.03.2015

Approved by : RBS

Revision : 0

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