



Key Parameters

V_{DRM} / V_{RRM}	= 2000V
$I_{T(AV)}$	= 1448A
I_{TSM}	= 19.0kA
$V_{T(TO)}$	= 1.30V
r_T	= 0.25mΩ

Features

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator
- Distributed gate

Applications

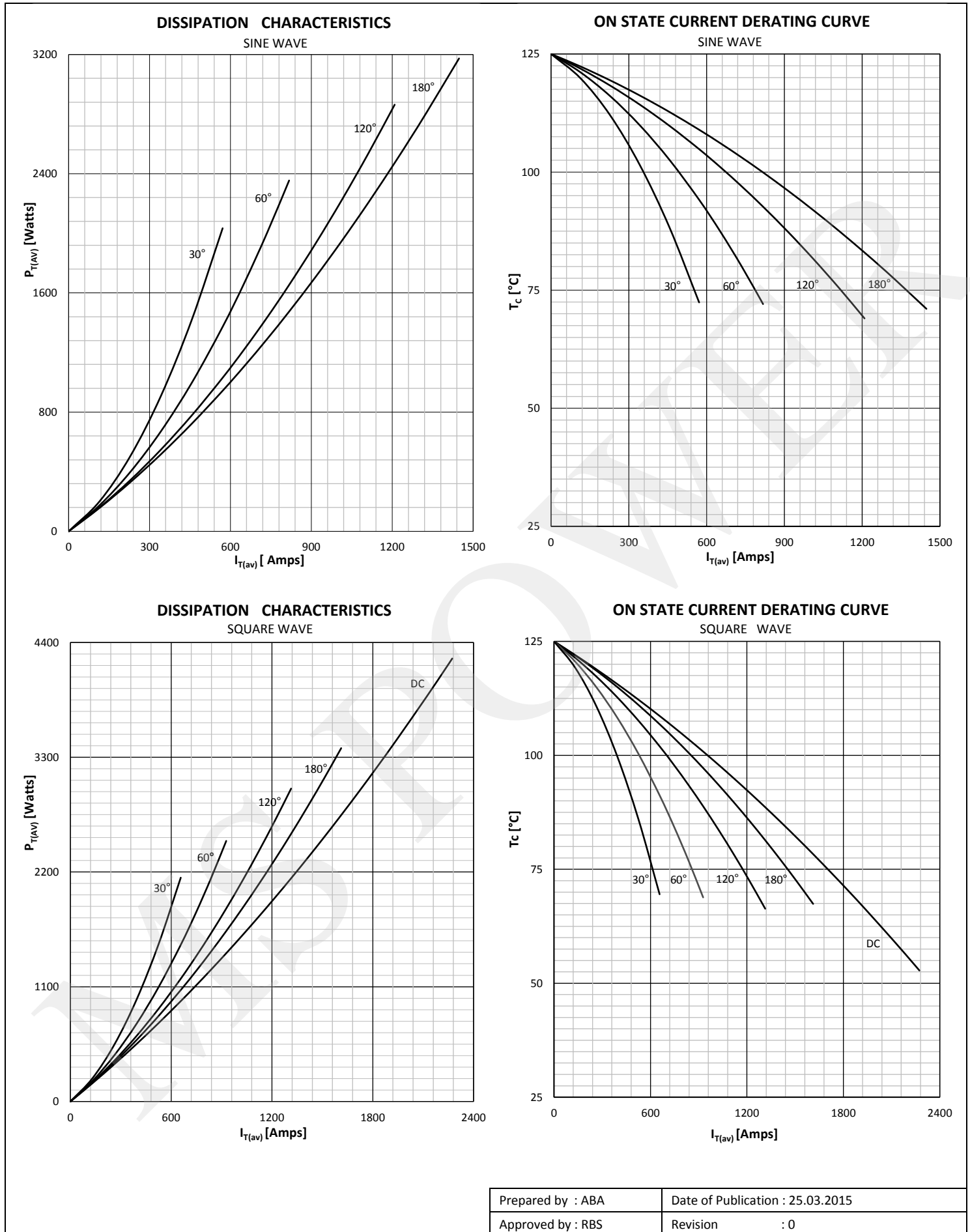
- Battery Chargers
- Medical Equipment
- UPS
- Power Supplies
- Motor control
- Transportation
- Induction Heating
- Welding

Ordering Information

MS TF	1448	C	XX	F	2K
Fast Switching Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V_{DRM}/V_{RRM}	Reapplied dv/dt F = 200V/μsec	Turn Off time code 2K = 40μsec 2J = 50μsec 2N = 60μsec 2G = 70μsec
Order Code MSKK1448C20F2K – 2000V V_{DRM}, V_{RRM} , $T_q=40\mu\text{sec}$, 26mm clamp height capsule					

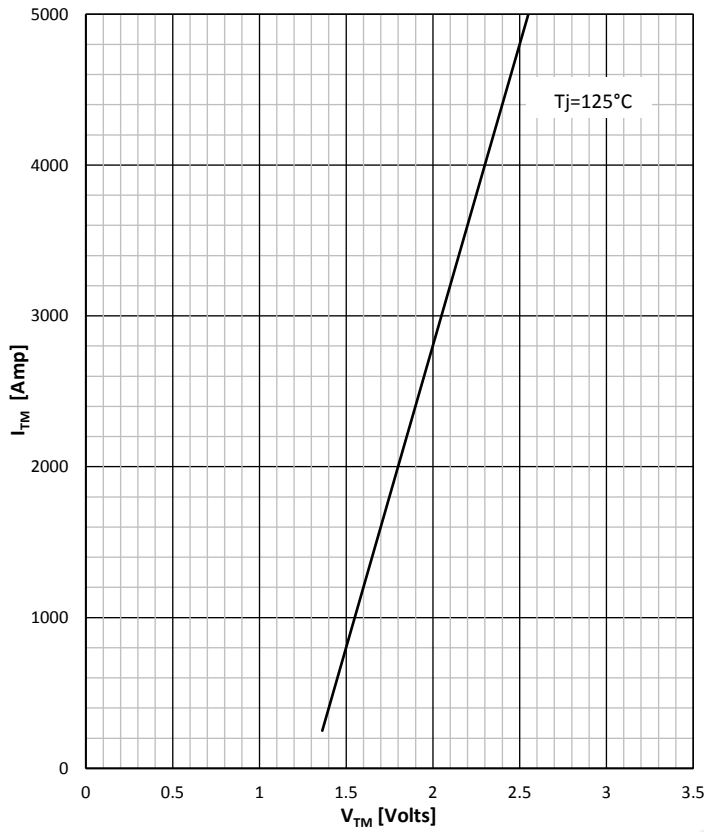
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Symbol	Characteristic	Conditions	T _j [°C]	Value	Unit
BLOCKING					
V _{RRM}	Repetitive peak reverse voltage		125	1800 - 2000	V
V _{RSM}	Non-repetitive peak reverse voltage		125	1900 - 2100	V
V _{DRM}	Repetitive peak off-state voltage		125	1800 - 2000	V
I _{RRM}	Repetitive peak reverse current	V = V _{RRM}	125	150	mA
I _{DRM}	Repetitive peak off-state current	V = V _{DRM}	125	150	mA
CONDUCTING					
I _{T(AV)}	Mean on state current	180° sin ,50 Hz, T _c =70°C, Double side cooled		1448	A
I _{RMS}	RMS on-state current	T _c =70°C, Double side cooled		2273	A
I _{TSM}	Surge on-state current	Sine wave, 10 ms Without reverse voltage	25	19000	A
			125	17000	A
I ² t	I ² t	Sine wave, 10 ms Without reverse voltage	25	1805 x 10 ³	A ² s
			125	1445 x 10 ³	A ² s
V _T	On-state voltage	On-state current = 2000A	125	1.80	V
V _{T(TO)}	Threshold voltage		125	1.30	V
r _T	On-state slope resistance		125	0.25	mΩ
SWITCHING					
di/dt	Critical rate of rise of on-state current	Repetitive, V _{DR} = 80%V _{DRM} , I _{FG} =2A, t _r < 0.5μs	125	1000	A/μs
dv/dt	Critical rate of rise of off-state voltage	V _{DR} = 80%V _{DRM}	125	200	V/μs
T _q	Circuit commutated turn off time	I _{TM} =1000A, -di _f /dt = 60A/μs, V _R = 50V, t _p =1000μs Reapplied dv/dt = 200V/μs, V _{DR} = 33%V _{DRM}	125	40 - 70	μs
GATE					
I _{gt}	Gate trigger current	V _D =6V	25	300	mA
V _{gt}	Gate trigger voltage	V _D =6V	25	3.0	V
I _H	Holding current	V _D =6V, gate open circuit	25	1000	mA
I _L	Latching current	V _D =6V	25	1200	mA
MOUNTING					
R _{th(j-c)}	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.017	°C/W
R _{th(j-c)}	Thermal impedance, rec120°	Junction to case, Double side cooled		0.019	°C/W
R _{th(c-h)}	Thermal impedance	Case to heatsink, Double side cooled		0.005	°C/W
T _j	Max. junction temperature			125	°C
T _{stg}	Storage temperature			-40 125	°C
M	Clamping Force			22 - 24	kN
W	Weight (Approx.)			500	gm
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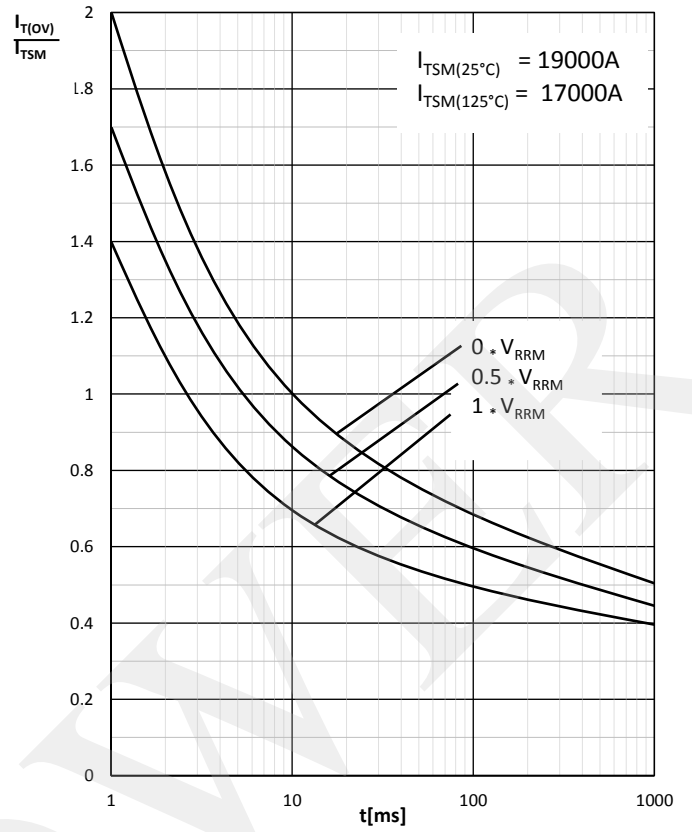


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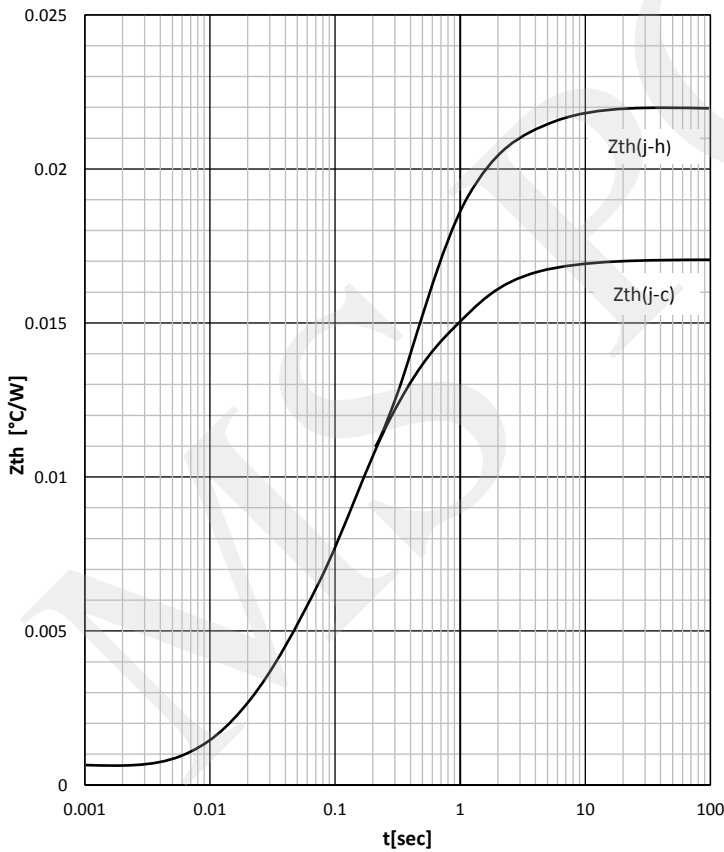
ON -STATE CHARACTERISTIC



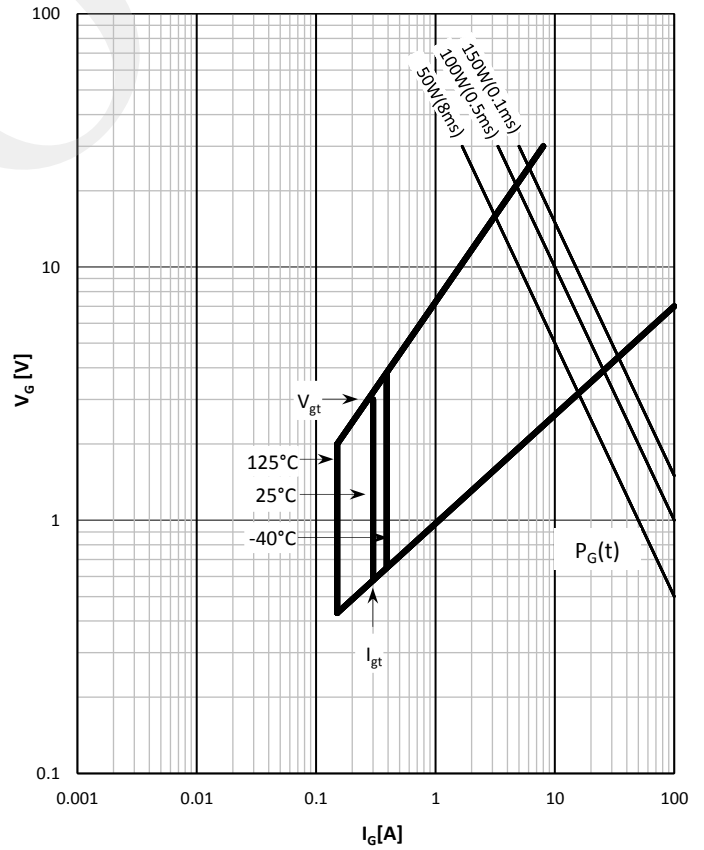
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE, PER ARM



GATE TRIGGER CHARACTERISTICS



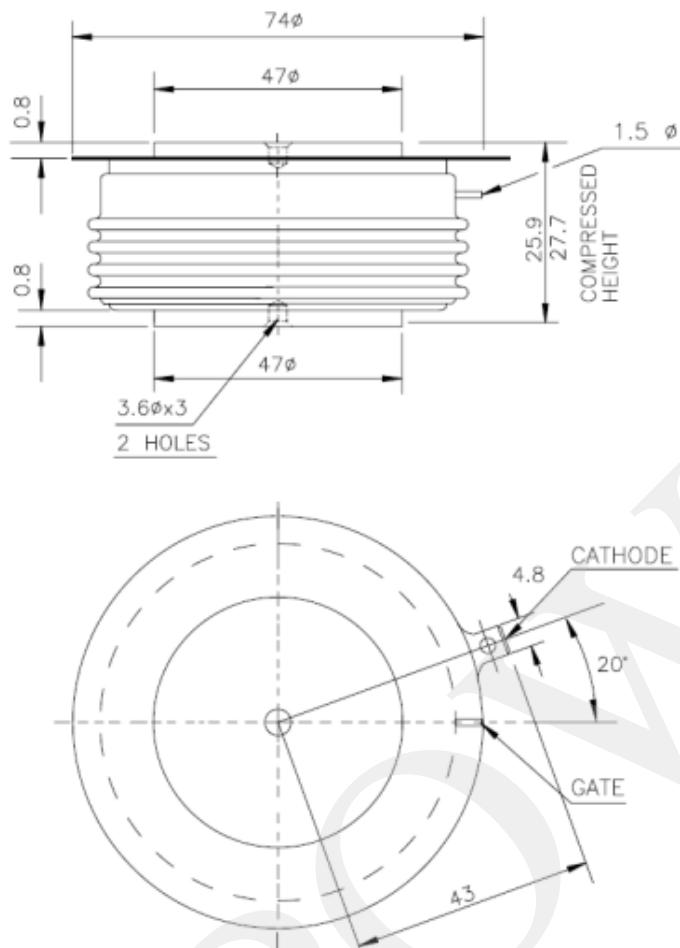
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Outline



MS Power GmbH

Mergenthalerallee 79-81
65760 Eschborn, Germany
Web: www.mspowergroup.com
Mail: info@mspowergroup.de

Sales & Enquiry:

sales@mspowergroup.de

Technical Support:

solution@mspowergroup.de

After sales Service:

service@mspowergroup.de

Phone: +49 (0) 6196/7768 666

Fax: +49 (0) 6196/7757 888



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