## **MS TF2100**





## **Key Parameters**

 $\begin{array}{lll} V_{DRM} \, / \, V_{RRM} & = 2000 V \\ I_{T(AV)} & = 2100 A \\ I_{TSM} & = 34 k A \\ V_{T(TO)} & = 1.271 V \\ r_{T} & = 0.255 m \Omega \end{array}$ 

#### **Features**

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator
- Distributed gate

## **Applications**

- Battery Chargers
- Medical Equipment
- UPS
- Power Supplies
- Motor control
- Transportation
- Induction Heating
- Welding

### **Ordering Information**

MS TF	2100	С	ХX	Н	2N
Fast Switching Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub>	Reapplied dv/dt H =400V/µsec	Turn Off time code 2N = 60µsec 4J = 100µsec
Order Code MS TF2100C20H2N – 2000V VDRM, VRRM, To=60usec, 26mm clamp height capsule					

True Code 1913 11 2 100 C20 1219 - 2000 V VDRM, VRRM, 19-00µsec, 2011111 claimp fieight capsule

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# Technical Information Fast Switching Thyristor

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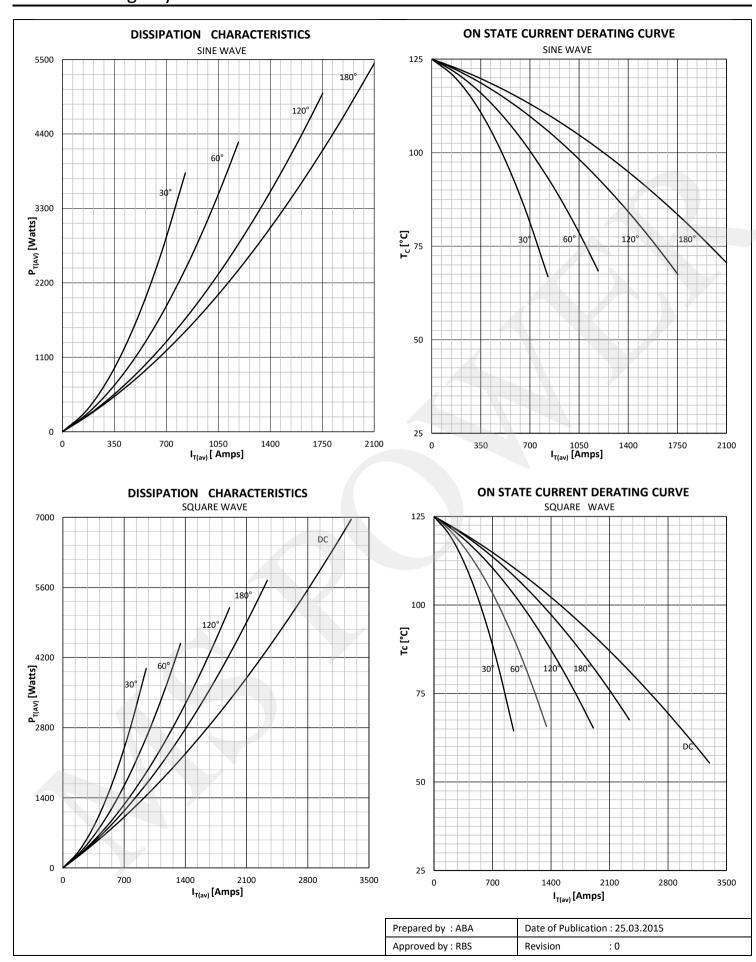


Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	NG				
V RRM	Repetitive peak reverse voltage		125	1400 - 2000	V
V RSM	Non-repetitive peak reverse voltage		125	1500 - 2100	V
V DRM	Repetitive peak off-state voltage		125	1400 - 2000	V
I RRM	Repetitive peak reverse current	V= V RRM	125	150	mA
I DRM	Repetitive peak off-state current	V= V DRM	125	150	mA
CONDU	CTING				
I T (AV)	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =70°C, Double side cooled		2100	Α
I RMS	RMS on-state current			3297	А
		Sine wave, 10 ms	25	34000	Α
I TSM	Surge on-state current	Without reverse voltage	125	33000	Α
	l² t	Sine wave, 10 ms	25	5780 x 10 <sup>3</sup>	A²s
I² t		Without reverse voltage	125	5445 x 10 <sup>3</sup>	A²s
V т	On-state voltage	On-state current = 2000A	125	2.04	V
V T(TO)	Threshold voltage		125	1.271	V
rт	On-state slope resistance		125	0.255	mΩ
SWITCH					
di/dt	Critical rate of rise of on-state current	Repetitive	125	300	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 67\%V_{DRM}$	125	1000	V/µs
Q <sub>RR</sub>	Recovered Charge	$I_{TM}=1000A$ , $-di_F/dt=50A/\mu s$ , $V_R=100V$ , $t_p=1000\mu s$	125	1370	μC
Tq	Circuit commutated turn off time	I <sub>TM</sub> =1000A, -di <sub>F</sub> /dt = 60A/μs, V <sub>R</sub> = 100V,t <sub>p</sub> =1000μs	125	60 - 100	μs
GATE		Reapplied dv/dt = 400V/μs, V <sub>DR</sub> = 80%V <sub>DRM</sub>			· · ·
I gt	Gate trigger current	V <sub>D</sub> =6V	25	250	mA
V gt	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	600	mA
ΙL	Latching current	V <sub>D</sub> =6V	25	1000	mA
MOUNTI	NG				
R th(j-c)	Thermal impedance, sin 180°	Junction to case, Double side cooled		0.010	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, Double side cooled		0.012	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, Double side cooled		0.002	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 125	°C
М	Clamping Force			40 - 45	kN
W	Weight (Approx.)			1650	gm

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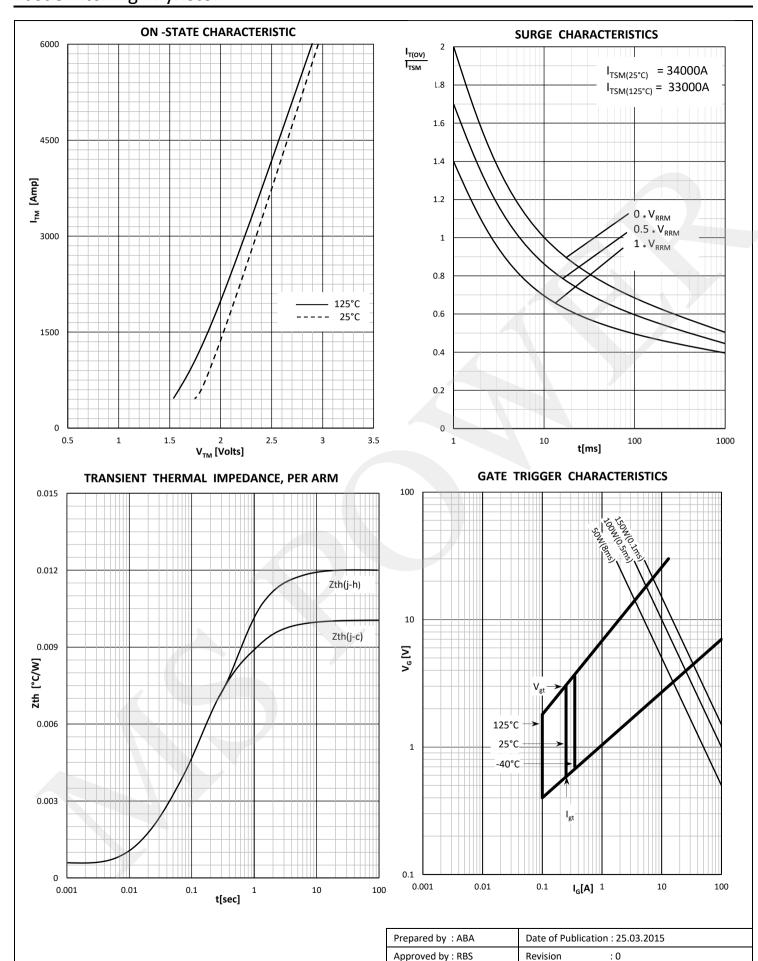




# Technical Information Fast Switching Thyristor

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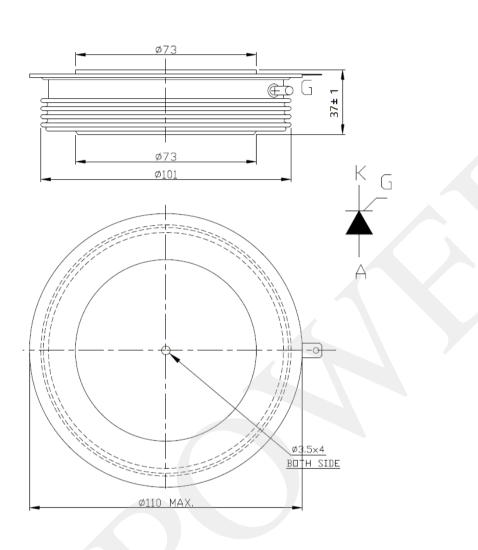




## **MS TF2100**



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