## Technical Information Fast Switching Thyristor

### **MS TF2750**





### **Key Parameters**

 $\begin{array}{lll} V_{DRM} \, / \, V_{RRM} & = 2500 V \\ I_{T(AV)} & = 2750 A \\ I_{TSM} & = 38 k A \\ V_{T(TO)} & = 1.308 V \\ r_{T} & = 0.17 m \Omega \end{array}$ 

#### **Features**

- Full blocking capability over wide temperature range
- High Surge current capability
- Hermetic metal case with ceramic insulator
- Distributed gate

### **Applications**

- Battery Chargers
- Medical Equipment
- UPS
- Power Supplies
- Motor control
- Transportation
- Induction Heating
- Welding

### **Ordering Information**

MS TF	2750	С	хх	F	2N
Fast Switching Thyristor	Current Code	C - Capsule package with Alloyed silicon technology	Voltage Code Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub>	Reapplied dv/dt F =200V/µsec	Turn Off time code 2J = 50µsec 2N = 60µsec 2G = 70µsec
Order Code MS TE2750C20E2N = 2000V VDBM VDBM Tc=60usec 37mm clamp height cansule					

Order Code MS TF2750C20F2N – 2000V V<sub>DRM</sub>,V<sub>RRM</sub>, T<sub>q</sub>=60µsec, 37mm clamp height capsule

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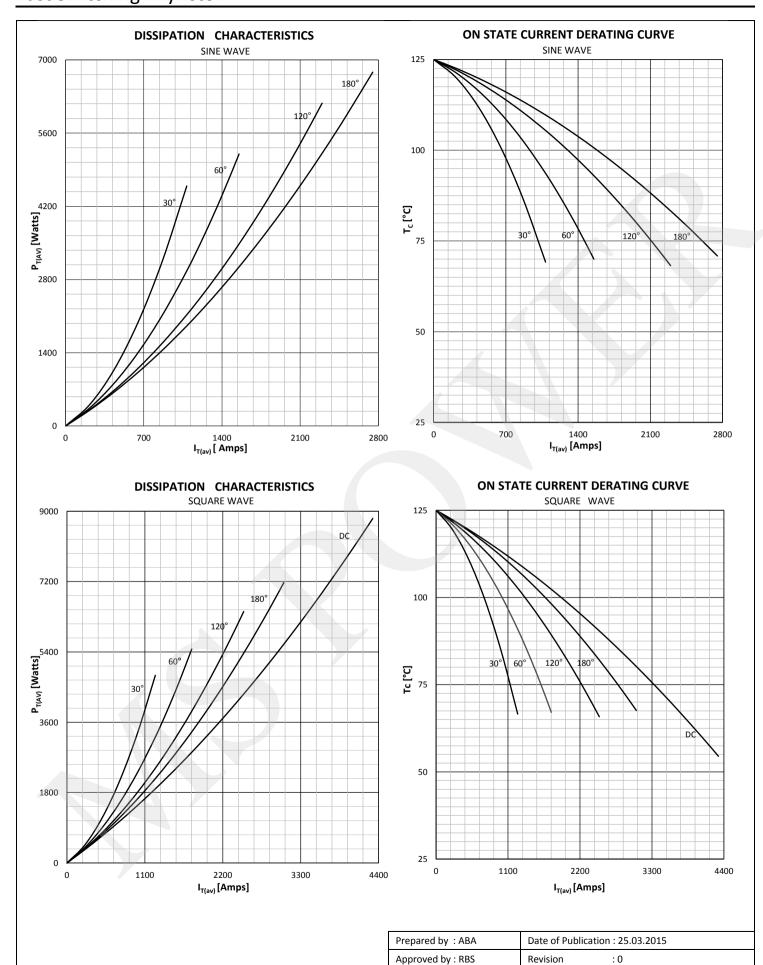
Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	NG				
V RRM Repetitive peak reverse voltage			125	1800 - 2500	V
V RSM Non-repetitive peak reverse voltage			125	1900 - 2600	V
V DRM	Repetitive peak off-state voltage		125	1800 - 2500	V
I RRM	Repetitive peak reverse current	V= V RRM	125	250	mA
I DRM	Repetitive peak off-state current	V= V DRM	125	250	mA
CONDU	CTING				
I T (AV)	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =70°C, Double side cooled 180° sin ,50 Hz, T <sub>c</sub> =74°C, Double side cooled		2750 2620	Α
IRMS	RMS on-state current	T <sub>c</sub> =70°C, Double side cooled		4317	A
		Sine wave, 10 ms Without reverse voltage	25	38.0	kA
I TSM			125	37.2	kA
		Sing ways 10 mg	25	7220 x 10 <sup>3</sup>	A²s
l² t	l² t	Sine wave, 10 ms Without reverse voltage	125	6919 x 10 <sup>3</sup>	A <sup>2</sup> s
Vт	On-state voltage	On-state current = 4000A	125	2.00	V
V T(TO)	Threshold voltage		125	1.308	V
r T	On-state slope resistance		125	0.170	mΩ
CWITCH					
SWITCH di/dt	Critical rate of rise of on-state current	Repetitive	125	1000	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 67\% V_{DRM}$	125	500	V/µs
Qrr	Recovered Charge	$I_{TM}$ =1000A, -di <sub>F</sub> /dt = 60A/µs, $V_R$ = 50V, $t_p$ =1000µs	125	1800	μС
	Circuit commutated turn off time	I <sub>TM</sub> =1000A, -dif/dt = 20A/μs, V <sub>R</sub> = 50V,t <sub>p</sub> =1000μs	125	50 - 70	-
Tq	Circuit commutated turn on time	Reapplied dv/dt = 200V/μs, V <sub>DR</sub> = 33%V <sub>DRM</sub>	123	50 - 70	μs
GATE	Onto triangle surrout	W 9W	0.5	000	^
I gt	Gate trigger current	V <sub>D</sub> =6V	25	300	mA V
V gt	Gate trigger voltage	V <sub>D</sub> =6V	25 25	3.0 1000	
I <sub>H</sub>	Holding current  Latching current	$V_D=6V$ , gate open circuit $V_D=6V$	25	1200	mA mA
	-	v D=0 v	23	1200	IIIA
MOUNTI R th(j-c)	NG Thermal impedance, sin 180°	Junction to case, Double side cooled		0.008	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, Double side cooled		0.0092	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, Double side cooled		0.0092	°C/W
T j	Max. junction temperature	Case to Heatshirk, Double Side Cooled		125	°C
ı j	Storage temperature			-40 125	
T eta	SIGNAGE ICHIDCIGIUIC			70 120	C
T stg	Clamping Force			30 - 47	kN

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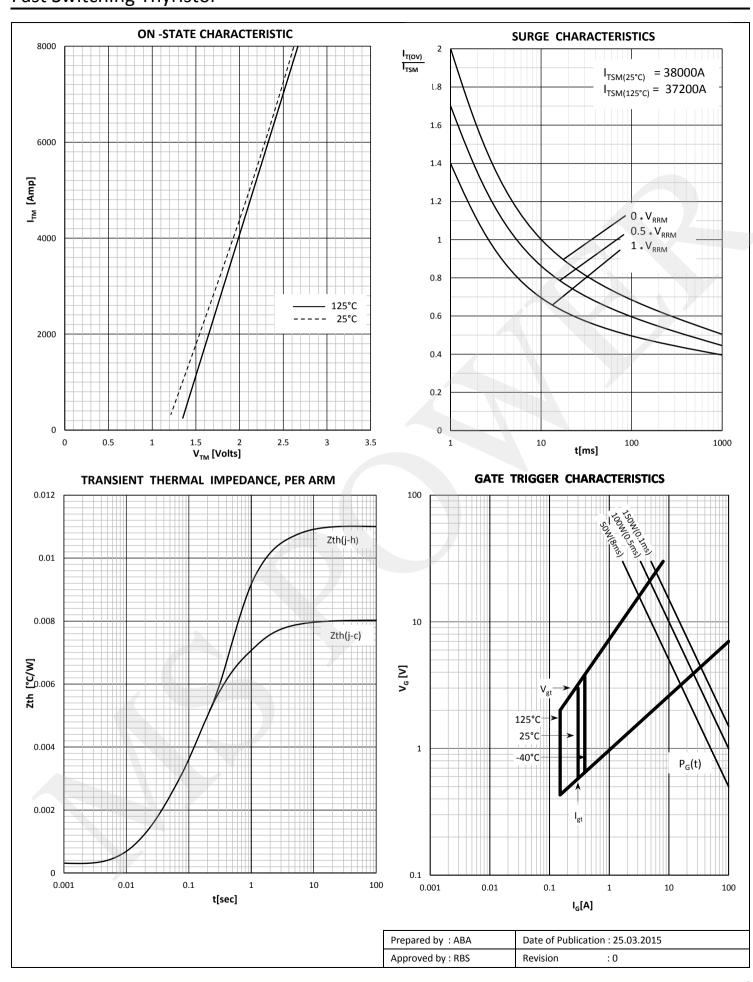




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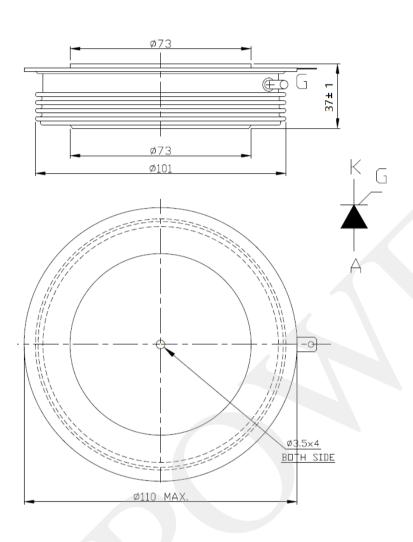




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#### **Outline**



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