## **Technical Information**

### Fast Switching Thyristor

### **MS TF810**





#### **Key Parameters**

V<sub>DRM</sub> / V<sub>RRM</sub> = 1200V= 810A $I_{T(AV)}$ = 10250A $I_{TSM}$ = 1.90 V $V_{T(TO)}$  $= 0.357 m\Omega$ rт

#### **Features**

- Full blocking capability over wide temperature
- High Surge current capability
- Hermetic metal case with ceramic insulator
- Distributed gate

- **Applications** Battery Chargers
- Medical Equipment
- **UPS**
- Power Supplies
- Motor control
- Transportation
- Induction Heating
- Welding

#### **Ordering Information**

| MS TF   | 810             | С  | ХX  | F                               | Α  |
|---|-----------------|--|---|---------------------------------|--|
| Fast Switching<br>Thyristor   | Current<br>Code | C - Capsule package with<br>Alloyed silicon technology | Voltage Code<br>Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub> | Reapplied dv/dt<br>F =200V/µsec | Turn Off time code A = 10µsec E = 15µsec |
| Order Code MS TF810C12FA – 1200V VDRM, VRRM, Tg=10usec, 26mm clamp height capsule |                 |  |   |                                 |  |

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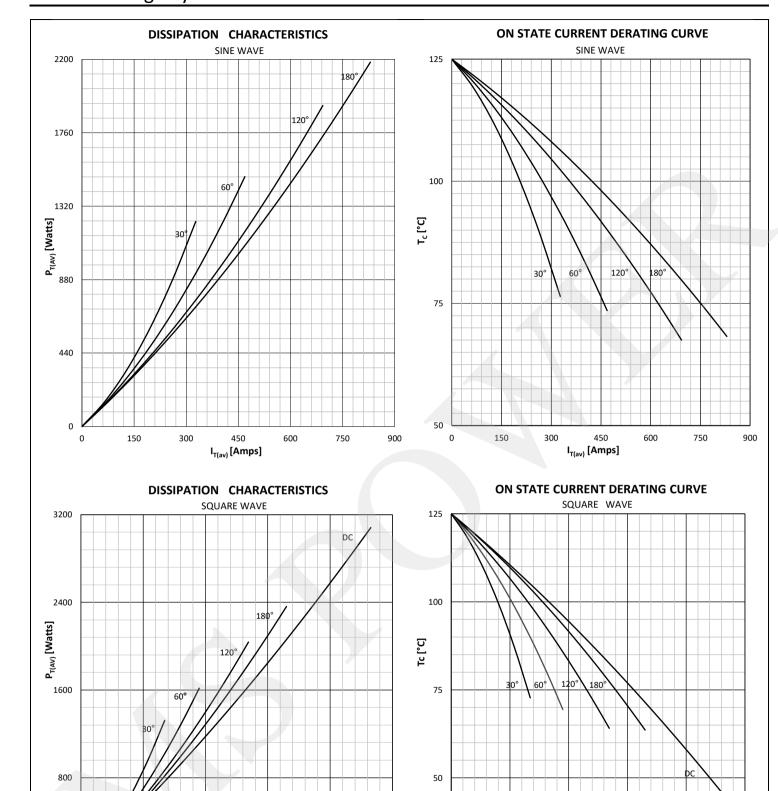
| Symbol         | Characteristic                             | Conditions  | Tj<br>[°C]            | Value  | Unit |
|----------------|--|---|-----------------------|--|------|
| BLOCKI         | NG   |   |                       |  |      |
| V RRM          | Repetitive peak reverse voltage            |   | 125                   | 800 - 1200                                   | V    |
| V RSM          | Non-repetitive peak reverse voltage        |   | 125                   | 900 - 1300                                   | V    |
| V DRM          | Repetitive peak off-state voltage          |   | 125                   | 800 - 1200                                   | V    |
| I RRM          | Repetitive peak reverse current            | V= V RRM  | 125                   | 70   | mA   |
| I DRM          | Repetitive peak off-state current          | V= V DRM  | 125                   | 70   | mA   |
| CONDU          | CTING                                      |   | "                     |  |      |
| I T (AV)       | Mean on state current                      | 180° sin ,50 Hz, $T_c$ =70°C, Double side cooled 180° sin ,50 Hz, $T_c$ =68°C, Double side cooled   |                       | 810<br>830                                   | A    |
| I RMS          | RMS on-state current                       | T <sub>c</sub> =68°C, Double side cooled  |                       | 1303   | Α    |
|                |  | Sine wave, 10 ms  | 25                    | 10250  | A    |
| I TSM          | Surge on-state current                     | Without reverse voltage   | 125                   | 9350   | Α    |
|                | Sine wave, 10 ms                           | 25  | 525 x 10 <sup>3</sup> | A <sup>2</sup> s                             |      |
| l² t           | l² t                                       | Without reverse voltage   | 125                   | 437 x 10 <sup>3</sup>                        | A²s  |
| Vт             | On-state voltage                           | On-state current = 1400A  | 125                   | 2.40   | V    |
| V T(TO)        | Threshold voltage                          |   | 125                   | 1.90   | V    |
| rт             | On-state slope resistance                  |   | 125                   | 0.357  | mΩ   |
| SWITCH         | ING  |   |                       | <u>                                     </u> |      |
| di/dt          | Critical rate of rise of on-state current  | Repetitive  | 125                   | 1000   | A/µs |
| dv/dt          | Critical rate of rise of off-state voltage | $V_{DR} = 80\%V_{DRM}$  | 125                   | 200  | V/µs |
| Tq             | Circuit commutated turn off time           | $I_{TM}$ =1000A, -di <sub>F</sub> /dt = 60A/µs, V <sub>R</sub> = 50V,t <sub>P</sub> =1000µs<br>Reapplied dv/dt = 200V/µs, V <sub>DR</sub> = 33%V <sub>DRM</sub> | 125                   | 10 - 15                                      | μs   |
| GATE           |  |   |                       |  |      |
| I gt           | Gate trigger current                       | V <sub>D</sub> =6V  | 25                    | 300  | mA   |
| $V_{gt}$       | Gate trigger voltage                       | V <sub>D</sub> =6V  | 25                    | 3.0  | V    |
| I <sub>H</sub> | Holding current                            | V <sub>D</sub> =6V, gate open circuit   | 25                    | 1000   | mA   |
| Iι             | Latching current                           | V <sub>D</sub> =6V  | 25                    | 1200   | mA   |
| MOUNTI         | NG   |   |                       |  |      |
| R th(j-c)      | Thermal impedance, sin 180°                | Junction to case, Double side cooled  |                       | 0.026  | °C/W |
| R th(j-c)      | Thermal impedance, rec120°                 | Junction to case, Double side cooled  |                       | 0.030  | °C/W |
| R th(c-h)      | Thermal impedance                          | Case to heatsink, Double side cooled  |                       | 0.006  | °C/W |
| Тj             | Max. junction temperature                  |   |                       | 125  | °C   |
| T stg          | Storage temperature                        |   |                       | -40 125                                      | °C   |
| М              | Clamping Force                             |   |                       | 10 - 20                                      | kN   |
| W              | Weight (Approx.)                           |   |                       | 300  | gm   |

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 $I_{T(av)}[Amps]$ 

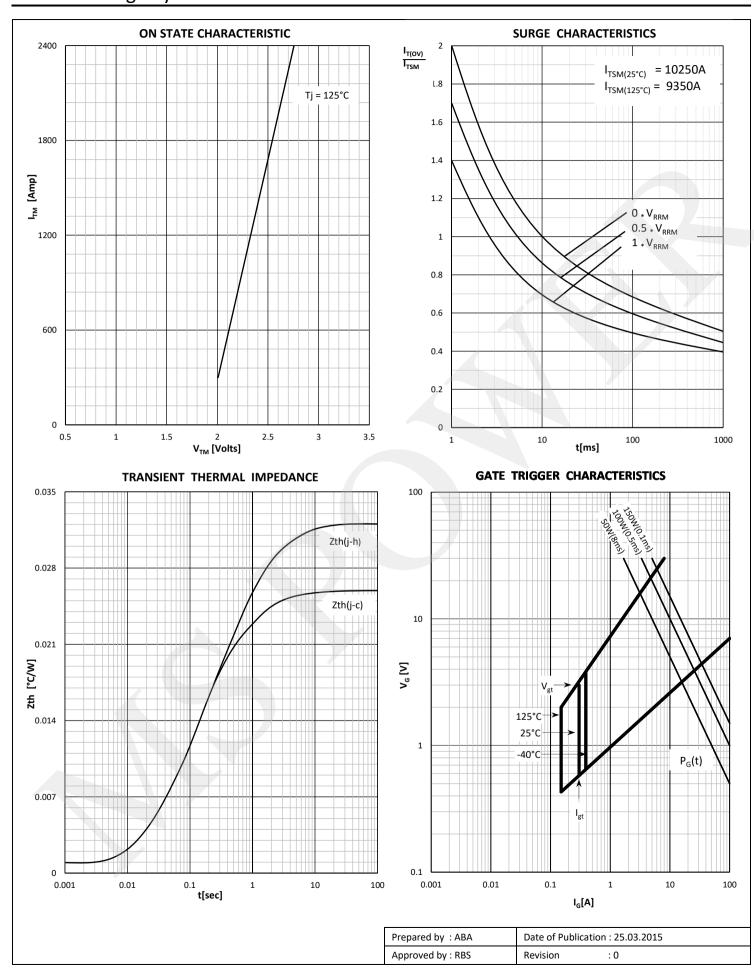
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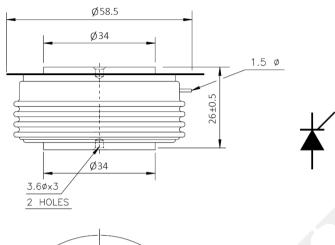


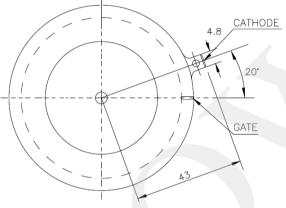


### **MS TF810**



#### **Outline**





#### **MS Power GmbH**

Mergenthalerallee 79-81 65760 Eschborn, Germany Web: www.mspowergroup.com Mail: info@mspowergroup.de

#### Sales & Enquiry:

sales@mspowergroup.de
Technical Support:
solution@mspowergroup.de
After sales Service:
service@mspowergroup.de

Phone: +49 (0) 6196/7768 666 Fax: +49 (0) 6196/7757 888



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