

Key Parameters

V_{DRM} / V_{RRM}	= 1800V
$I_{T(AV)}$	= 106A
I_{TSM}	= 2250A
$V_{T(TO)}$	= 0.9V
r_T	= 2.0mΩ

Features

- Full blocking capability over wide temperature range
- Heat transfer through aluminium oxide ceramic isolated metal baseplate
- Hard soldered joints for high reliability

Applications

- Power Supplies
- DC motor control
- Controlled Rectifiers
- Temperature control

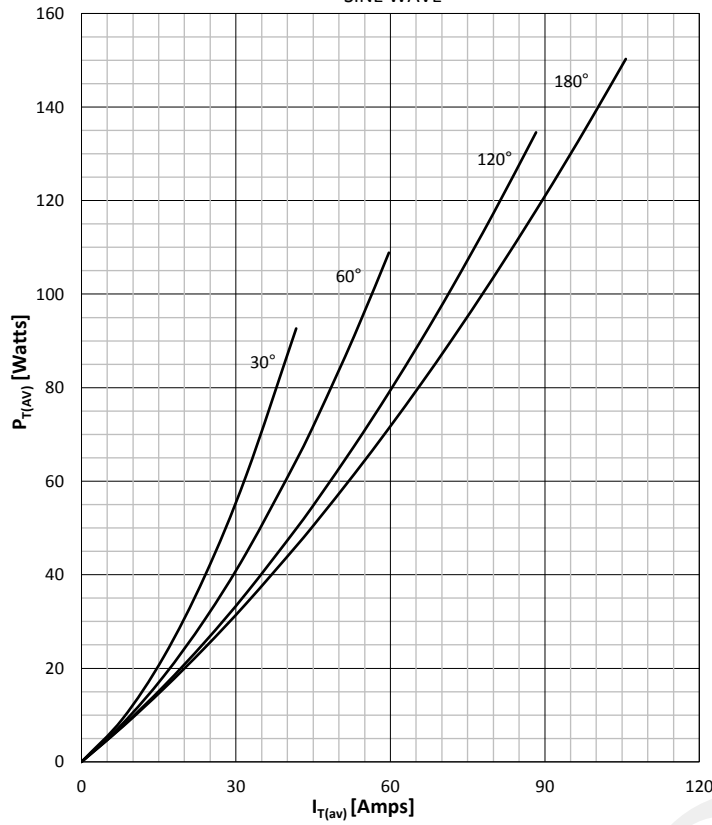
Ordering Information

MS	TT	106	S	XX	XX
Fixed code	TT- Thyristor- Thyristor Module TD- Thyristor- Diode Module	Current Code	Technology S = Solder Bond Technology	Voltage Code Code X 100 = V_{DRM}/V_{RRM}	None - Standard connection KK - Common Cathode
Order Code MS TT106S18 : 1800V V_{DRM}, V_{RRM} , Thyristor-Thyristor Module					

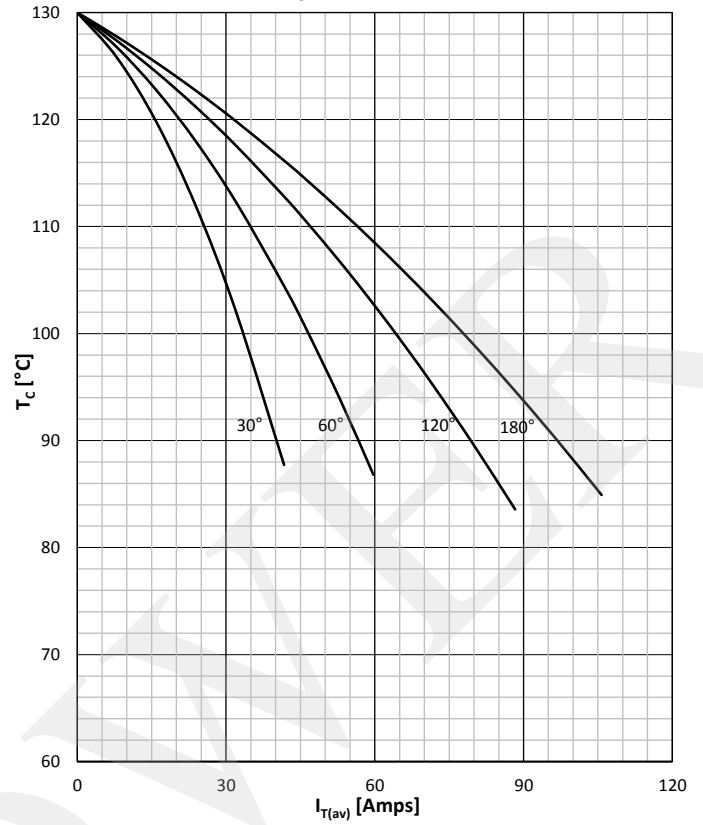
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Symbol	Characteristic	Conditions	T _j [°C]	Value	Unit
BLOCKING					
V _{RRM}	Repetitive peak reverse voltage		130	200 - 1800	V
V _{RSM}	Non-repetitive peak reverse voltage		130	300 - 1900	V
V _{DRM}	Repetitive peak off-state voltage		130	200 - 1800	V
I _{RRM}	Repetitive peak reverse current	V = V _{RRM}	130	20	mA
I _{DRM}	Repetitive peak off-state current	V = V _{DRM}	130	20	mA
CONDUCTING					
I _{T(AV)}	Mean on state current	180° sin ,50 Hz, T _c =85°C		106	A
I _{RMS}	RMS on-state current			166	A
I _{TSM}	Surge on-state current	Sine wave, 10 ms Without reverse voltage	25	2250	A
			130	1900	A
I ² t	I ² t	Sine wave, 10 ms Without reverse voltage	25	25300	A ² s
			130	18050	A ² s
V _T	On-state voltage	On-state current = 300A	25	1.65	V
V _{T(TO)}	Threshold voltage		130	0.9	V
r _T	On-state slope resistance		130	2.0	mΩ
SWITCHING					
di/dt	Critical rate of rise of on-state current		130	150	A/μs
dv/dt	Critical rate of rise of off-state voltage	V _{DR} = 67%V _{DRM}	130	1000	V/μs
GATE					
I _{gt}	Gate trigger current	V _D =6V	25	150	mA
V _{gt}	Gate trigger voltage	V _D =6V	25	3.0	V
I _H	Holding current	V _D =6V, gate open circuit	25	250	mA
I _L	Latching current	V _D =6V	25	600	mA
MOUNTING					
R _{th(j-c)}	Thermal impedance, sin 180°	Junction to case, per arm per module		0.30 0.15	°C/W
R _{th(j-c)}	Thermal impedance, rec120°	Junction to case, per arm per module		0.34 0.17	°C/W
R _{th(c-h)}	Thermal impedance	Case to heatsink, per arm per module		0.2 0.1	°C/W
T _j	Max. junction temperature			130	°C
T _{stg}	Storage temperature			-40 ... 125	°C
V _{ISOL}	Insulation test voltage,RMS	F=50Hz, 1min		2.5	KV
M1	Mounting torque			5 ± 15%	Nm
M2	Terminal connection torque			3 ± 15%	Nm
W	Weight (Approx.)			105	gm
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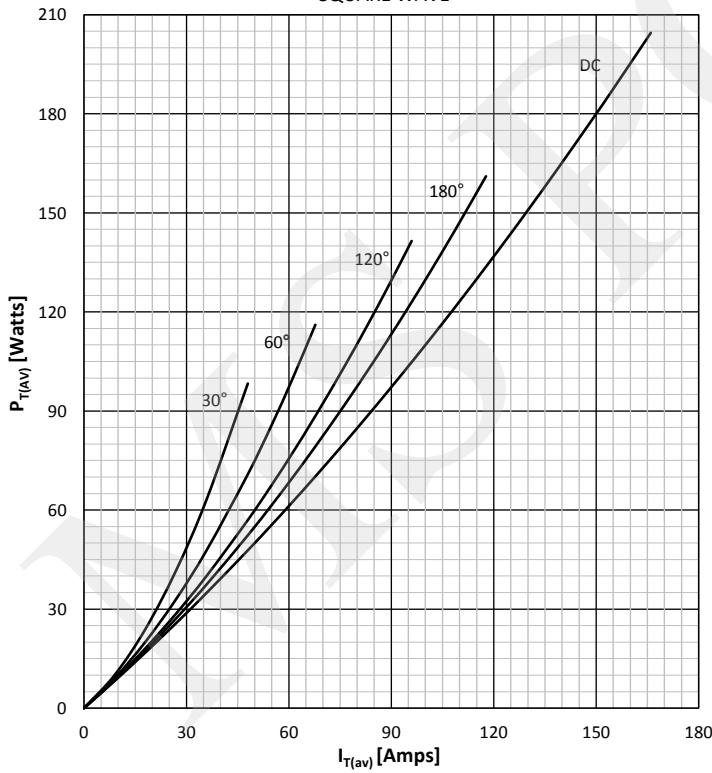
DISSIPATION CHARACTERISTICS
SINE WAVE



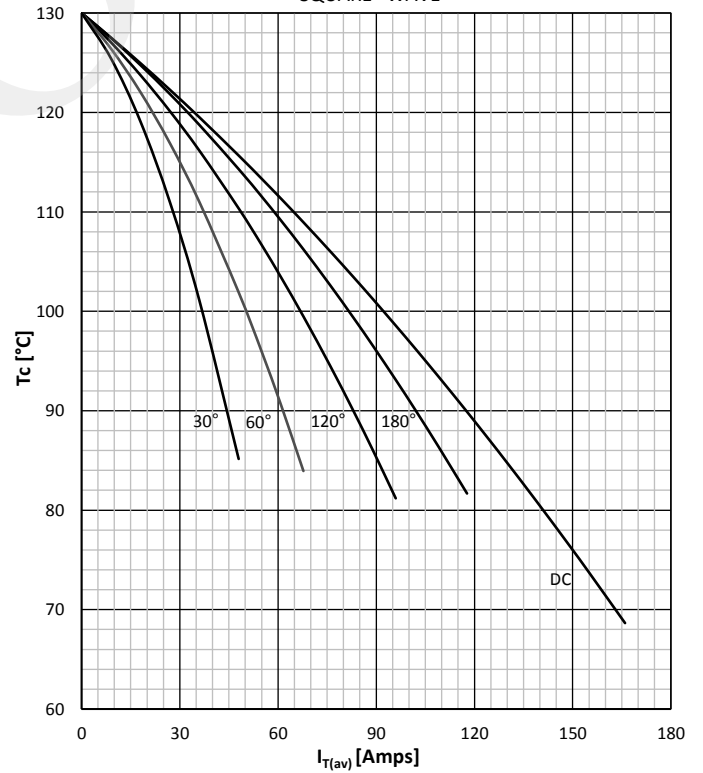
ON STATE CURRENT DERATING CURVE
SINE WAVE



DISSIPATION CHARACTERISTICS
SQUARE WAVE



ON STATE CURRENT DERATING CURVE
SQUARE WAVE



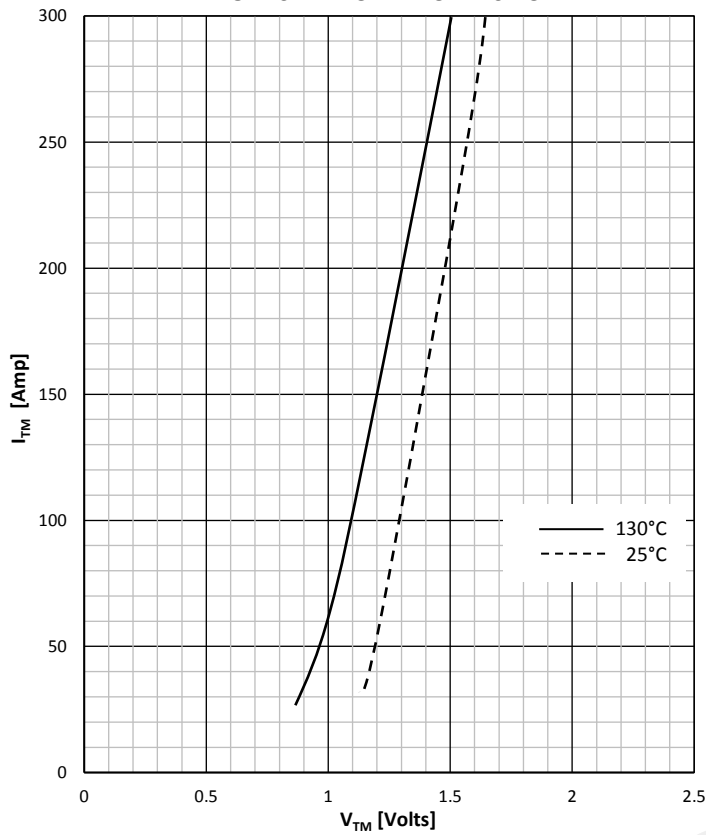
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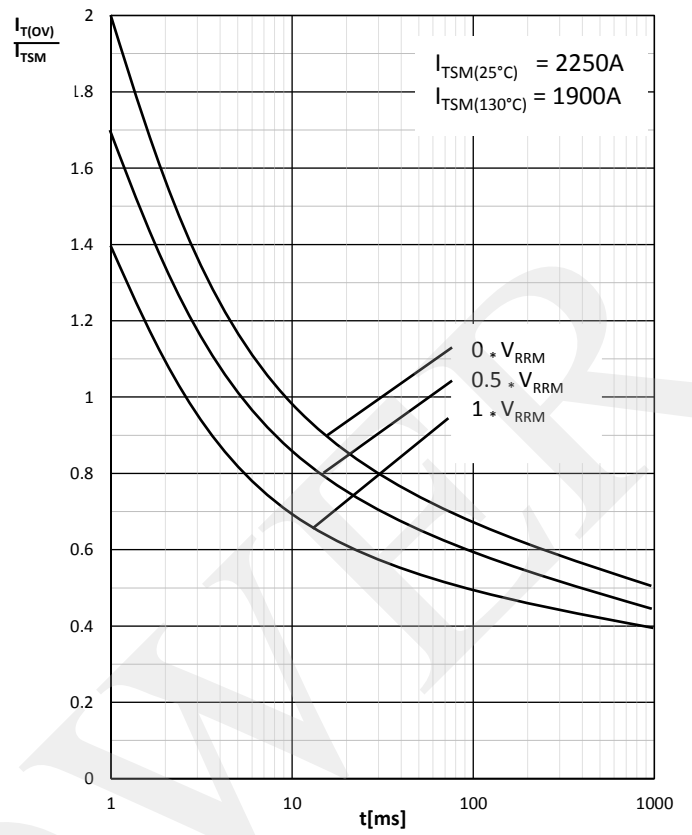
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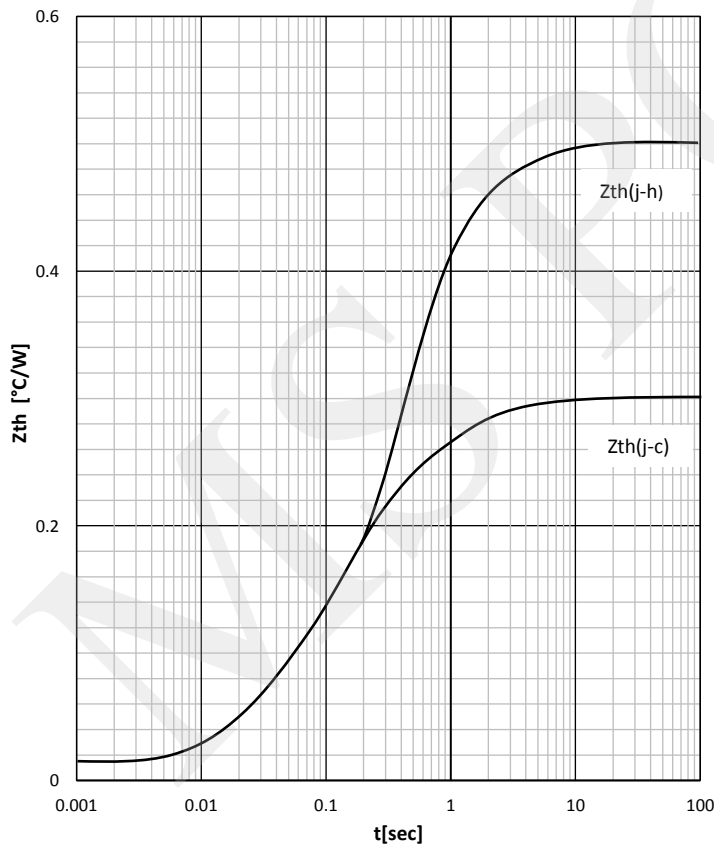
ON -STATE CHARACTERISTIC



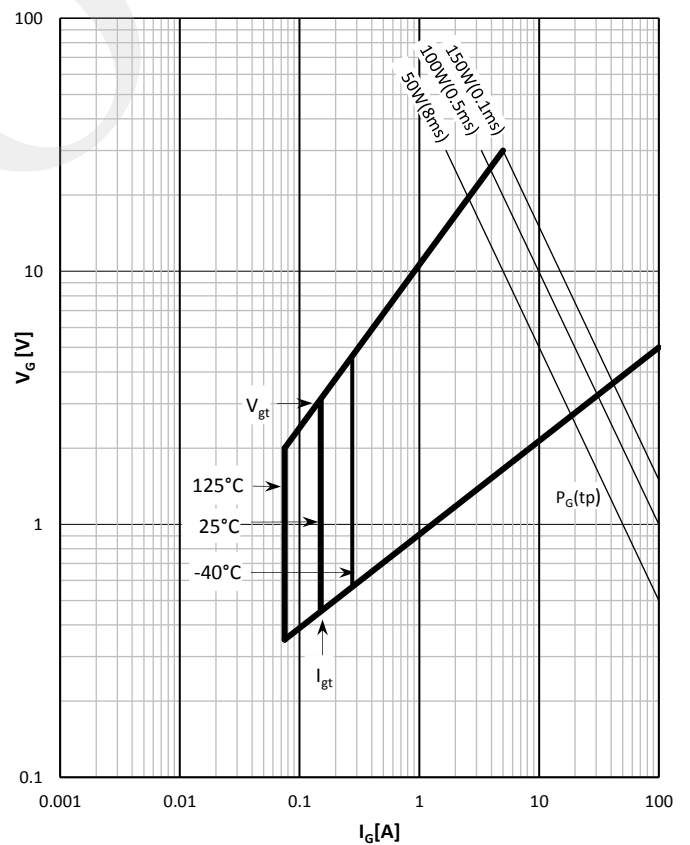
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE, PER ARM

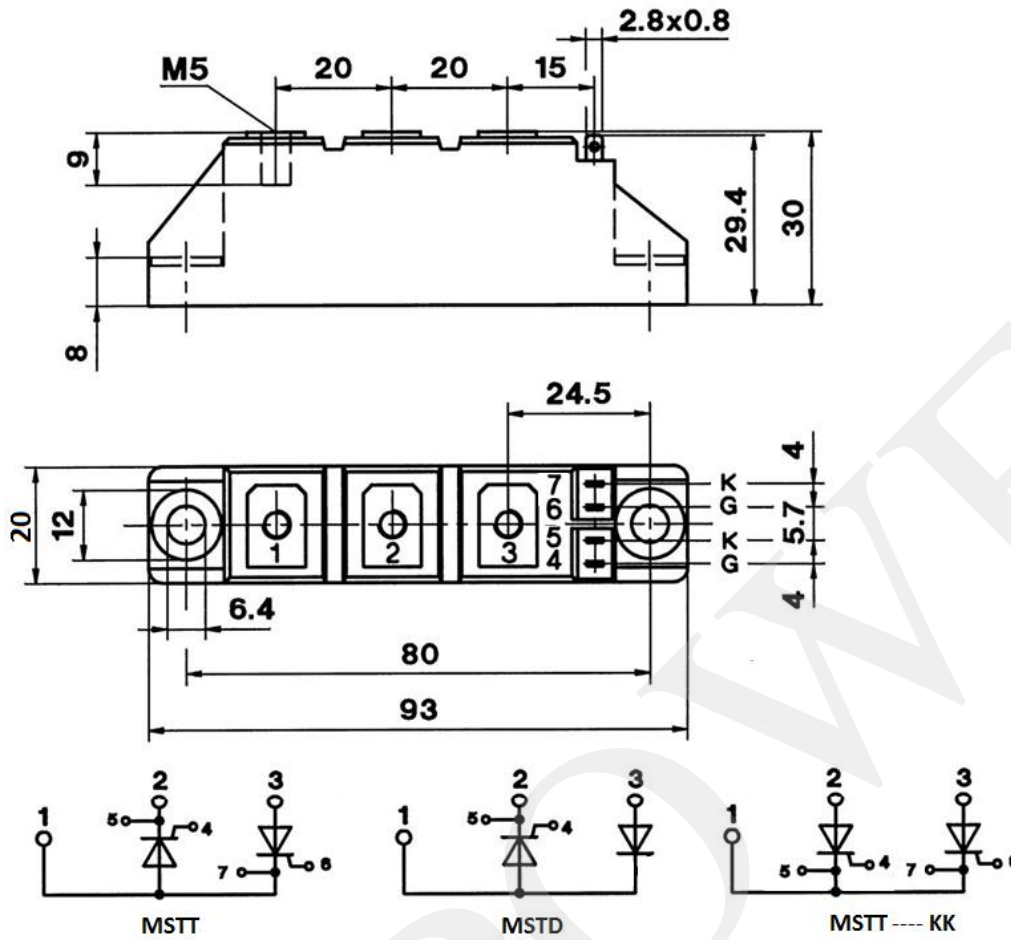


GATE TRIGGER CHARACTERISTICS



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