



Key Parameters

V_{DRM} / V_{RRM}	= 2400V
$I_{T(AV)}$	= 252A
I_{TSM}	= 8700A
$V_{T(TO)}$	= 0.80V
r_T	= 0.70m Ω

Features

- Full blocking capability over wide temperature range
- Heat transfer through aluminium oxide ceramic isolated metal baseplate
- Pressure contacts technology for high reliability
- UL Recognized, file no. E505556

Applications


- Power Supplies
- DC motor control
- Controlled Rectifiers
- AC switch

Ordering Information

MS	TT	252	K	24
Fixed code	TT- Thyristor- Thyristor Module	Current Code	Technology K = Pressure Contact Technology	Voltage Code Code X 100 = V_{DRM}/V_{RRM}

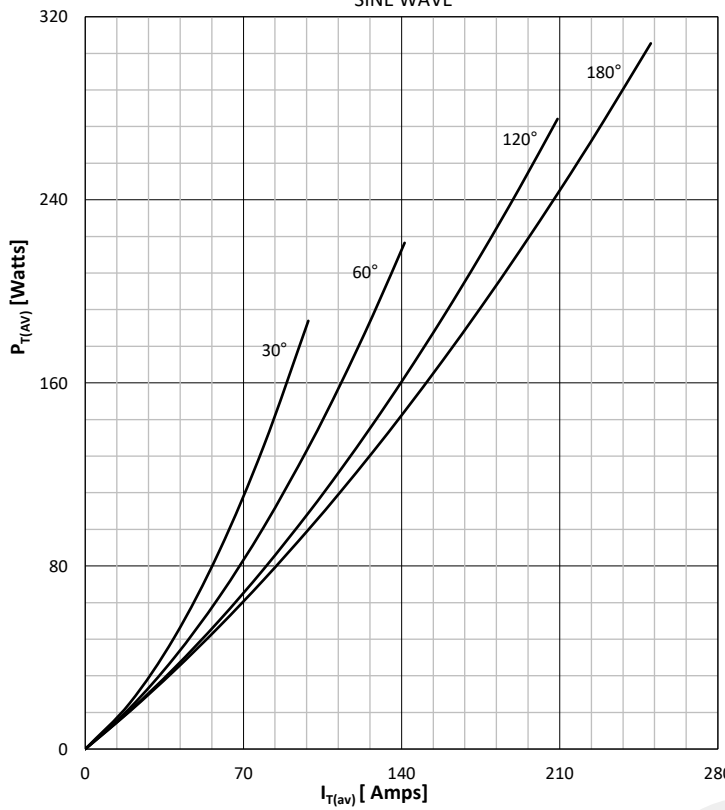
Order Code MS TT252K24 : 2400V V_{DRM}, V_{RRM} , Thyristor-Thyristor Module

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Symbol	Characteristic	Conditions	T _j [°C]	Value	Unit
BLOCKING					
V _{RRM}	Repetitive peak reverse voltage		125	2000 - 2400	V
V _{RSM}	Non-repetitive peak reverse voltage		125	2100 - 2500	V
V _{DRM}	Repetitive peak off-state voltage		125	2000 - 2400	V
I _{RRM}	Repetitive peak reverse current	V = V _{RRM}	125	40	mA
I _{DRM}	Repetitive peak off-state current	V = V _{DRM}	125	40	mA
CONDUCTING					
I _{T(AV)}	Mean on state current	180° sin ,50 Hz, T _c =91°C 180° sin ,50 Hz, T _c =85°C		252 283	A
I _{RMS}	RMS on-state current	T _c =91°C		393	A
I _{TSM}	Surge on-state current	Sine wave, 10 ms Without reverse voltage	25	8700	A
			125	7600	A
I ² t	I ² t	Sine wave, 10 ms Without reverse voltage	25	378450	A ² s
			125	288800	A ² s
V _T	On-state voltage	On-state current = 785A	25	1.50	V
V _{T(TO)}	Threshold voltage		125	0.80	V
r _T	On-state slope resistance		125	0.70	mΩ
SWITCHING					
di/dt	Critical rate of rise of on-state current	Non-repetitive f=1Hz, I _{GM} =2.0A, di _G /dt>1.0A/μs, I _{TM} =2I _{TAV} , V _D =67%V _{DRM}	125	500	A/μs
dv/dt	Critical rate of rise of off-state voltage	V _{DR} = 67%V _{DRM}	125	1000	V/μs
GATE					
I _{gt}	Gate trigger current	V _D =6V	25	200	mA
V _{gt}	Gate trigger voltage	V _D =6V	25	3.0	V
I _H	Holding current	V _D =6V, gate open circuit	25	600	mA
I _L	Latching current	V _D =6V	25	1000	mA
MOUNTING					
R _{th(j-c)}	Thermal impedance, sin 180°	Junction to case, per arm per module		0.11 0.055	°C/W
R _{th(j-c)}	Thermal impedance, rec120°	Junction to case, per arm per module		0.13 0.065	°C/W
R _{th(c-h)}	Thermal impedance	Case to heatsink, per arm per module		0.04 0.02	°C/W
T _j	Max. junction temperature			125	°C
T _{stg}	Storage temperature			-40 150	°C
V _{ISOL}	Insulation test voltage,RMS	F=50Hz, 1min		3.0	KV
M1	Mounting torque			5 ± 15%	Nm
M2	Terminal connection torque			12 ± 15%	Nm
W	Weight (Approx.)			650	gm
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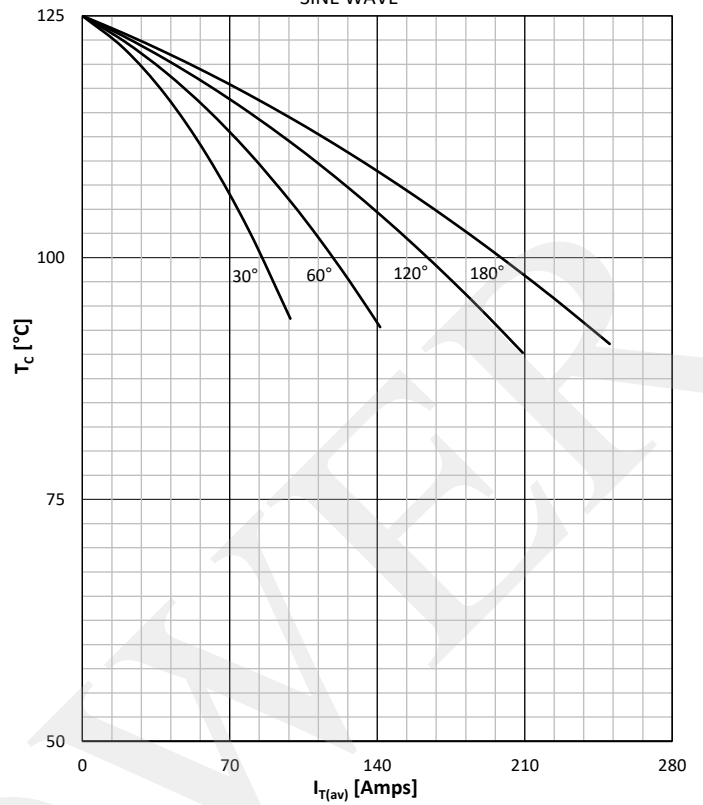
DISSIPATION CHARACTERISTICS

SINE WAVE



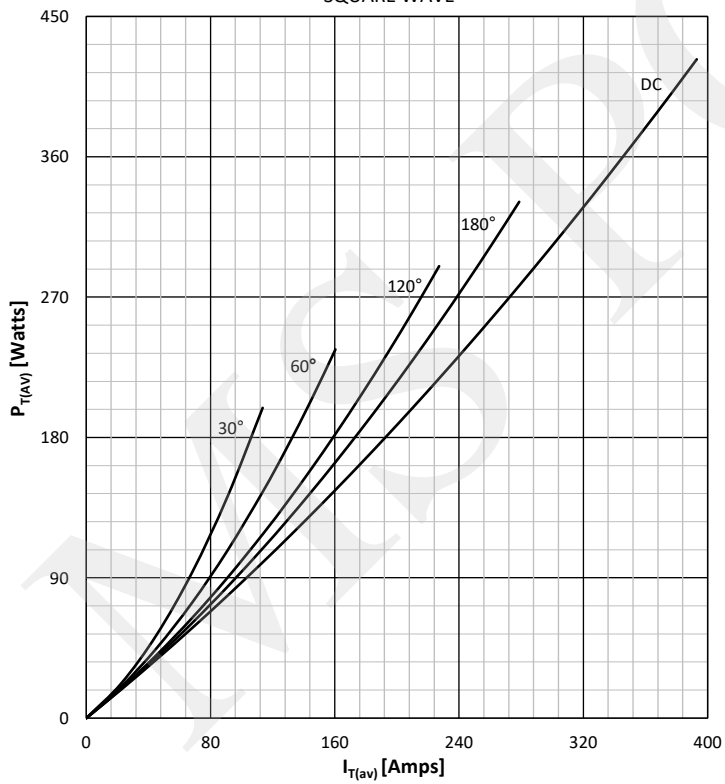
ON STATE CURRENT DERATING CURVE

SINE WAVE



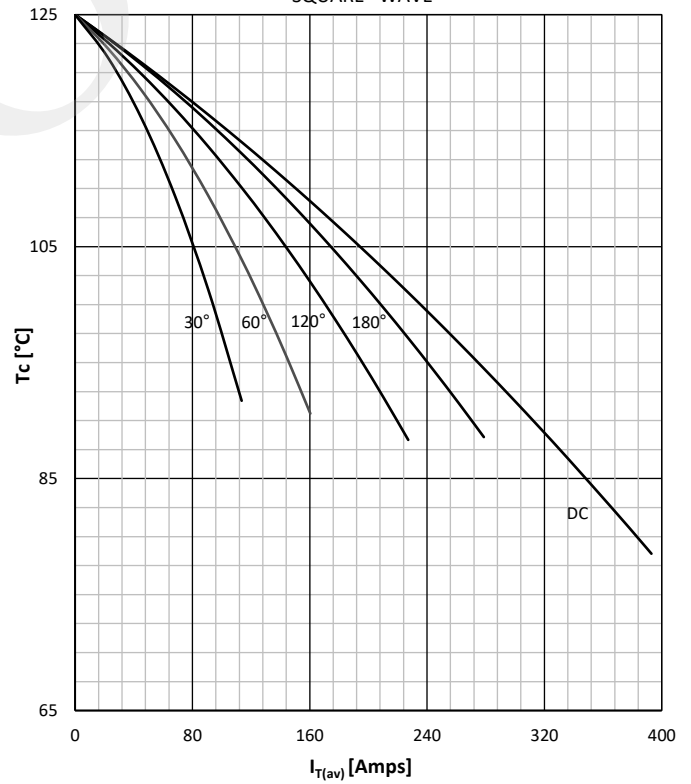
DISSIPATION CHARACTERISTICS

SQUARE WAVE



ON STATE CURRENT DERATING CURVE

SQUARE WAVE



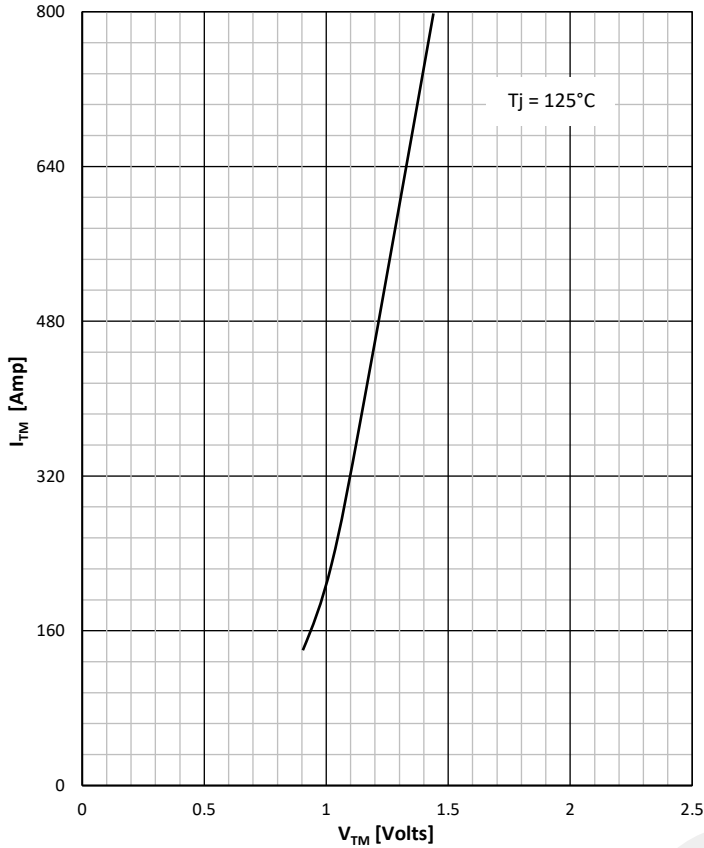
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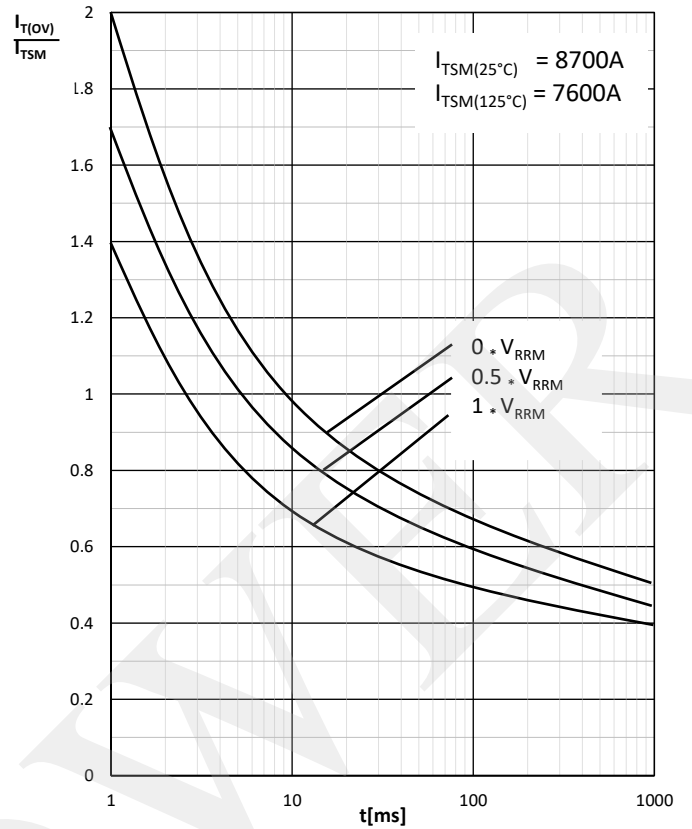
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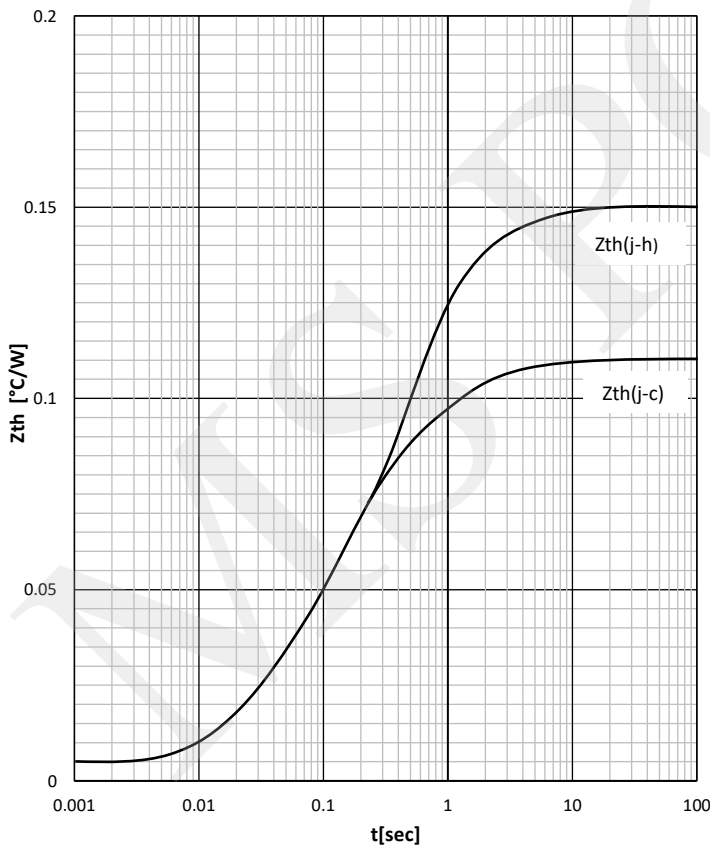
ON -STATE CHARACTERISTIC



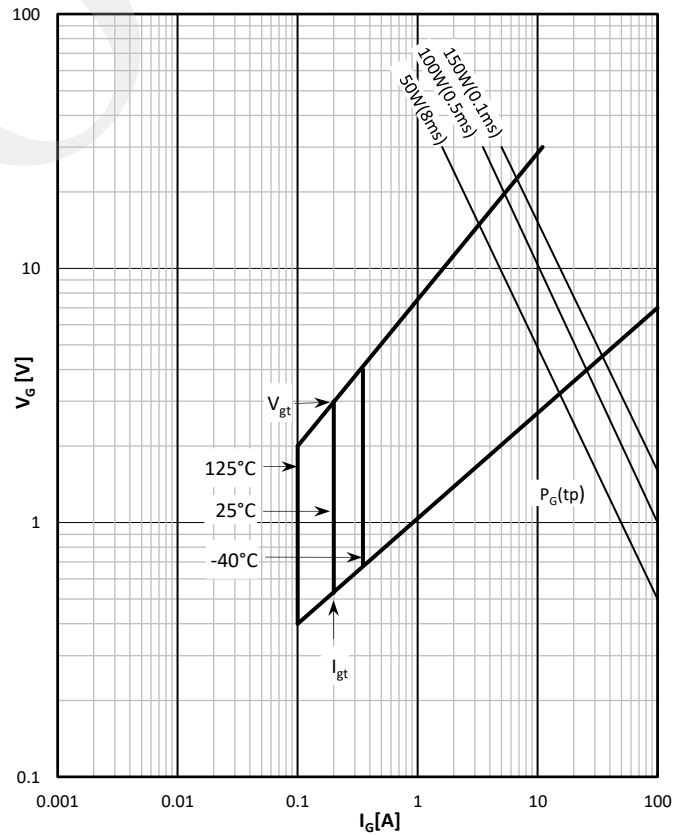
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE, PER ARM



GATE TRIGGER CHARACTERISTICS



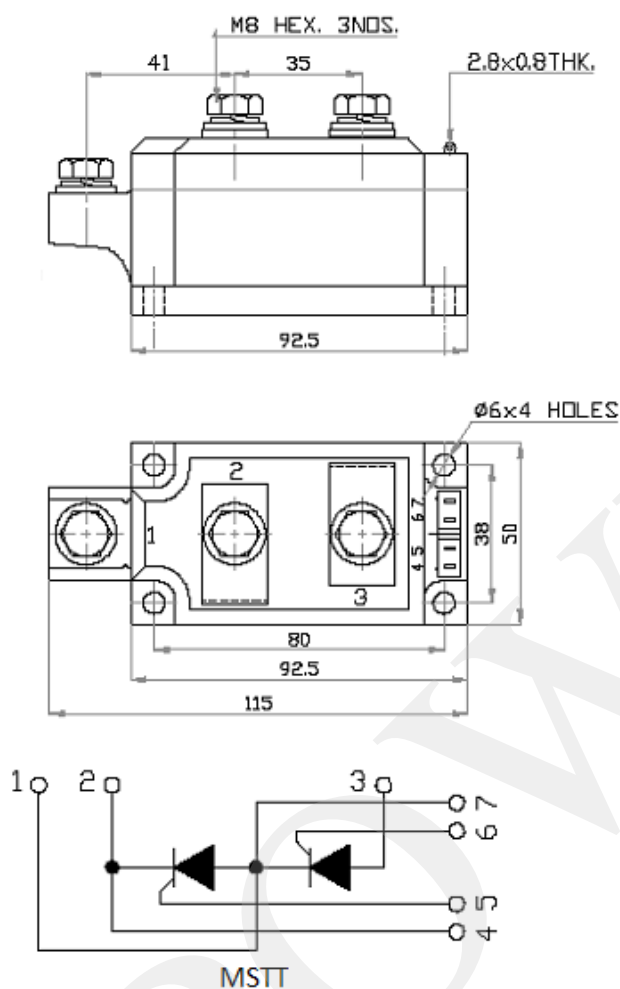
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