# **MS TT330**





# **Key Parameters**

V<sub>DRM</sub> / V<sub>RRM</sub> = 1800V= 330A $I_{T(AV)}$ = 9500AITSM  $V_{T(TO)}$ = 0.84V $= 0.58 \text{m}\Omega$ rт

### **Features**

- Full blocking capability over wide temperature range
- Heat transfer through aluminium oxide ceramic isolated metal baseplate
- Pressure contacts technology for high reliability
  UL Recognized, file no. E505556

# **Applications**

- Power Supplies
- DC motor control
- Controlled Rectifiers
- AC switch

# **Ordering Information**

MS	TT	330	K	18
Fixed code	TT- Thyristor- Thyristor Module	Current Code	Technology K = Pressure Contact Technology	Voltage Code Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub>
Order Code MS TT330K18: 1800V VDRM, VRRM, Thyristor-Thyristor Module				

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# Technical Information Thyristor Modules

# **MS TT330**

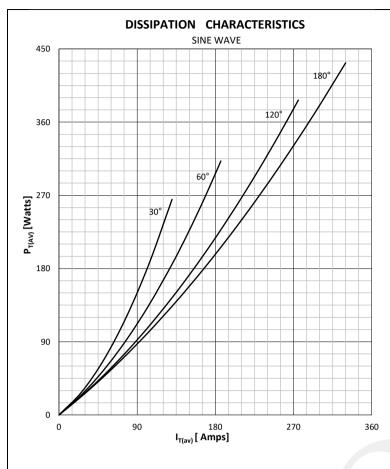


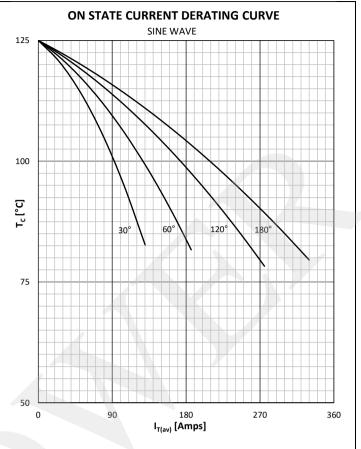
Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	NG				
V RRM	Repetitive peak reverse voltage		125	200 - 1800	V
V RSM	Non-repetitive peak reverse voltage		125	300 - 1900	V
V DRM	Repetitive peak off-state voltage		125	200 - 1800	V
I RRM	Repetitive peak reverse current	V= V RRM	125	80	mA
I DRM	Repetitive peak off-state current	V= V DRM	125	80	mA
CONDU	CTING				
I T (AV)	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =80°C T <sub>c</sub> =85°C		330 305	A
I RMS	RMS on-state current			518	А
		Sine wave, 10 ms	25	9500	Α
I TSM	Surge on-state current	Without reverse voltage	125	8000	Α
		Cina ways 40 mg	25	451000	A²s
l² t	l² t	Sine wave, 10 ms Without reverse voltage	125	320000	A <sup>2</sup> s
V т	On-state voltage	On-state current = 750A	25	1.40	V
		On-State Current = 750A		0.84	V
V T(TO)	Threshold voltage		125		
r⊤	On-state slope resistance		125	0.58	mΩ
SWITCH	ING				
di/dt	Critical rate of rise of on-state current		125	250	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 67\%V_{DRM}$	125	1000	V/µs
GATE					
I gt	Gate trigger current	V <sub>D</sub> =6V	25	200	mA
$V_{gt}$	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	600	mA
I <sub>L</sub>	Latching current	V <sub>D</sub> =6V	25	1000	mA
MOUNT	ING				
R th(j-c)	Thermal impedance, sin 180°	Junction to case, per arm per module		0.105 0.053	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, per arm per module		0.130 0.065	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, per arm per module		0.04 0.02	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 150	°C
$V_{ISOL}$	Insulation test voltage,RMS	F=50Hz, 1min		3.0	KV
M1	Mounting torque			5 ± 15%	Nm
M2	Terminal connection torque			12 ± 15%	Nm
W	Weight (Approx.)			650	gm
<b>A</b>	File No.			E505556	
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# Technical Information Thyristor Modules

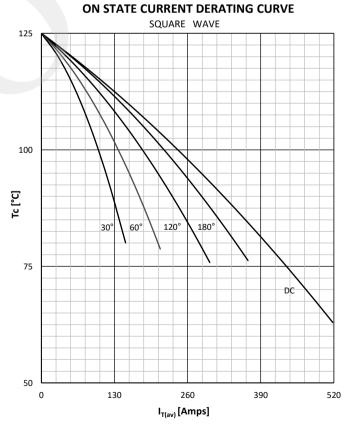
# **MS TT330**





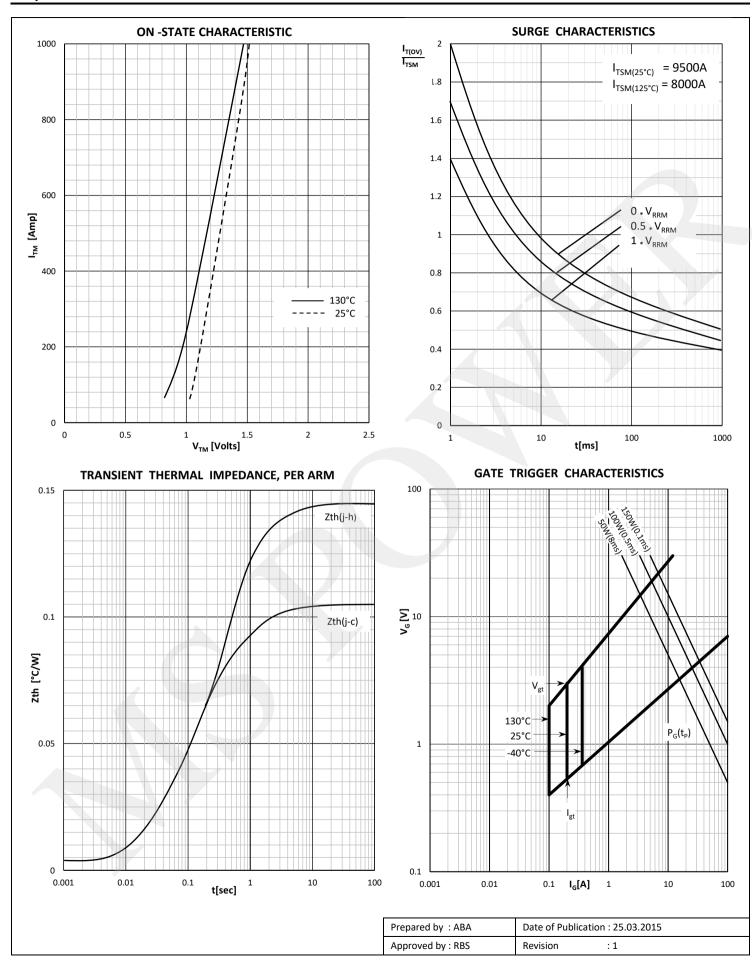


# ### DISSIPATION CHARACTERISTICS SQUARE WAVE 450 450 120° 1300 150 0 130 260 1<sub>(av)</sub> [Amps]



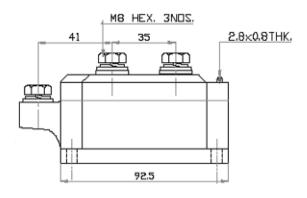
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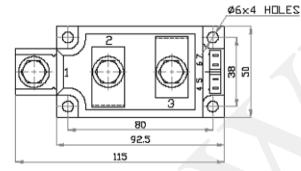


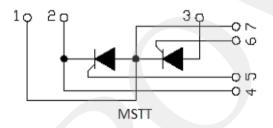




## **Outline**







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