

### **Key Parameters**

VDRM / VRRM = 1800V= 95A $I_{T(AV)}$ = 2000AITSM  $V_{T(TO)}$ = 0.9V $= 2.0 \text{m}\Omega$ rт

#### **Features**

- Full blocking capability over wide temperature range
- Heat transfer through aluminium oxide ceramic isolated metal baseplate
- Hard soldered joints for high reliability

### **Applications** ■ Power Supplies

- DC motor control
- Controlled Rectifiers
- Temperature control

### **Ordering Information**

MS	тт	95	S	ХX
Fixed code	TT- Thyristor - Thyristor Module TD- Thyristor - Diode Module DT- Diode - Thyristor Module	Current Code	Technology S = Solder Bond Technology	Voltage Code Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub>
Order Code MS TT95S18: 1800V V <sub>DRM</sub> , V <sub>RRM</sub> , Thyristor-Thyristor Module				

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# Technical Information Thyristor / Diode Modules

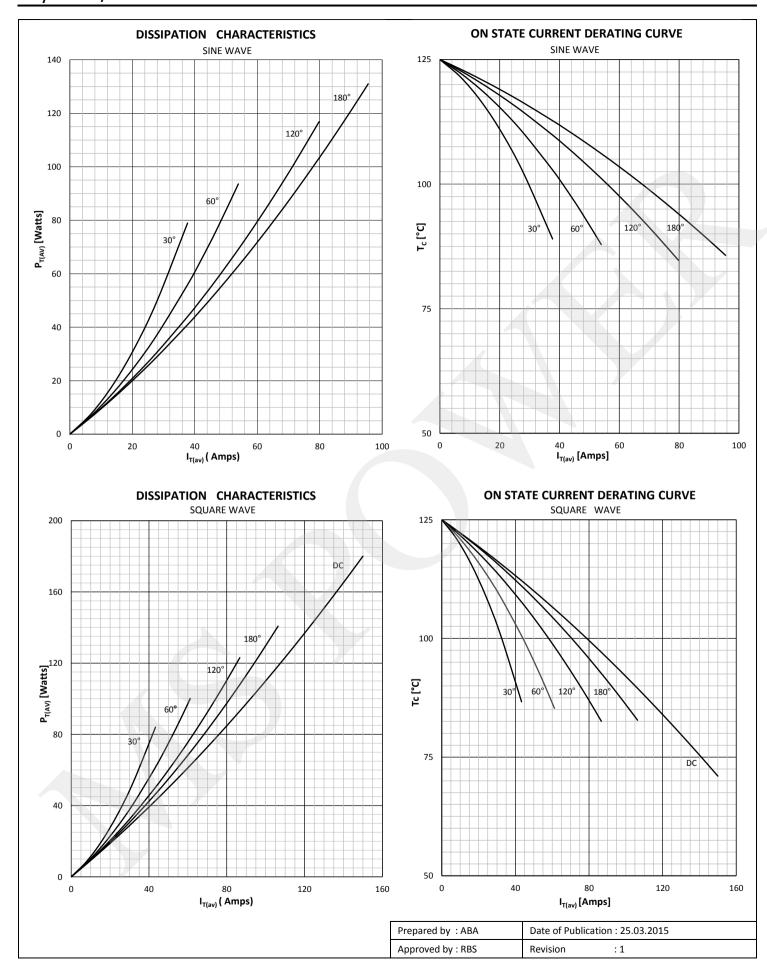
### MS TT/TD95



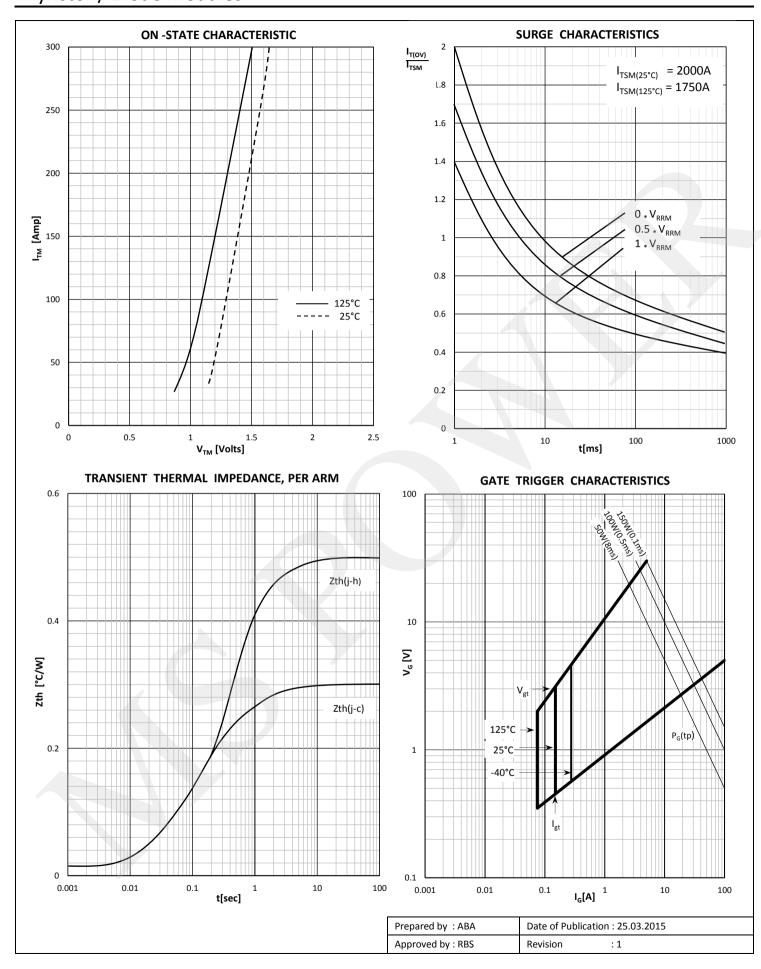
Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	NG				
V RRM	Repetitive peak reverse voltage		125	200 - 1800	V
V RSM	Non-repetitive peak reverse voltage		125	300 - 1900	V
V DRM	Repetitive peak off-state voltage		125	200 - 1800	V
I RRM	Repetitive peak reverse current	V= V RRM	125	20	mA
I DRM	Repetitive peak off-state current	V= V DRM	125	20	mA
CONDU	CTING		,		
IT(AV)	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =85°C		95	А
I RMS	RMS on-state current			150	А
	_	Sine wave, 10 ms	25	2000	А
I тѕм	Surge on-state current	Without reverse voltage	125	1750	Α
		Sine wave, 10 ms	25	20000	A²s
I² t	l <sup>2</sup> t	Without reverse voltage	125	15312	A <sup>2</sup> s
V т	On-state voltage	On-state current = 300A	25	1.65	V
V T(TO)	Threshold voltage		125	0.9	V
r T	On-state slope resistance		125	2.0	mΩ
SWITCH	·				
di/dt	Critical rate of rise of on-state current		125	150	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 67\%V_{DRM}$	125	1000	V/µs
GATE					· ·
I <sub>qt</sub>	Gate trigger current	V <sub>D</sub> =6V	25	150	mA
V <sub>gt</sub>	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	250	mA
l L	Latching current	V <sub>D</sub> =6V	25	600	mA
MOUNTI	NG				
R th(j-c)	Thermal impedance, sin 180°	Junction to case, per arm per module		0.30 0.15	°C/W
R th(j-c)	Thermal impedance, rec120°	Junction to case, per arm per module		0.34 0.17	°C/W
R th(c-h)	Thermal impedance	Case to heatsink, per arm per module		0.2 0.1	°C/W
Тj	Max. junction temperature			125	°C
T stg	Storage temperature			-40 125	°C
V <sub>ISOL</sub>	Insulation test voltage,RMS	F=50Hz, 1min		2.5	KV
M1	Mounting torque			5 ± 15%	Nm
M2	Terminal connection torque			3 ± 15%	Nm
W	Weight (Approx.)			105	gm

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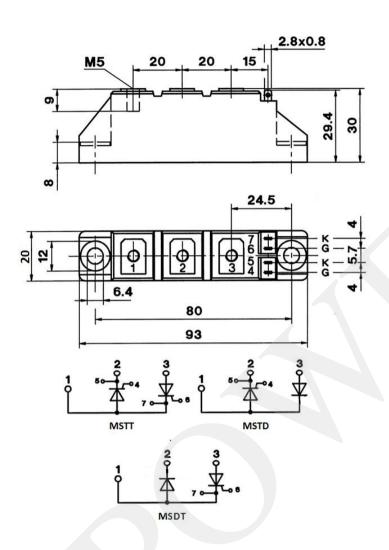








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### **MS TT/TD95**



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