**MS TZ310** 





#### **Key Parameters**

Vdrm / Vrrm	= 2600V
It(AV)	= 310A
ITSM	= 9000A
V <sub>T(TO)</sub>	= 1.00V
rΤ	= 0.86mΩ

#### **Features**

- Full blocking capability over wide temperature range
- . Heat transfer through aluminium oxide ceramic isolated metal baseplate
- Pressure contacts technology for high reliability

- ApplicationsPower Supplies
- DC motor control .
- Controlled Rectifiers

### **Ordering Information**

MS	TZ	310	ĸ	X X
Fixed code	TZ - Thyristor Module	Current Code	Technology K = Pressure Contact Technology	Voltage Code Code X 100 = V <sub>DRM</sub> /V <sub>RRM</sub>
Order Code	MS TZ310K26	: 2600V Vdrm,Vrf	RM, Thyristor Module	
			Prepared by : ABA	Date of Publication : 25.03.201
			Approved by : RBS	Revision : 1

**MS TZ310** 

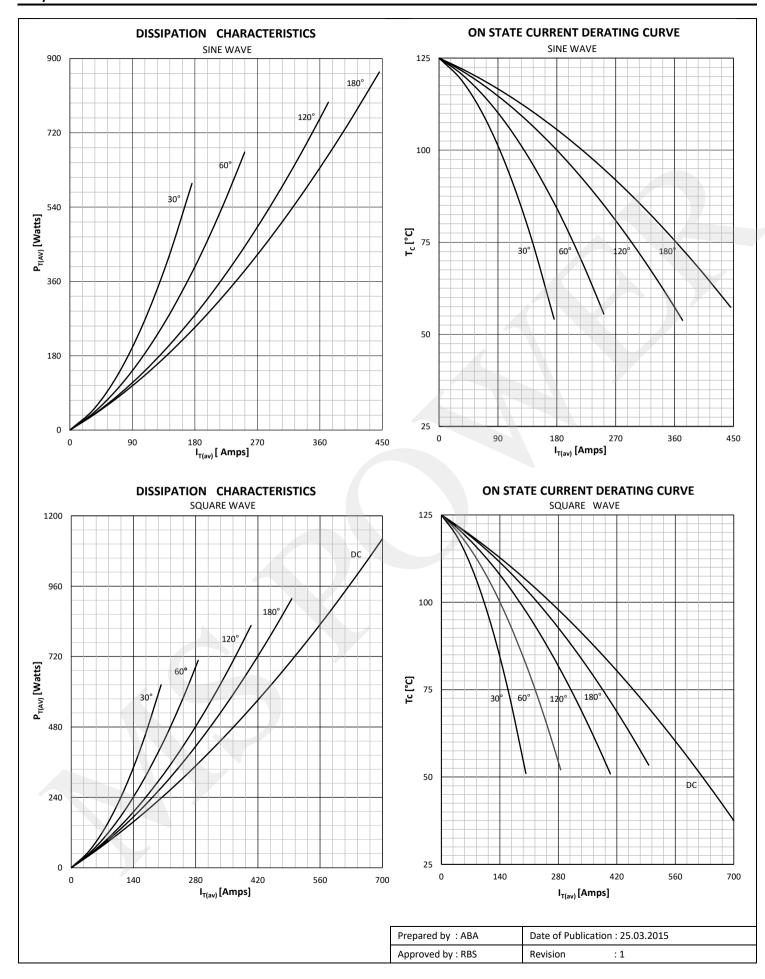


Symbol	Characteristic	Conditions	Tj [°C]	Value	Unit
BLOCKI	NG				
V RRM	Repetitive peak reverse voltage		125	2000 - 2600	V
V RSM	Non-repetitive peak reverse voltage		125	2100 - 2700	V
V drm	Repetitive peak off-state voltage		125	2000 - 2600	V
I RRM	Repetitive peak reverse current	V= V RRM	125	80	mA
I DRM	Repetitive peak off-state current	V= V drm	125	80	mA
CONDU	CTING				
I T (AV)	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =85°C 180° sin ,50 Hz, T <sub>c</sub> =58°C		310 445	A
I RMS	RMS on-state current			700	А
I TSM Surge on-state current		Sine wave, 10 ms	25	9000	А
	Without reverse voltage	125	8000	А	
		Since where 10 mg	25	405 x 10 <sup>3</sup>	A²s
l² t	t l <sup>2</sup> t Sine wave, 10 ms Without reverse voltage		125	320x 10 <sup>3</sup>	A²s
Vт	On-state voltage	On-state current = 1300A	125	2.22	V
V τ(το)	Threshold voltage		125	1.00	V
rт	On-state slope resistance		125	0.86	mΩ
SWITCH di/dt	Critical rate of rise of on-state current	i <sub>GM</sub> =1.25A, d <sub>iG</sub> /dt=1.25A/µs, f=50Hz	125	120	A/µs
dv/dt	Critical rate of rise of off-state voltage	$V_{DR} = 67\% V_{DRM}$	125	120	V/µs
	Childa hate of hise of on-state voltage	V DR - O7 76 V DRM	125	1000	ν/μ۵
GATE	Gate trigger current	V <sub>D</sub> =6V	25	200	mA
l <sub>gt</sub>		V <sub>D</sub> =6V	25	3.0	V
V <sub>gt</sub>	Gate trigger voltage Holding current	$V_D=6V$ , gate open circuit	25	300	
		V <sub>D</sub> =6V	25	1000	mA
ΙL	Latching current	vD=0v	25	1000	mA
MOUNT		Le trata con a constato		0.070	0000
R th(j-c)	Thermal impedance, sin 180°	Junction to case, per module Junction to case, per module		0.078	°C/W °C/W
R th(j-c) R th(c-h)	Thermal impedance, rec120° Thermal impedance	Case to heatsink, per module		0.089	°C/W
T j	Max. junction temperature			125	°C
T stg	Storage temperature			-40 130	0°
V <sub>ISOL</sub>	Insulation test voltage,RMS	F=50Hz, 1min		3.0	KV
M1	Mounting torque			7 ± 15%	Nm
M2	Terminal connection torque			12 ± 15%	Nm
	romma connection torque			12 ± 10/0	INIT

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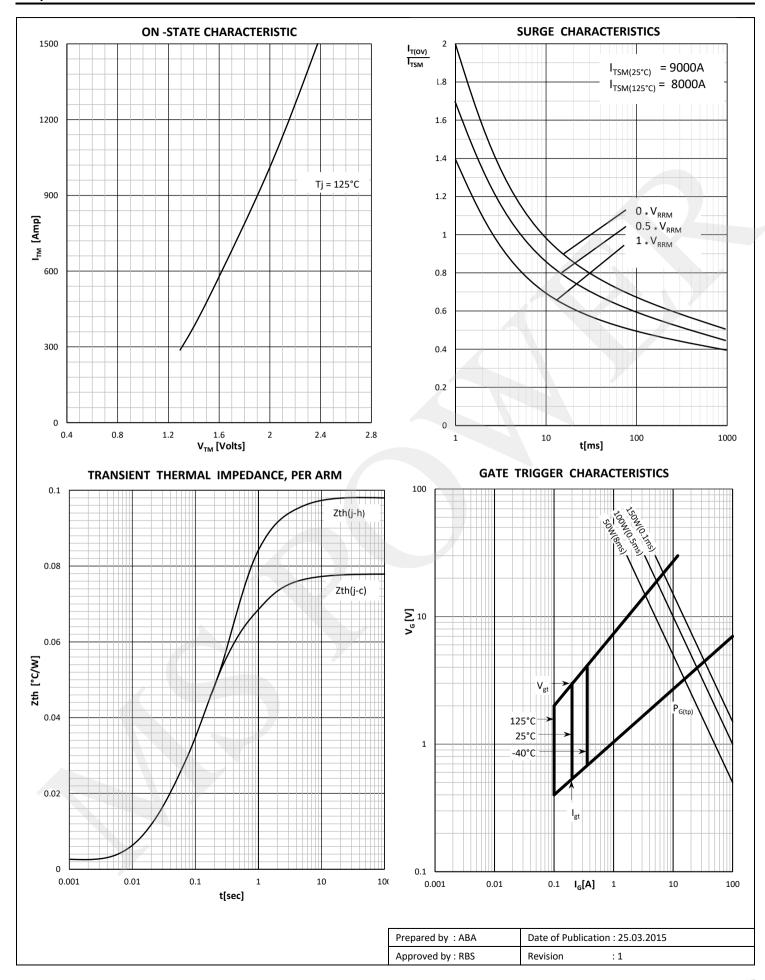
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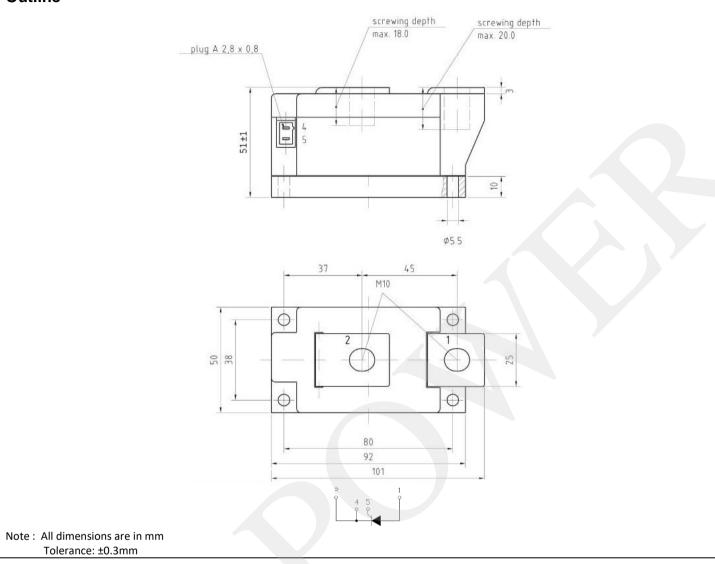


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