

**Key Parameters**

$V_{DRM} / V_{RRM}$	= 1800V
$I_{T(AV)}$	= 560A
$I_{TSM}$	= 17000A
$V_{T(TO)}$	= 0.80V
$r_T$	= 0.38m $\Omega$

**Features**

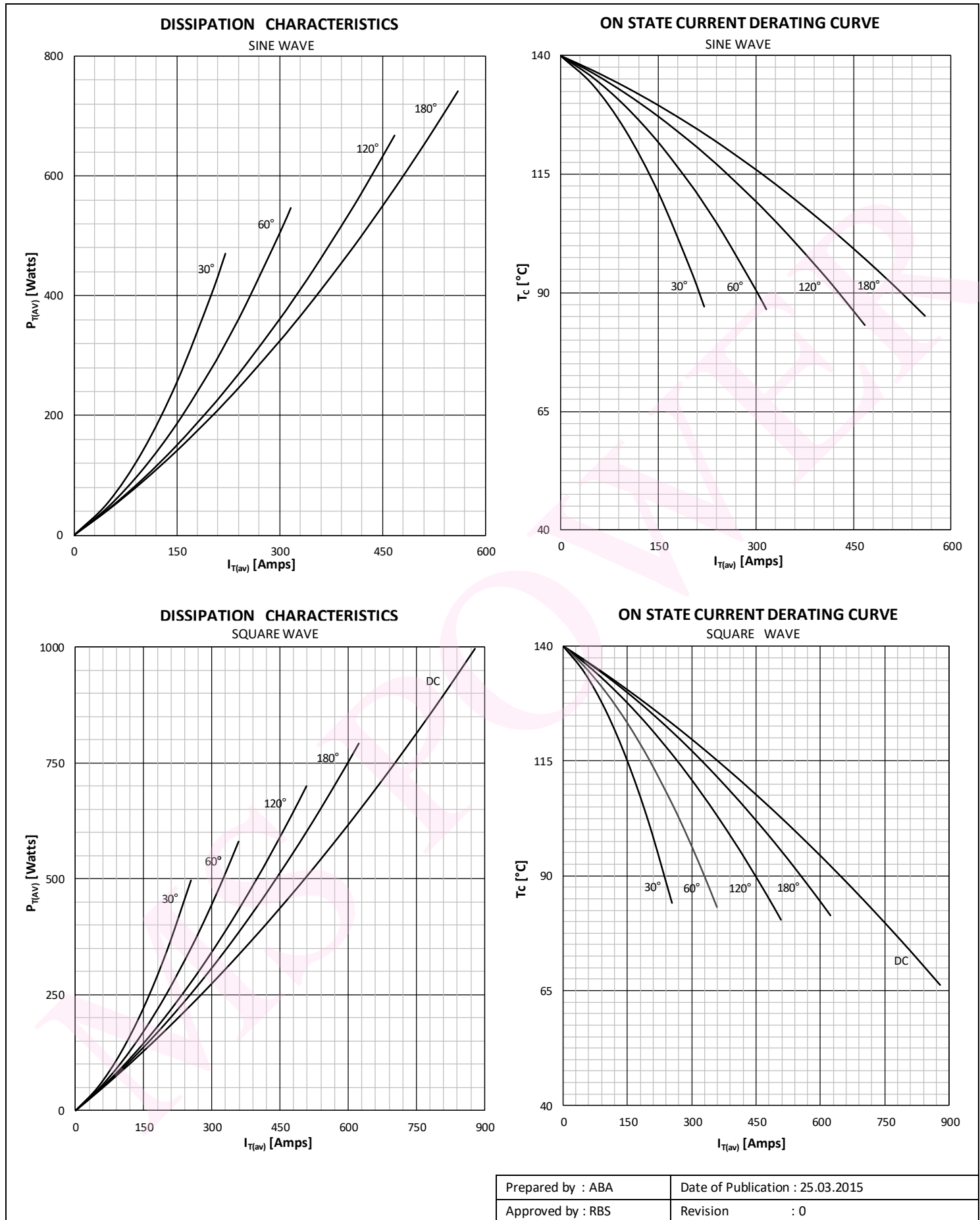
- Full blocking capability over wide temperature range
- Heat transfer through aluminium nitride ceramic isolated metal baseplate
- Pressure contacts technology for high reliability

**Ordering Information**

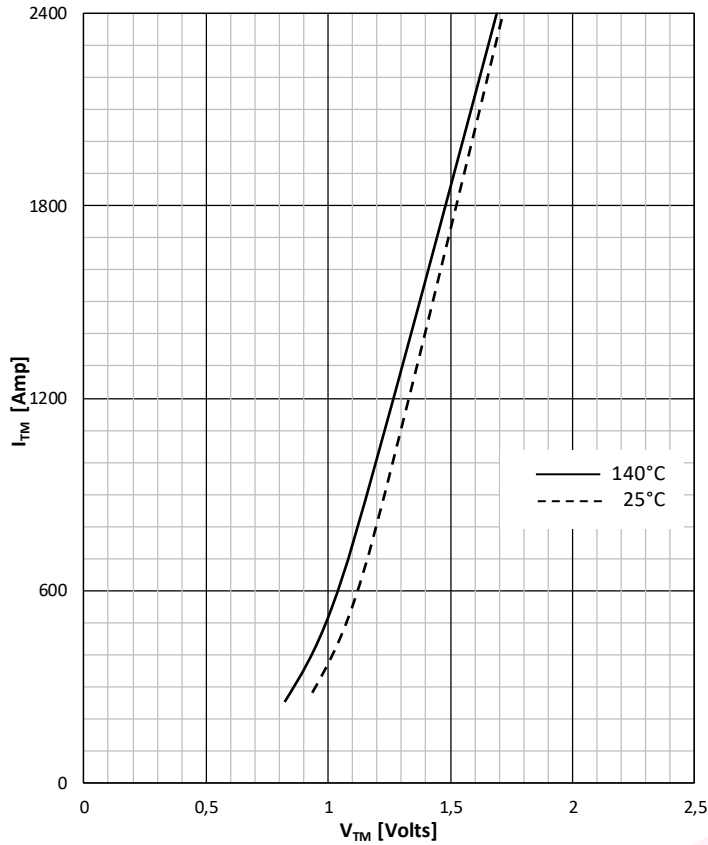
MS	TZ	560	K	XX
Fixed code	TZ - Thyristor Module	Current Code	Technology K = Pressure Contact Technology	Voltage Code Code X 100 = $V_{DRM}/V_{RRM}$
Order Code MS TZ560K18 : 1800V $V_{DRM}, V_{RRM}$ , Thyristor Module				

Prepared by : ABA	Date of Publication : 25.03.2015
Approved by : RBS	Revision : 0

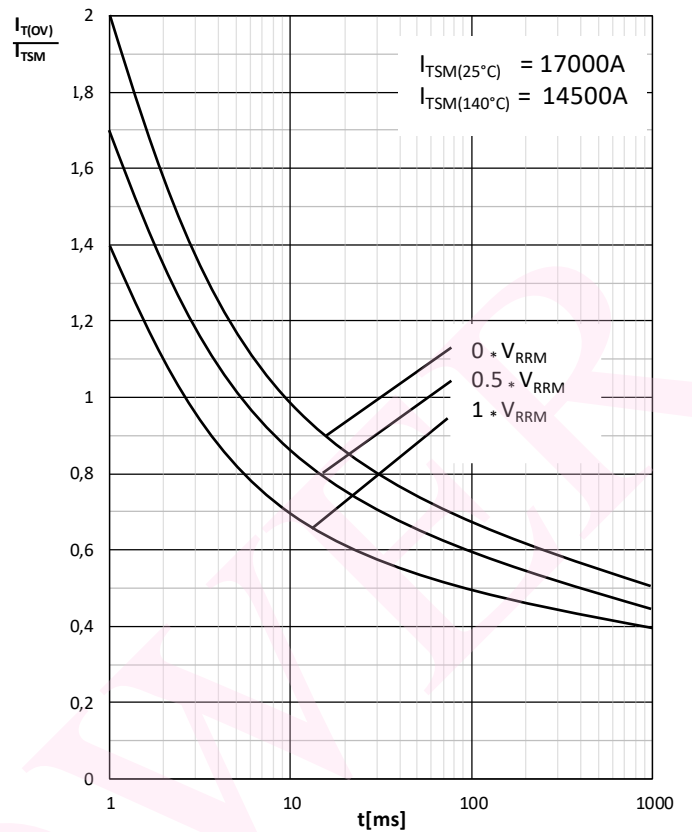
Symbol	Characteristic	Conditions	T <sub>j</sub> [°C]	Value	Unit
<b>BLOCKING</b>					
V <sub>RRM</sub>	Repetitive peak reverse voltage		140	200 - 1800	V
V <sub>RSM</sub>	Non-repetitive peak reverse voltage		140	300 - 1900	V
V <sub>DRM</sub>	Repetitive peak off-state voltage		140	200 - 1800	V
I <sub>RRM</sub>	Repetitive peak reverse current	V = V <sub>RRM</sub>	140	50	mA
I <sub>DRM</sub>	Repetitive peak off-state current	V = V <sub>DRM</sub>	140	50	mA
<b>CONDUCTING</b>					
I <sub>T(AV)</sub>	Mean on state current	180° sin ,50 Hz, T <sub>c</sub> =85°C		560	A
I <sub>RMS</sub>	RMS on-state current			879	A
I <sub>TSM</sub>	Surge on-state current	Sine wave, 10 ms Without reverse voltage	25	17000	A
			140	14500	A
I <sup>2</sup> t	I <sup>2</sup> t	Sine wave, 10 ms Without reverse voltage	25	1445 x 10 <sup>3</sup>	A <sup>2</sup> s
			140	1051 x 10 <sup>3</sup>	A <sup>2</sup> s
V <sub>T</sub>	On-state voltage	On-state current = 1000A	25	1.27	V
V <sub>T(TO)</sub>	Threshold voltage		140	0.80	V
r <sub>T</sub>	On-state slope resistance		140	0.38	mΩ
<b>SWITCHING</b>					
di/dt	Critical rate of rise of on-state current	i <sub>GM</sub> =1A, d <sub>G</sub> /dt=1A/μs, f=50Hz	140	100	A/μs
dv/dt	Critical rate of rise of off-state voltage	V <sub>DR</sub> = 67%V <sub>DRM</sub>	140	1000	V/μs
<b>GATE</b>					
I <sub>gt</sub>	Gate trigger current	V <sub>D</sub> =6V	25	200	mA
V <sub>gt</sub>	Gate trigger voltage	V <sub>D</sub> =6V	25	3.0	V
I <sub>H</sub>	Holding current	V <sub>D</sub> =6V, gate open circuit	25	300	mA
I <sub>L</sub>	Latching current	V <sub>D</sub> =6V	25	1000	mA
<b>MOUNTING</b>					
R <sub>th(j-c)</sub>	Thermal impedance, sin 180°	Junction to case, per module		0.072	°C/W
R <sub>th(j-c)</sub>	Thermal impedance, rec120°	Junction to case, per module		0.083	°C/W
R <sub>th(c-h)</sub>	Thermal impedance	Case to heatsink, per module		0.024	°C/W
T <sub>j</sub>	Max. junction temperature			140	°C
T <sub>stg</sub>	Storage temperature			-40 .... 140	°C
V <sub>ISOL</sub>	Insulation test voltage, RMS	F=50Hz, 1min		3.0	KV
M1	Mounting torque			7 ± 15%	Nm
M2	Terminal connection torque			12 ± 15%	Nm
W	Weight (Approx.)			650	gm
			Prepared by : ABA	Date of Publication : 25.03.2015	
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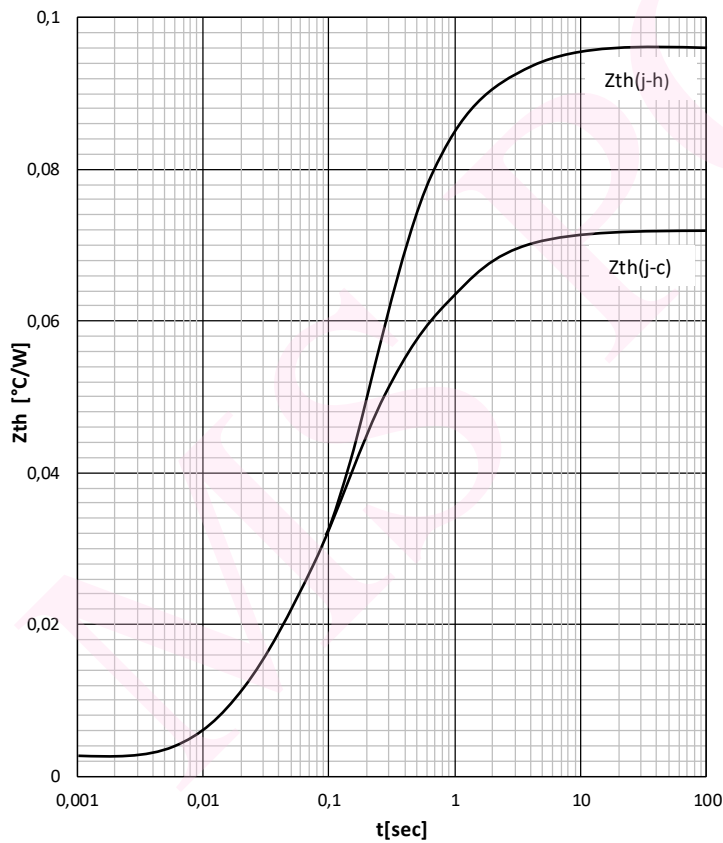
ON -STATE CHARACTERISTIC



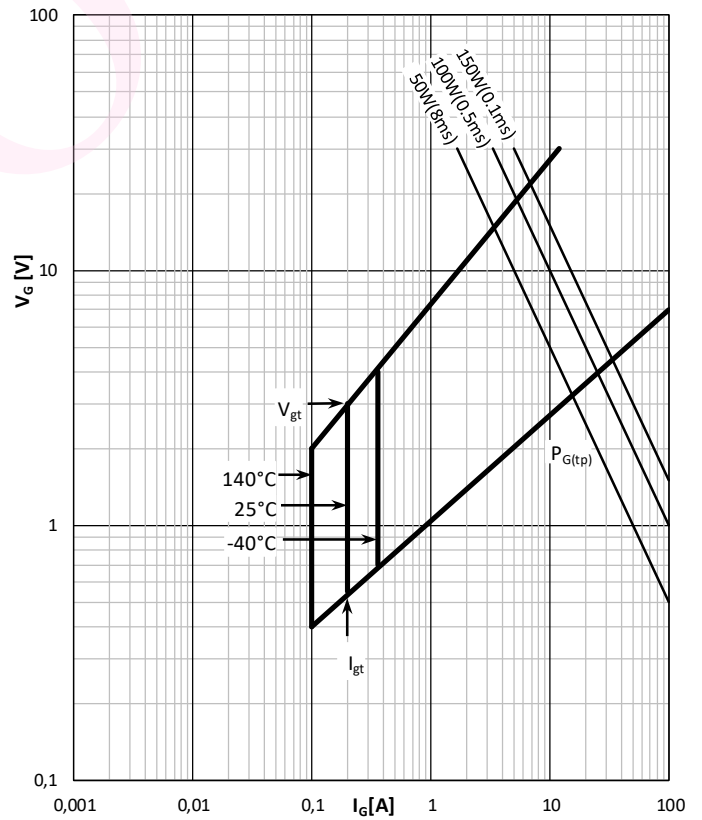
SURGE CHARACTERISTICS



TRANSIENT THERMAL IMPEDANCE, PER ARM



GATE TRIGGER CHARACTERISTICS



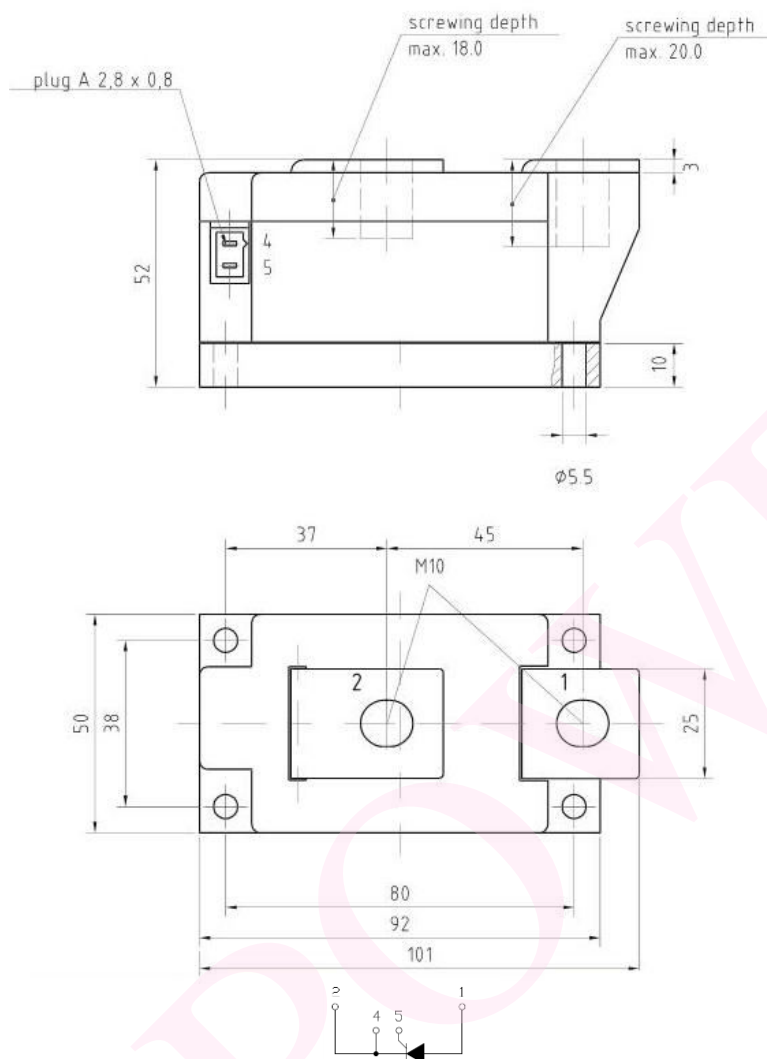
Prepared by : ABA

Date of Publication : 25.03.2015

Approved by : RBS

Revision : 0

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